Ex. No.:1

Date: 19/9/2019

Verification of NETWORK THEOREMS

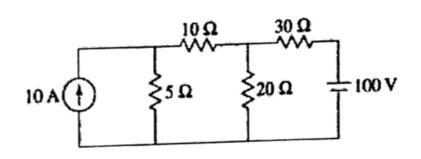
(Thevenin's Theorem)

Aim: To veruify the Thevenin's Theorem for the given network by theoretical values and simulation methods.

Apparatus/Tool required:

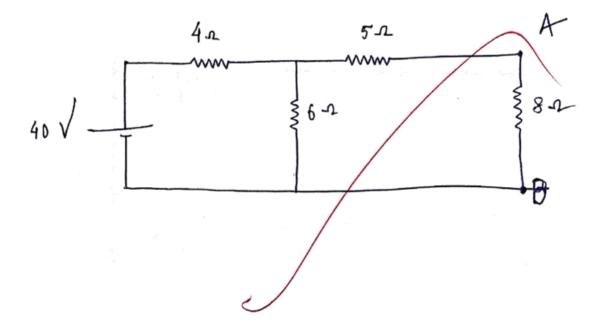
ORCAD / Capture CIS --> Analog Library - R, Source Library - Vdc, Idc & Ground (GND) - 0 (zero) Simulation Settings: Analysis Type - Bias Point

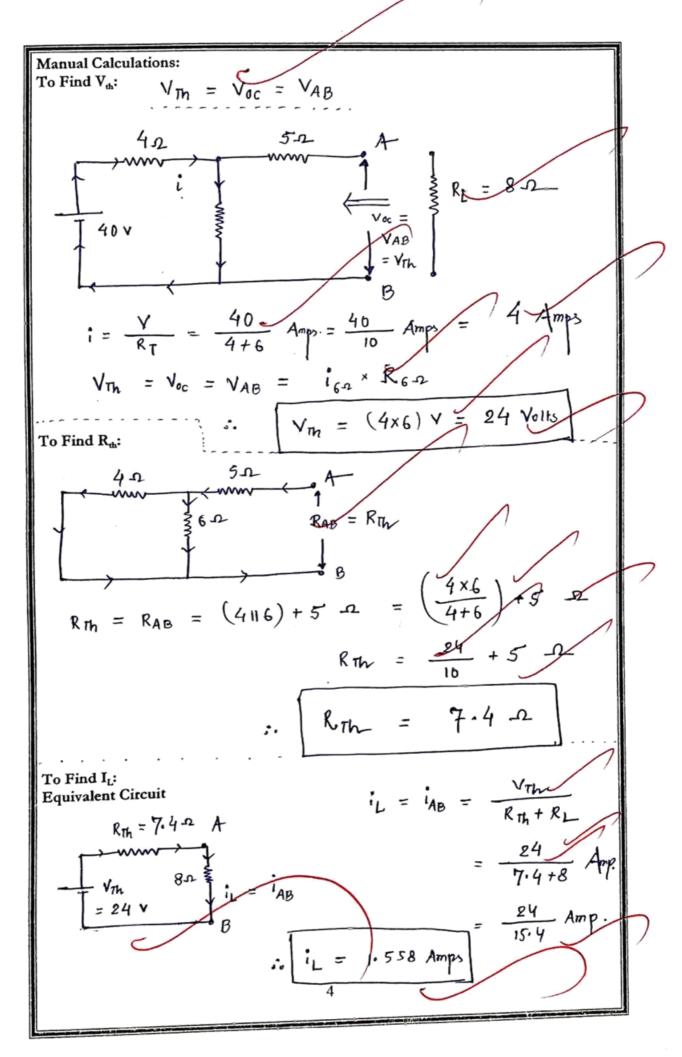
Circuit Diagram

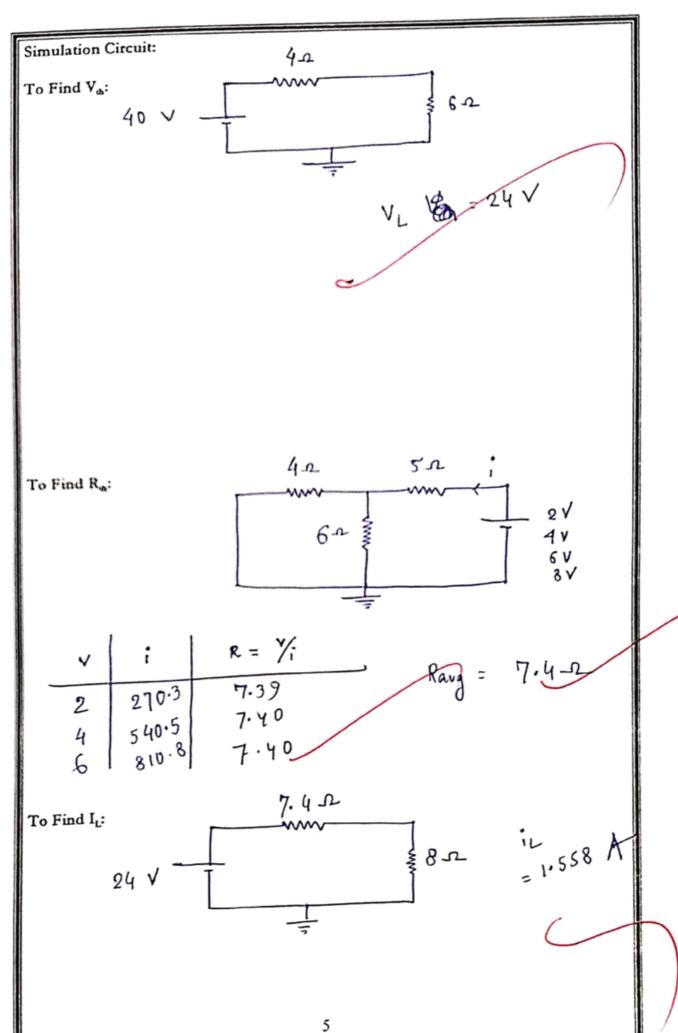


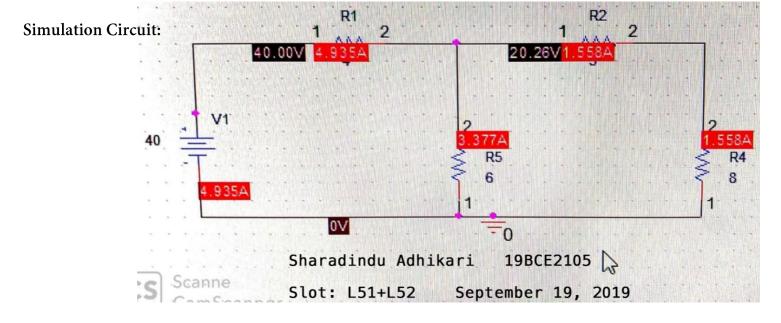
Statement: Thevenin's Theorem

In a linear bilateral network with output terminals of and B, can be replaced by a single voltage source in series with equivalent resistance.

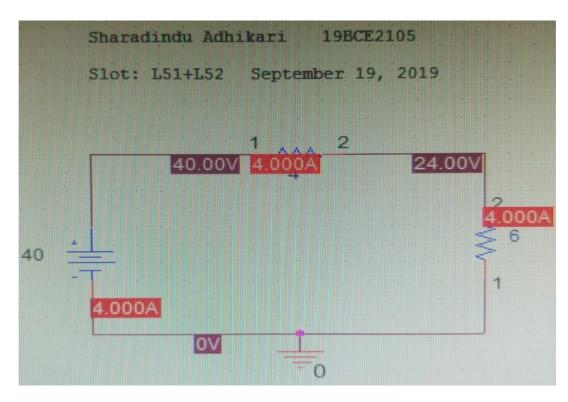




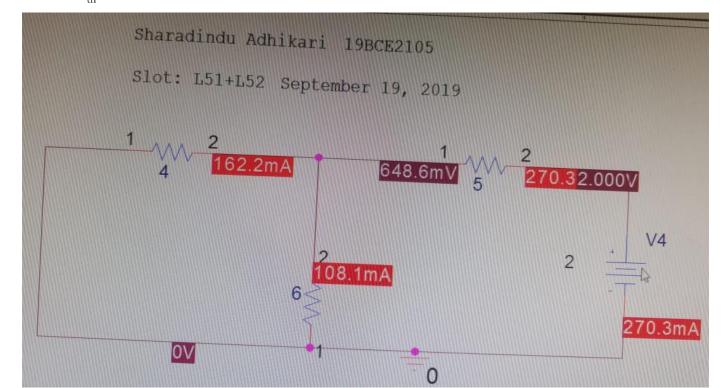


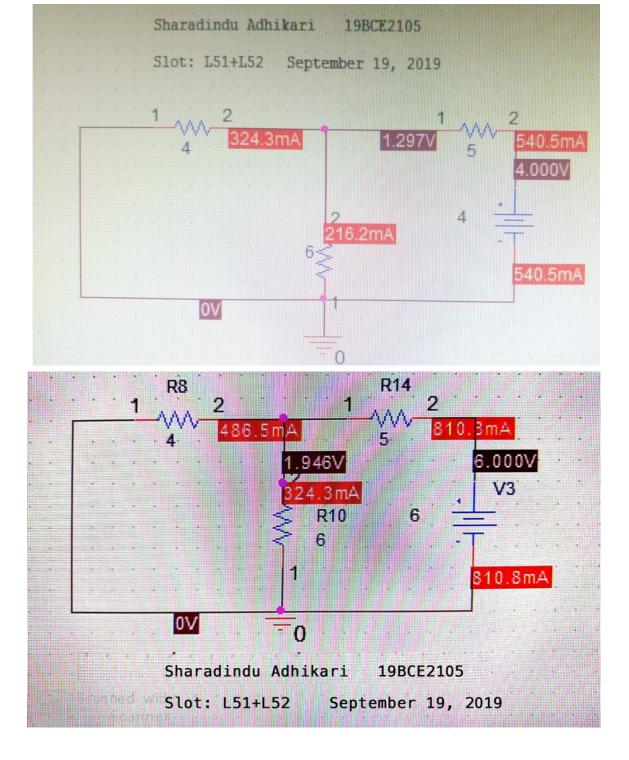


• To find V_{th} :

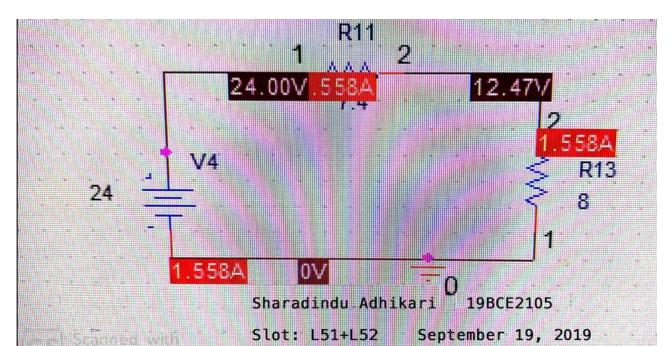


• To find R_{th}:





• To find I_L:



Procedure: Result: The Thevenin's Theorem have been verified for the given network by theoretical & simulation values & the following results are tabulated: Thevenin's Theorem

Manual Calculations Manual Calculations Simulated Result 247 V_{L} RTh 1.578 1.558 V Inference: Reg. No: 19BCE 2105 Name: SHARADIN DU Date: 19 / 9 / 2019 ADHIKARI