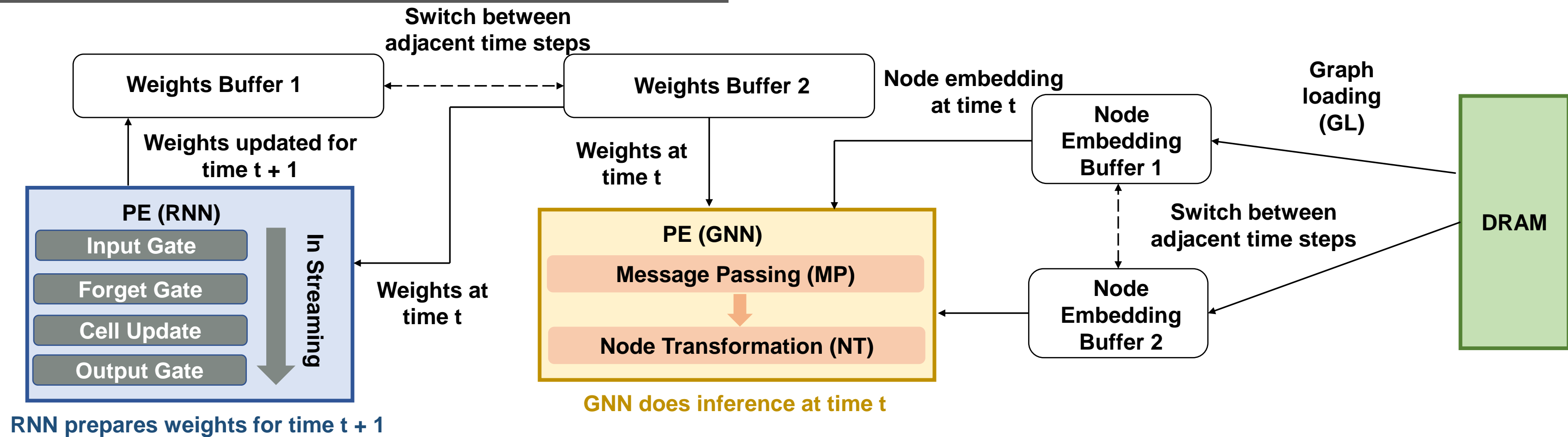
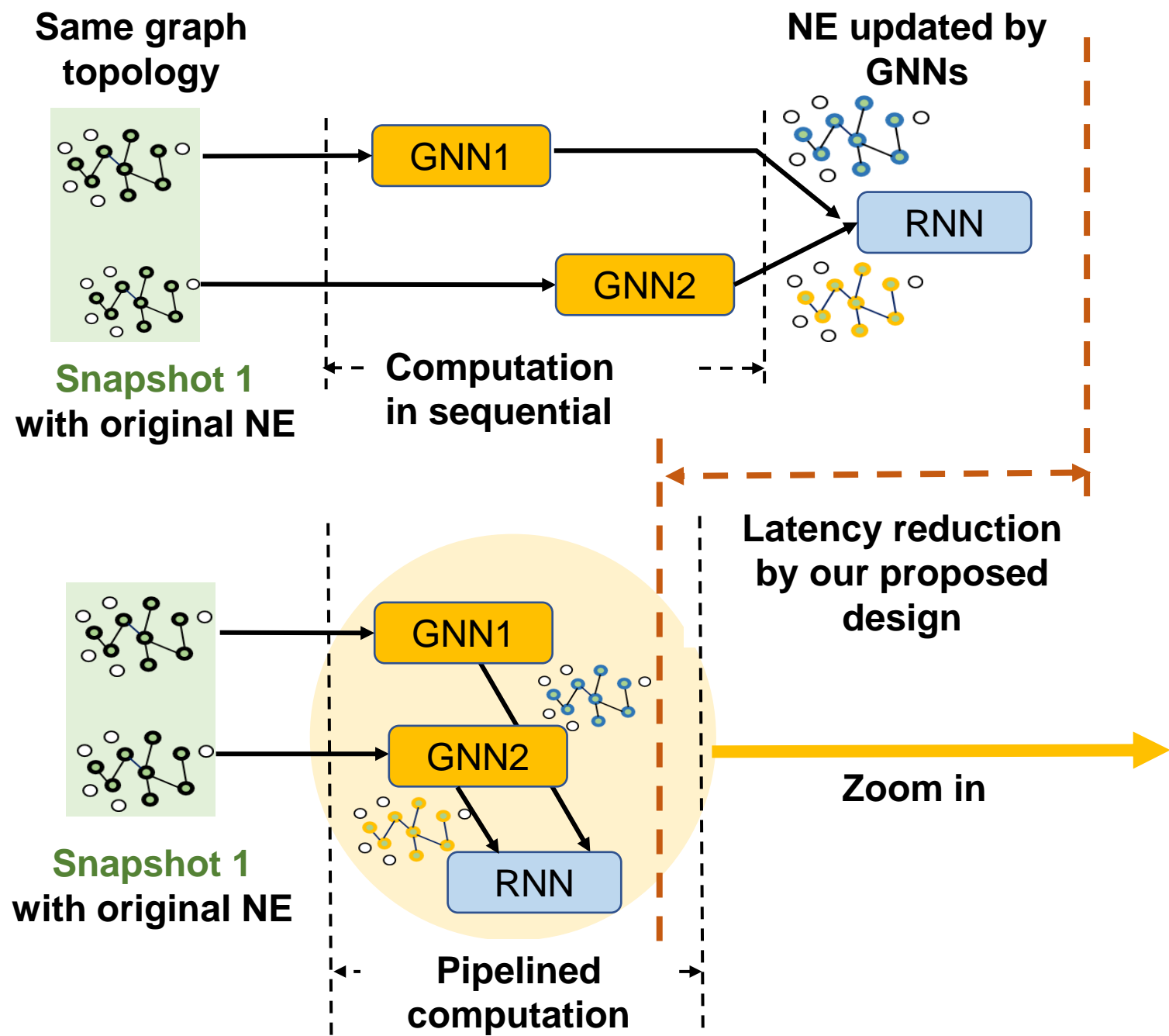


Optimized FPGA Implementation DGNN-Booster V1



Optimized FPGA Implementation DGNN-Booster V2



TP: Temporal Processing using RNN
NE: Node Embedding
PE: Processing Element

