Zainulla Sharief

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B.E. Electronics And Communication

B.E. Electronics And Communication Engineering student (2025 graduate) with hands-on experience in VLSI design, embedded systems, and multi-body simulations. Passionate about semiconductor technologies, brain–computer interfaces, and developing innovative solutions bridging hardware and software.

Career Objective

To leverage my skills in VLSI design, embedded systems, and multi-body simulation to contribute to cutting-edge semiconductor technologies and innovative product development.

Education

HKBK College Of Engineering

Dec 2021 - Sept 2025

BE in Electronics and communication

- CGPA: 6.5
- Coursework: Computer Architecture, Digital Electronics, Analog Electronics, VLSI Desing, Wireless Communication, IOT, Control Systems, Embedded Systems

Experience

VLSI Design Engineer Intern

Bangalore, KA

Rooman Technologies

Sept 2024 - May 2025

- Designed and implemented digital circuits using Verilog/VHDL, ensuring functionality and timing accuracy.
- o Performed simulation and verification of combinational and sequential circuits using tools like ModelSim
- Assisted in the layout design and floorplanning of VLSI modules, optimizing area and power consumption.
- Documented design processes and prepared technical reports on simulation results and optimizations.
- Designed a low-power ALU using clock gating and operand isolation techniques, reducing dynamic power consumption by 15percent.

Systems Engineer Intern

Bangalore, KA

JSS Electric Vehicles Mobility Center of Excellence and Innovation

June 2024 - July 2024

- Developed a multi-body simulation model of a gyroscopic system using Modelica to study rotational dynamics and precession behavior.
- Configured mechanical components including cylindrical bodies, spherical joints, revolute joints, and fixed translations to accurately represent gyroscopic motion..

Projects

Early-Stage Identification of Neurological Disorder using Brain Computer Interface

- Engineered an innovative Brain-Computer Interface (BCI) system for real-time epilepsy monitoring by integrating hardware for biosignal acquisition and a machine learning module for automated diagnosis.
- o Tools Used: Python, Firebase Realtime Database, PythonAnywhere, ML

Skills

Verilog, MATLAB, C, Embedded C, Python, Shell Scripting, Linux, DSP, VHDL, Embedded systems, Github, Git, IOT, Digital Electronics, Analog Electronics, Thinkercad, Multisim, Xilinx

Certificates

• VLSI Design Engineer- Rooman Technologies	2025
\circ VLSI Design Engineer - $ESSCI$	2025
o On The Job Training - Rooman Technologies	2025
\circ The Realm Of IOT Application And Beyond - $Roboram$	2023
o Life Skills (Jeevan Kaushal) - Wadhwani Foundation	2025
• Systems Engineer - JSS Electric Vehicle Mobility COE	2024