# Offinising dieve of ERATOSTHENES 22 23 24 25 Gner 0 1 2 3 4 vector<br/>kboolzsieve(int n) ? vector Lbool > siere(n+1, toue) 1=2=(4),6,8,10,12,14,16,18,20, Sieve [o] = Sieve [1] = falk; 3->6(9),12,15,16,21,24 for (int i= 2; it=n; i+f) { ill sievetij = zfrue) of
int j= i\*xi; (1x2) 5-, 10, 18, 20(28) 7-) 14, 21, 28,35 2x2=4 While (j<=n)? 2 x3 = 6 Sieve(j]=falæ:

j j (j+=i'; 224=8 humme ix2 se 3 X2=10 Iluru nhi karna has shere karo ixi se kuki jo petur sière; ix2 hoga vo pehle se mærk hoga idgar ski baat karu toh stor pehla hoga jo urmarked hoge uske pehle ka toh unmarked ho Chuka har toh Offinization (ixi) huga in Outer loop Optimization: cioner doop in jeixi; while (j<=N) {
Sieve [j]=false; 0000 Samsung Quad Camera 3 J+=1; Shot with my Galaxy A21s

Je loop nhi chalega

Jo outer loop har vo shure karo 1=2 & jao 1 = JN 1 Jo array ban shi hai Ose 25 tak agan keoptim se Ver ka root niklate pe 5 milta hai extra worklan sha hai imer loop bol tha hai be i =5, j=ixi=25 se bury hoga iska meetly meetleb ægar i=5 se jo bhi badi value hai 7 9 11 ye sab jab ayega toh j. ki value 25 se bad kai no jayegi ishiye outer loop ko utna hi chalo jitne badi avray har chale soft he toh inner loop kaam nhi karega
so lels optimise outer loop asi= 2; [iz=JN]

(i xix=n) 

for (int i=2; ixix=n; i+t) ~nlog(logn) | Best way to make siene vector about > sievelind n) { Nector/bood> sieve (n+1, frue) sievelo]= sieveli] = false; Products for lind 1=2; 1x1 =n; 1+1) { Sun if (sieveli] = = true) & j = ixi; while(jk=n) { sievelj]=false; J += 1. amsung Quad Camera not with my Galaxy A21s

Iskebeech me jihne bhi prime hou! 98935 R=198935 L=13956 Ef Ristor large F=> 109

109 prine JR Tab daf 109 tak ki array allocate nhi kar paooge. main()

fun()

fun() initialize unitialize data jabreturn karke mains venetele, wapay aa jate ho je stack se remove ho jafa heu: memory free ho j'ati has. fun() iska bhimaxsize hota hæi.

Means kissi bhi array funcime

iNa[1000], aap array declare kan she ho

John will toh uska max size > 106 hosuktac Lint, double, chan, array, max-size=106

h bool array = 107 86 freth Crlobal array = int, clouble, char bool - 108 depend compuler 0000 Jun

operating System ki ek limit Iga ke sheta han Agar kissi ne bola 109 tak ke beech seve bna do toh that's not possible. Agan deft or Right ki as stange di'hui hai foh hum (R-L) niklate hai Kequirement in B. 1<=(L,R) <=109 (R-L) <=106 But toh ije lo tak aayega. duppose L= 110, R=130 (R-11) = 20 -3 max size array 130 lio isko bill kardugion
130 o,1º jha l hoja
10 vo prime
10 vo prime
no hoga U (renerate all prime responsible to mark segmented siens wing JR Initially all I. I fek away brake Informalsiere, me a ke multiple, 3 ke multiple onto the Mape vo wale mikalneshe their to marking me help karege? Normal Siève SIN=JR de duga
Normal se nikleger N=J130
Siève me N=1 These prime ishe under jithe bhi prime hoge segmented sieve 1) Bore Poume = {2,2,5,7,113 t find first incless to start marking index =0 -> Resemble 110 1ndex 20 -> 11 130 I first multiple - 1= (10/2) x2 Samsung Quad Camera
Shot with my Galaxy A21s

Prime 3 - s first multiple => (10/3) +3 =(36.6) \$3 = 108. if (firstmeel < 1) {
firstmeel += Prime.

=) int j= (first onul, prime \* prime)

Vector <bool>sieve = Sieve [

Difference blu 32-bit vs 64-bit 05 billions of transistor

CPU chip motherboard pe embedded hotihar

Gransistors hote har's individual digital

ccircuit. Register epu ke andar hote hai j'ha actual computation hoti hai It basically hot ds the address. It is a memory block. 32 bit processor me 32 bit jo registers hote hai vo 32-bit ka data shold kar sakte hai et baar me cove Both are diff architecture. DA 32-bit Ds has 32-bit registers of it can access 232 unique memory address. i.e. 4645 of physical memory.

address. i.e. 4645 of physical memory.

DA 64-bit Os has 64-bit registers 264 mA, i.e. 12,179,869,1846806 physical memory coldness.

Of can fetch data from 0; from CPU.

Of also, it can also fetch nth 10/1 > 232 unique address locate. 32-6it CPU pe [0]1 4 byte ACRB RAM ko access kan sakta hai Registers ko botha deta hai 264 great sol kithet we have doubled the size of register Now we can support greater amount of menoy, 000 Dun allocate more addresses. Samsung Quad Cameran hogya. Shot with my Galaxy A21s

2 rycle lag jayege Et 64 bit ke addition ko 32 bit agar karte hai toh cpu ke case me sycle ko Bambhalte hai Fitna kam cycle utna accha Derformance - All calculations feike place in 264, to register. When you are performing math in your code, operand are loaded from memory into registers. 0 Do, have large registers allow you to perform darger calculation at the same pouble amount work kan paa than har 1 cycle time Cratzaghi btala me Isec me secule har cycle. I byfes of data in 1 instruction cycle while 64-bit mounthat forocerror can execute 8 bytes in 1 instruction degree. In Isec there could be thousands of billion of inst-cycle depending upon a processor design. B) Resource Usage - (64 bit) > 32 bit agar isme cle entra fan install tar di toh ye support whi I tarega. (9) comp 64bit Epo run boths 32 & 64 > nears downward compatibility hav lekin 22-bit ke andar only 82bit ka dola ka Os chal sakta hai ( Better Craphics Performance - 6646it > 326it & bytes graphics Caller make graphics-intensive ables