

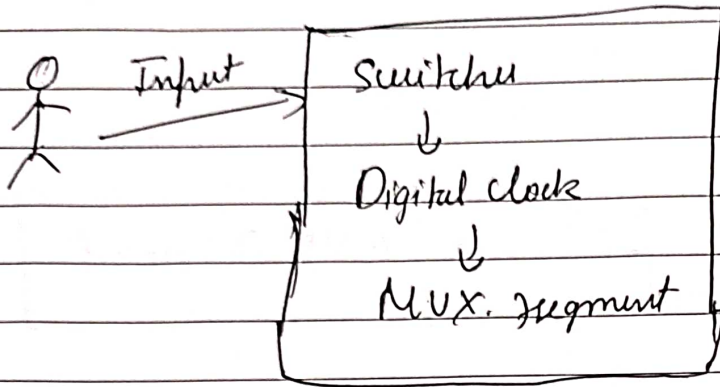
Requirements:

- 1) To display ~~display~~ digital clock in mm:ss format in the 7-segment MUX LED.
- 2) The Digital clock should include 2 modes, Set and Run.
- 3) In set mode, the time has to start from the time set by the user.
- 4) In run mode, the clock has to start running from the set ~~mode~~ timings.

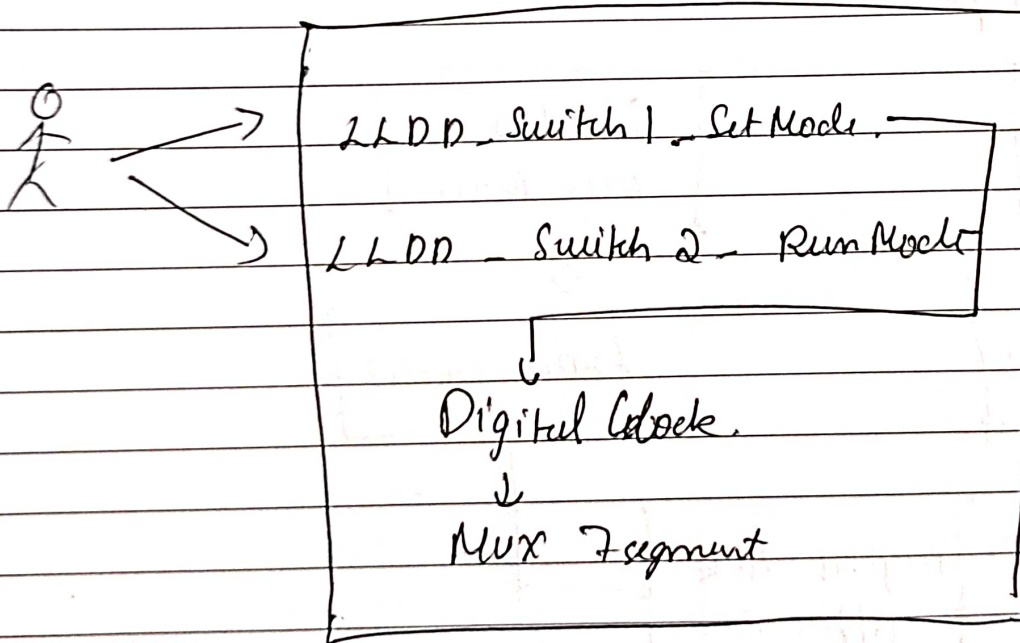
DIGITAL CLOCK.

UML Call diagram

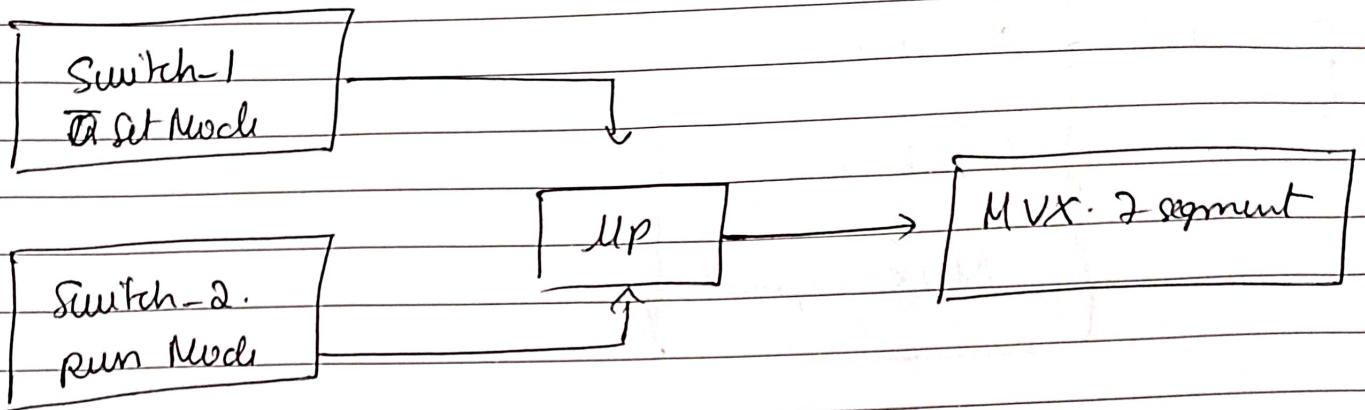
use case - 1 Run Mode.



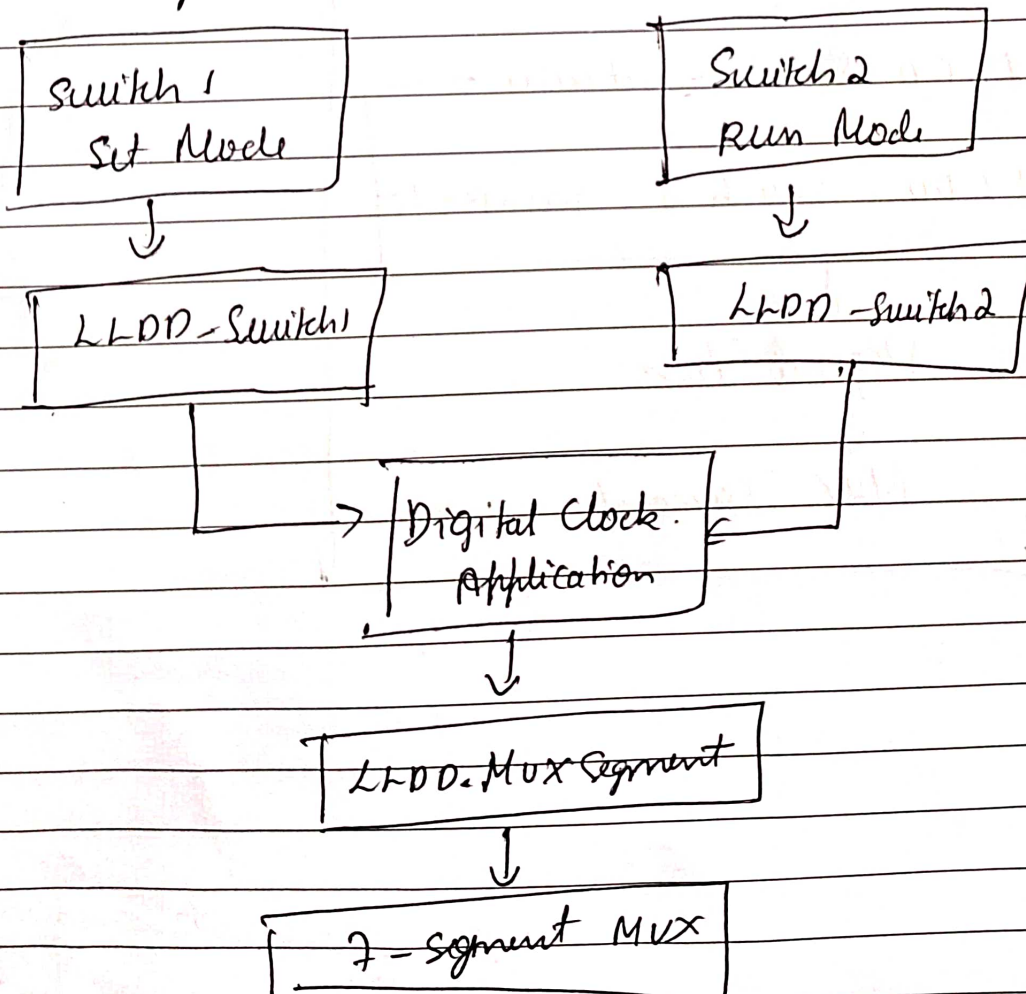
Use Case - 2 Set Mode.



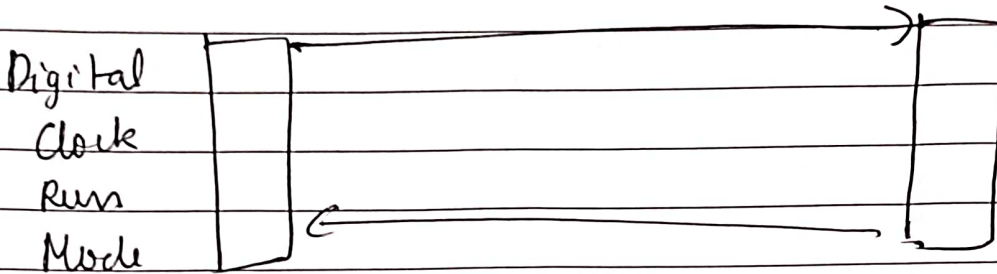
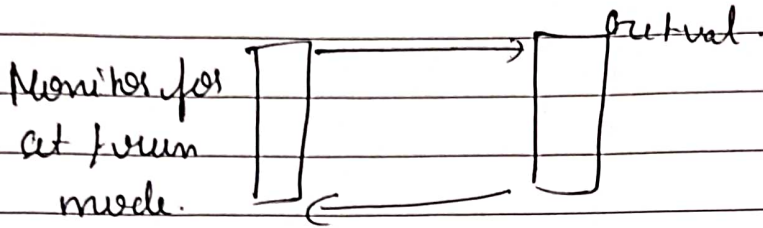
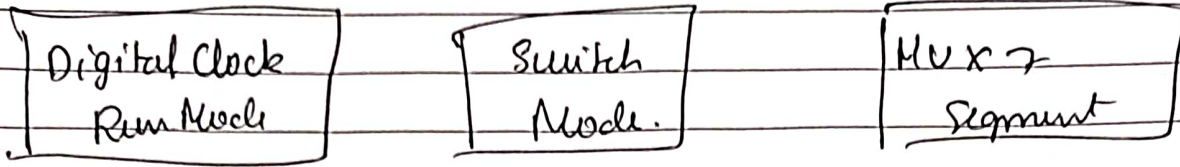
Block diagram Hardware.



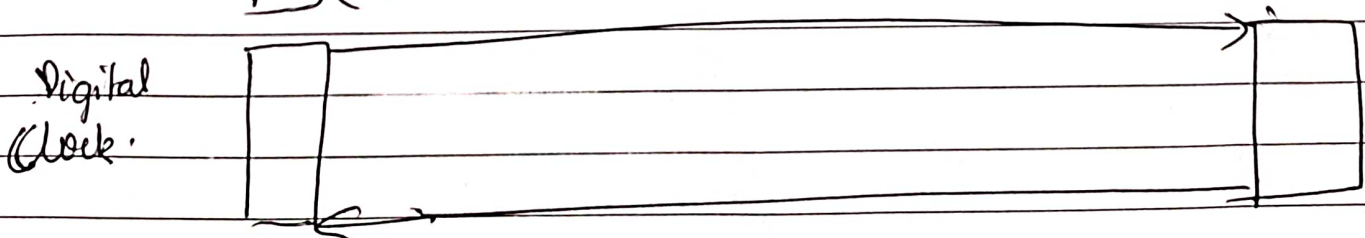
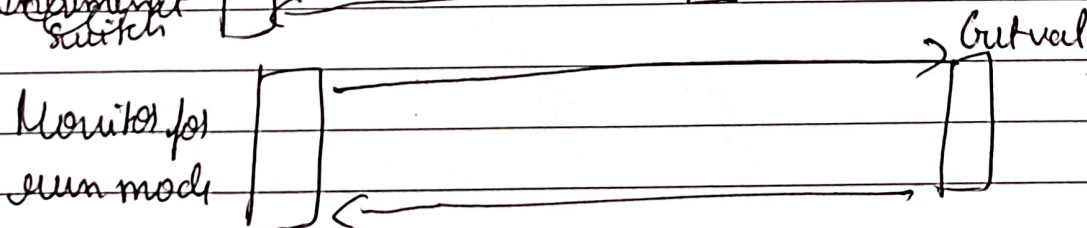
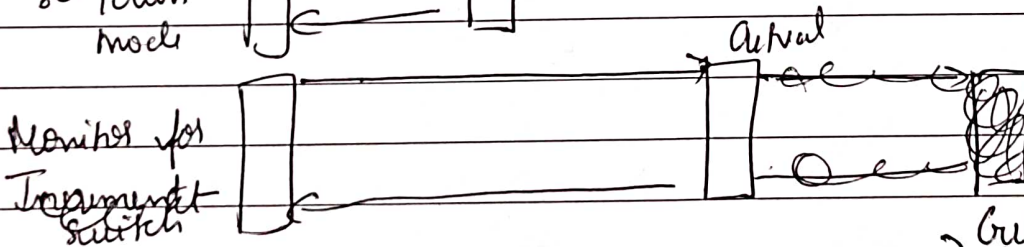
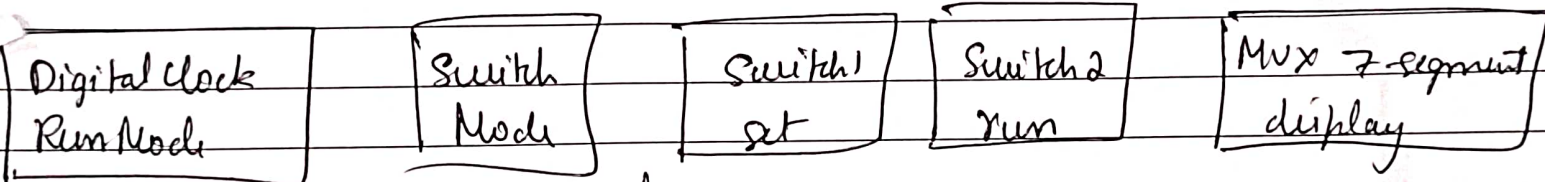
Block diagram Software.



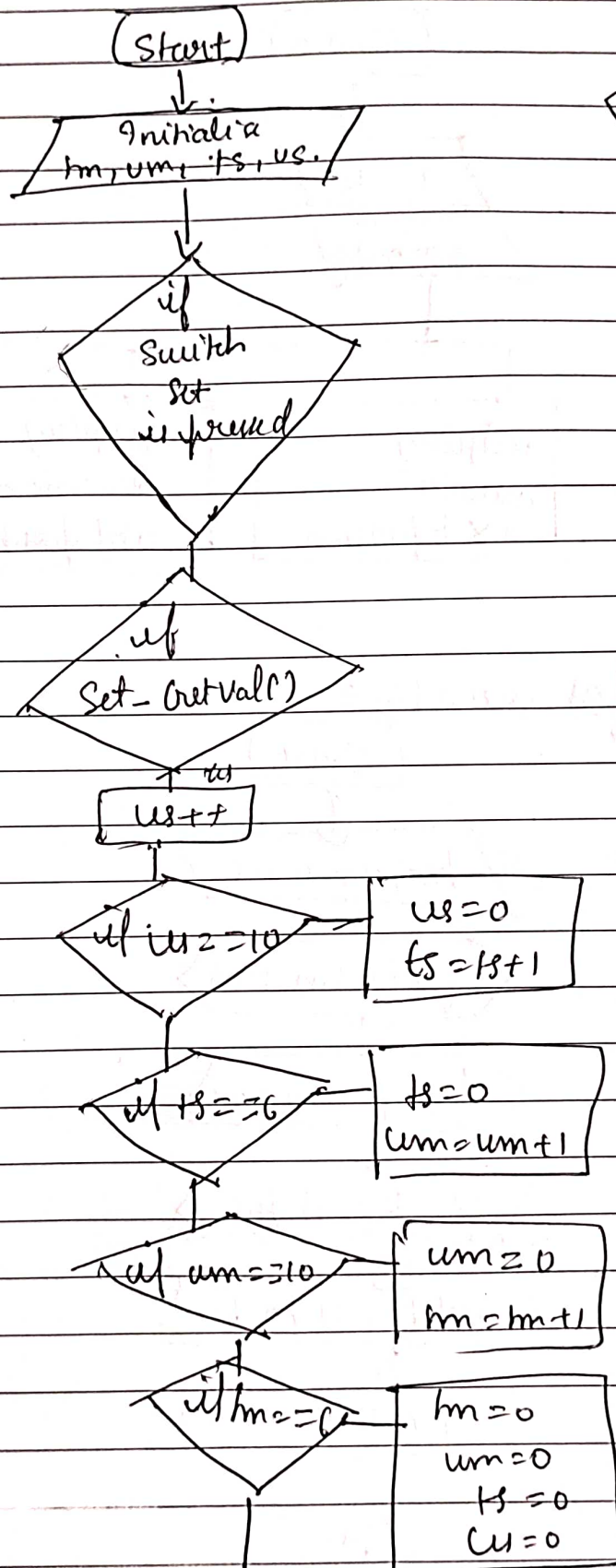
VC-1. run mode.



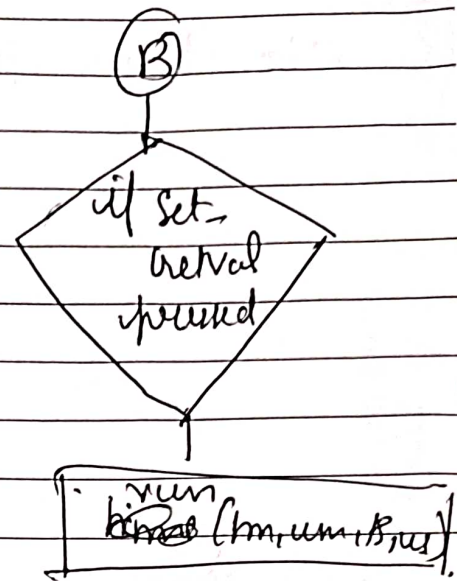
VC-1 Set Mode.



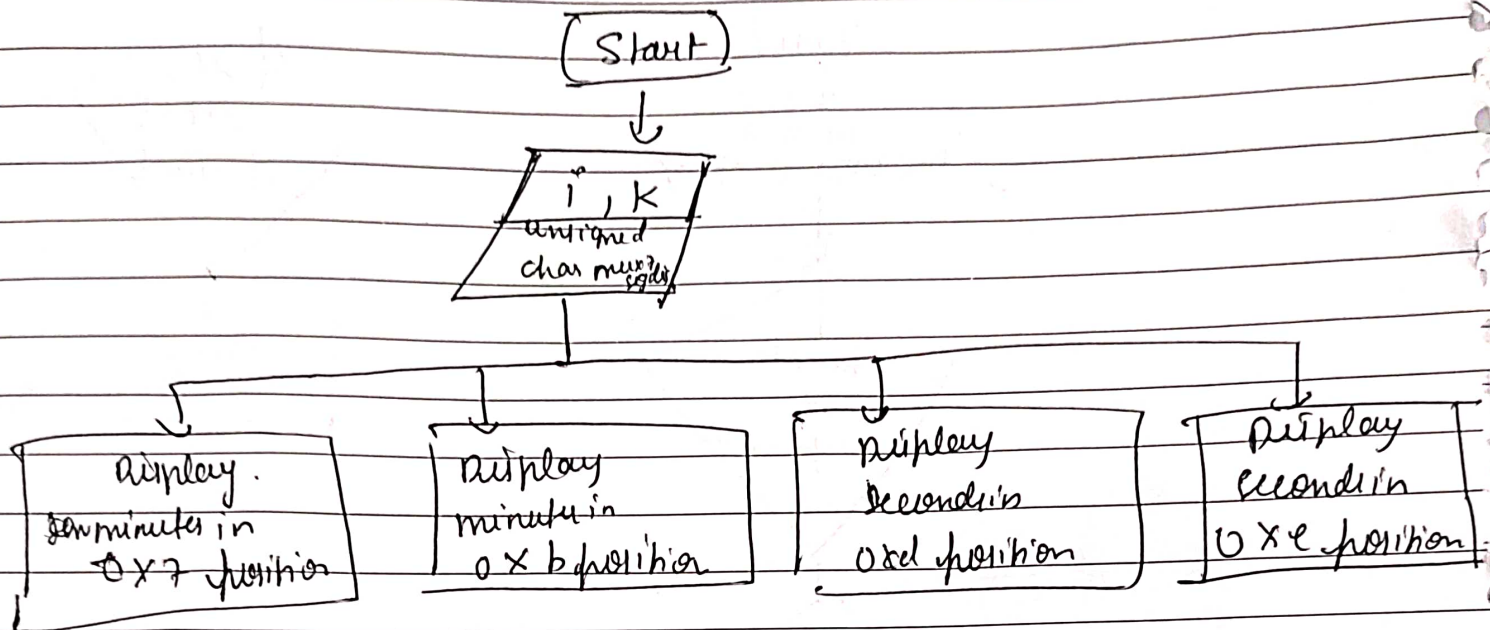
Flowchart for main function Run mode.



Call (A) time(hm, um, ts, us)



Flowchart for Set time.



Flowchart for run time.

