

Reg. No.:

Name :

VIT<sup>®</sup>

Vellore Institute of Technology

(Deemed to be University under section 3 of UGC Act, 1956)

## Continuous Assessment Test I – September 2023

Programme	: B.Tech (BCE/BPS/BAI/BRS)	Semester	: FS 2023-24
Course	: Microprocessors and Microcontrollers	Code	: BECE204L
		Class Nbr	: CH2023240101166 CH2023240101169 CH2023240101178 CH2023240100941 CH2023240100943 CH2023240100947 CH2023240100951 CH2023240100954 CH2023240100959 CH2023240100963
Faculty	: REVATHI S, SUBHASHINI N, MUTHULAKSHMI S, MANOJ KUMAR R, BALA MURUGAN M S, SOURABH PAUL, S SELVENDRAN, LAKSHMI PRIYA, AUGUSTA SOPHY BEULET P, SIVASUBRAMANIAN A	Slot	: E1+TE1
Time	: 90 Minutes	Max. Marks	: 50

Answer ALL the questions

Note: All the programs should have the comments which describes the logic of the program

Q.No.	Sub. Sec.	Questions	Marks																
1.		Compare 8085 processor with 8086 processor.	5																
2.		<p>The various registers in 8086 microprocessor contain the value as given in Table 1.</p> <p style="text-align: center;"><b>Table 1</b></p> <table border="1"> <tr> <td>CS : 2000H</td><td>DS: 3000H</td><td>ES: 3500H</td><td>SS: 2500H</td></tr> <tr> <td>DI: 4000H</td><td>BP: 4C50H</td><td>BX:34FE</td><td>IP:2345H</td></tr> <tr> <td>SI: 1000H</td><td>SP: 1550H</td><td>DX: 13F2H</td><td></td></tr> </table> <p>Fill column 3 and column 4 of Table 2 for the instruction given in column 2 of Table 2. (Note: Detailed calculation of physical address is expected in answer sheet, final answer to be written in column 4).</p> <p style="text-align: center;"><b>Table 2</b></p> <table border="1"> <tr> <th>S. No</th><th>Instruction</th><th>Addressing Mode</th><th>Physical Address</th></tr> </table>	CS : 2000H	DS: 3000H	ES: 3500H	SS: 2500H	DI: 4000H	BP: 4C50H	BX:34FE	IP:2345H	SI: 1000H	SP: 1550H	DX: 13F2H		S. No	Instruction	Addressing Mode	Physical Address	10
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		1.	CLC				
		2.	MUL [DX]				
		3.	MOV AL, [BX+80H]				
		4.	MOV AL, [BP+70H]				
		5.	MOV AL, 56H				
3.		<p>Ten 8-bit hexadecimal numbers are stored in memory locations starting from 2000H to 2009H. Write an 8086 assembly language program to perform the following equation for stored ten numbers.</p> $N = \frac{(\text{Sum of odd numbers} - \text{sum of even numbers})}{(\text{number of odd number} - \text{number of even number})}$ <p>Store the 'N' value in location 200AH.</p>					10
4.	a.	<p>Mention the interface used for connecting input/output device to 8086 microprocessor.</p> <p>(1 mark)</p>					10
	b.	<p>Explain the various modes of operation in detail of the interface you identified in part (a).</p> <p>(5 marks)</p>					
	c.	<p>Write the control word format of the interface you identified in part (a), to connect 3 devices given (i) transceiver (that can both transmit and receive simultaneously), (ii) LCD &amp; (iii) 2 LEDs. Explain the same. (4 marks)</p>					
5.		<p>Explain the function of the following pins of 8051</p> <p>(i) <math>\overline{EA}</math></p> <p>(ii) <math>\overline{PSEN}</math></p> <p>(iii) ALE</p> <p>(iv) P0.0-P0.7</p> <p>(v) P3.2(<math>\overline{INT0}</math>)</p>					5
6		<p>Write the results after execution of each instruction in the following program</p> <pre> ORG 0000H MOV B,#23H MOV R1,#7EH MOV 0E0H, 01H SETB PSW.7 RLC A CPL A ANL A,B XCH A,B MUL AB MOV 25H, R1 MOV @R1,A END </pre>					10

