

process means program in execution. OS can allocate required memory space for program execution. The process have a predefined structure as shown below

	stack
	heap
	data
	code / text

In this process memory space is divided into four segments

#### (i) Code / text segment.

It used to store actual program. It is fixed size segment. Its size cannot be change when the process executed.

#### (ii) Data segment.

It is used to store data values related to the program like variable initialization etc.

#### (iii) Heap segment.

It allocates memory which may be processed during its run time.

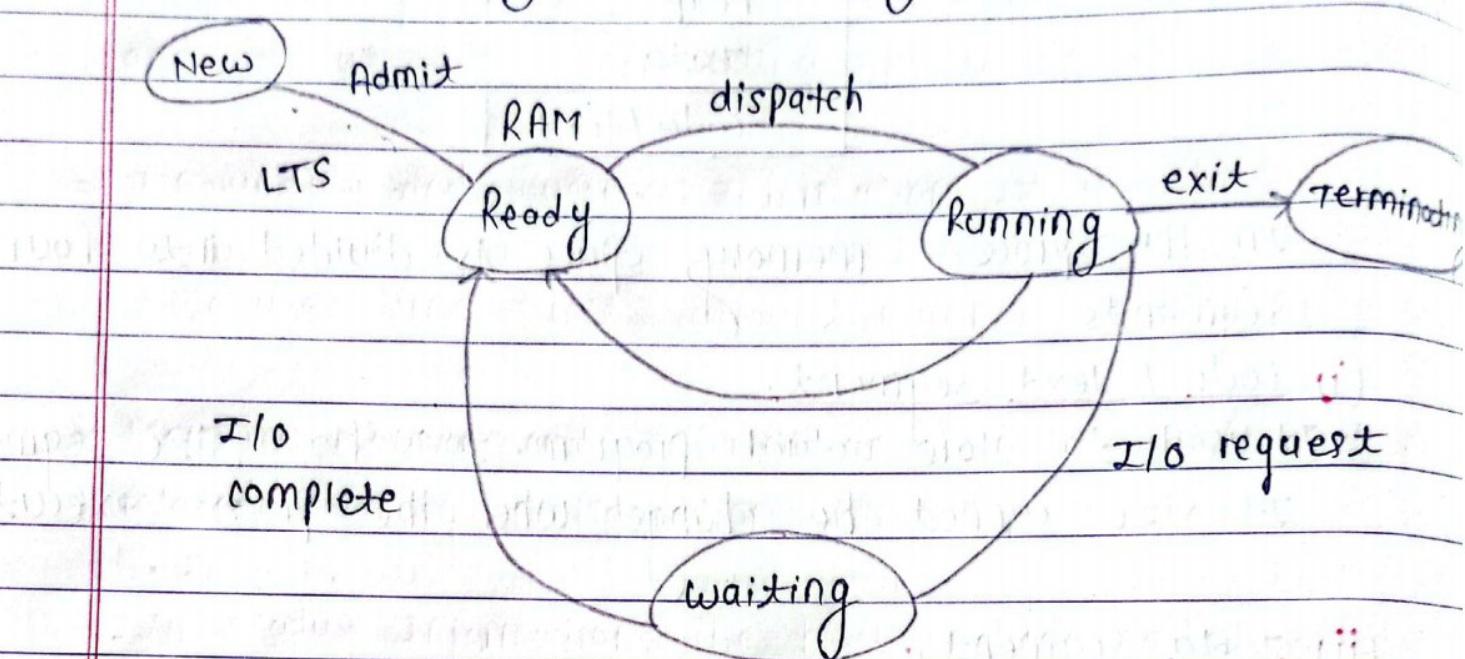
#### (iv) Stack segment.

The stack store temporary data like function parameters, return address and local variables.

Imp

## ① Process state:

A process may change its state during process execution. Process states describes position of the process at particular time. A process possible to placed in following states during its execution.



④ New: The process that is just being created. The program counter block (PCB) is already being made but the program is not yet loaded in main memory (RAM). The program remains in the new state until the long term scheduler (LTS) moves the process to the ready state (main memory).

② Ready: A process that is waiting to be executed.

③ Running: The process that is currently being executed.

④ Waiting: A process that cannot execute until some event occurs, such as the completion of an I/O operation.

- ⑤ Terminated : The process that is finished due to some reasons.

## # Process Control Block (PCB).

A process control block (PCB) is a data structure that is created and managed by the OS for every process. Every process or program that runs need a PCB. When a user requires to run a particular program, the OS constructs a corresponding process control block for that program. The process control block is the key tool that enables the OS to support multiple processes and to provide multi-processing.

Since a process is uniquely characterized by the following elements, a control process control block contains all these elements.

process state
program counter
CPU Register
CPU scheduling Information.
Accounting and business Information.
Memory Management Information.
I/O status Information.

→ Process state: A process can be new, ready, running, waiting, terminating

→ Program Counter: The program counter lets you know the address of the next instruction, which should be executed for the process.

- CPU register: This component include accumulators, index and general purpose registers and information of condition code.
- CPU scheduling information: This component include a process priority, pointers for scheduling queue and various other scheduling parameters.
- Accounting and Business information: It include the amount of CPU and time utilites a like real no. time use, time limit, account numbers, job or processes etc.
- Memory management information: include information related memory configuration for a process
- I/O status Information: The block include a list of open files, the list of I/O device that are allocated to the process.

## # Threads

A process is divided into smaller task and each task is called a Thread. A thread (sometimes called a lightweight) is a single sequential execution stream within a process. It is not a itself a self program because it cannot run its own. A thread has its own registers, program counters, stack. A thread share address space, program code counter program code, global variables, OS resource (files, I/O devices) with other threads.

The process can be split down into so many threads. For eg, in a browser, many tabs

Tabs can be viewed as thread.

### Multi-threading

- A process is divided into number of smaller task, each task is called a thread. Number of threads within a process execute at a time is called multi-threading.
- If a program is multi-threaded even when some portion of it is blocked the whole program is not blocked. The rest of the program continues working if multiple CPU's are available.
- Multi-threading gives best performance. If we have only a single thread, no. of CPU's available, no performance benefits achieved.

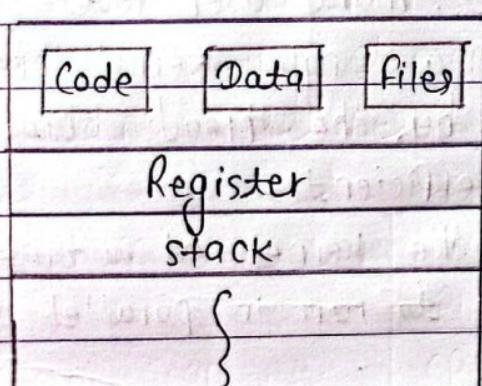


fig single threaded process

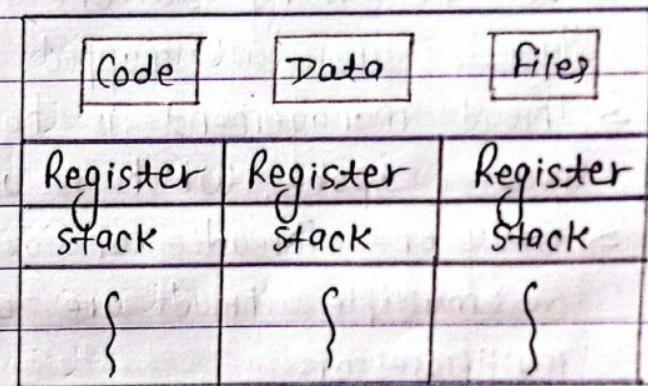


fig multiple threaded process.

### Types of Thread

#### ① User level threads.

- User-level threads are managed by user level library.
- User-level threads are typically fast.
- Context switching fast.
- If one user level threads perform blocking operation then entire process get blocked

## ② Kernel-level threads.

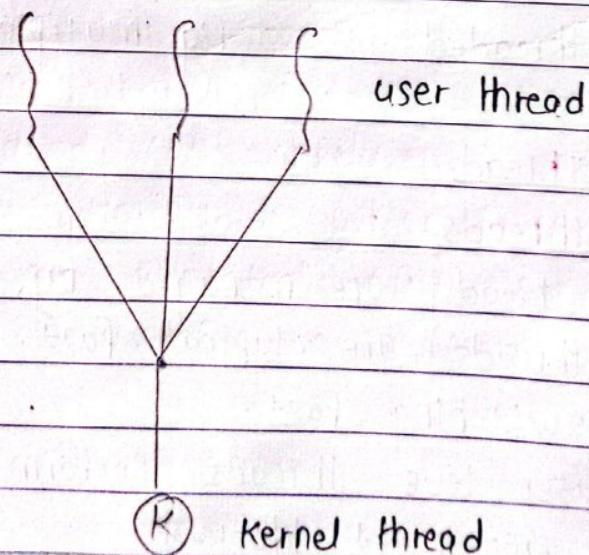
- Kernel-level threads are managed by OS.
- Kernel-level threads are slower than User-level threads.
- Context-switching is slow.
- If one Kernel-level threads blocked, no effect on other.

## Multithreading models.

- ① Many to one relationship
- ② One to one relationship.
- ③ Many to Many relationship.

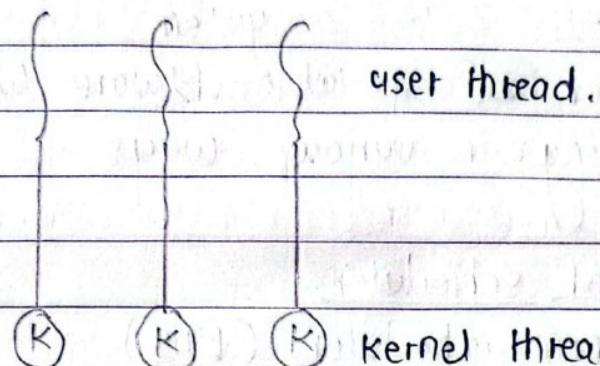
### ① Many to one relationship.

- In the many to one model, many user level threads are mapped onto a single kernel thread.
- Thread management is handled by the thread library in user space, which is very efficient.
- Only one thread can access the kernel at a time, so multiple threads are unable to run in parallel on multiprocessor.



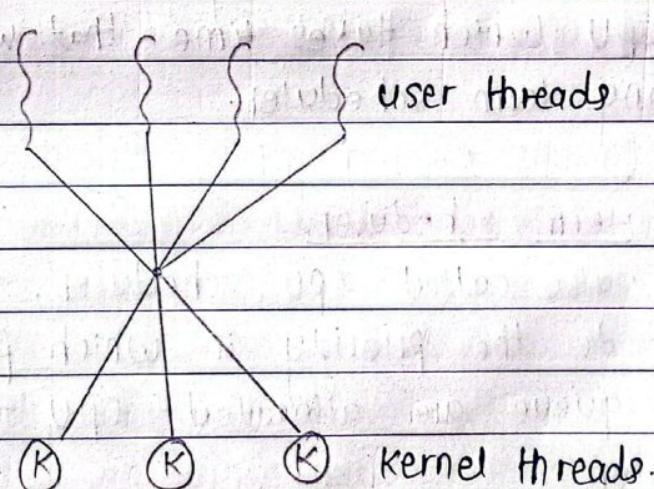
### ② One to one relationship.

- It supports multiple threads to execute in parallel on up.
- One to one model overcomes the problem listed above involving blocking system calls and the splitting of processes across multiple CPUs.



### ③ Many to many relationship

- Many to many model, many user level threads, multiplexed to the kernel threads of small or equal numbers. The no. of kernel threads may be specific to either a particular application or a particular machine.
- User have no restriction on the no. of threads created.



### Scheduling

process scheduling in the OS that schedules processes in various states such as Ready, waiting and running. process scheduling allows the OS to assign each

process a time interval for CPU execution.

→ The prime aim of the process scheduling system is to keep the CPU busy all the time and to deliver minimum response time for all programs.

### Scheduler system

Schedulers are special software which handle process scheduling in various ways

#### Types of scheduler.

- ① Long term scheduler (LTS)
- ② Short term scheduler (STS)
- ③ Medium term scheduler (MTS)

##### ① Long term scheduler

process of long term scheduler are placed in the ready state because in this state the process is ready to execute waiting for calls for execution from CPU which takes time that's why this is known as Long term scheduler.

##### ② Short term scheduler.

- It is also called CPU scheduler.
- It decides the priority in which processes are in the Ready queue are allocated CPU time for their execution.

##### ③ Medium term scheduler.

- It is also called swap scheduler.
- The task of moving from Main memory to Secondary memory is called swapping out. The task of moving

a swapped out process from secondary memory to main memory is known as swapping in.

→ The swapping of processes is performed to ensure the best utilization of main memory.

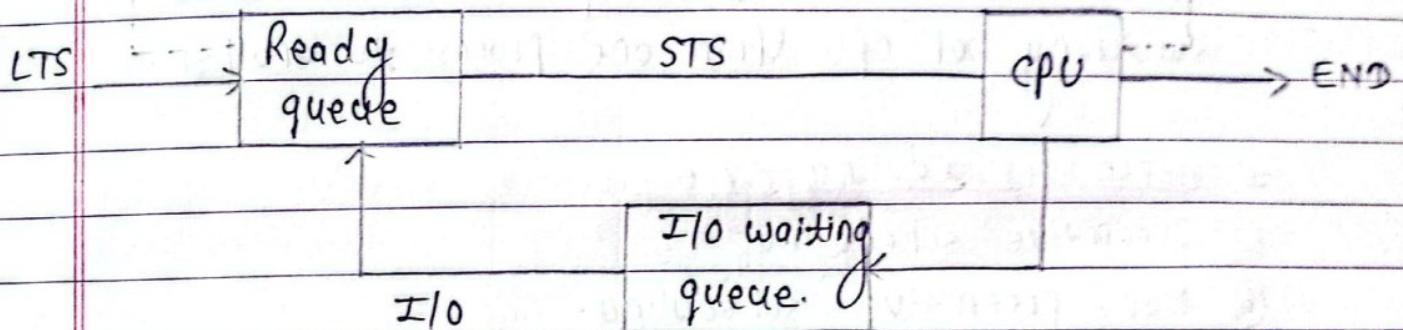


Fig: types of scheduler.

### # Scheduling Criteria.

#### ① Arrival Time.

A time at which the process arrives in the Ready queue

#### ② Completion time.

Time at which process completes its execution.

#### ③ Burst Time

Time required by a process for CPU execution. This doesn't include waiting time.

#### ④ Turn Around Time (TAT)

Time difference between completion time and Arrival time.

OR,

$$\text{Time Around Time} = \text{Completion Time} - \text{Arrival Time.}$$

#### ⑤ Waiting Time (WT)

Time difference between Turn Around Time and Burst

TIME.

OR,

Waiting time = Turn around time - Burst time.

### ⑥ Context switching

A context switching is a process that involves switching of CPU from one process to another.

### # Process scheduling types.

- ① Preemptive scheduling
- ② Non-preemptive scheduling.

#### ① Preemptive scheduling.

The scheduling in which a running process can be interrupted if a high priority process enters the queue and is allocated to the CPU is called preemptive scheduling. In this case, current <sup>process</sup> switched from the running queue to Ready queue and the high priority process utilizes the CPU cycle.

#### ② Non-preemptive scheduling.

→ The scheduling in which a running process cannot be interrupted by any other process is called Non-preemptive scheduling.

→ Any other process which enters the queue has to wait until the current process finish its CPU cycle.

→ When a Non-preemptive process with a high CPU burst time is running, the other process would have to wait for a long time, and that increases the process average waiting time in the Ready queue.

## # Scheduling algorithms.

The various scheduling algorithm are as follows:

### ① Scheduling in batch system

- First come first served (FCFS) scheduling
- shortest job first (SJF) scheduling
- shortest Remaining Time first (SRTF)

### ② Scheduling in interactive system.

- Priority scheduling.
- Round Robin (RR) scheduling.
- Highest Response ratio next (HRRN) scheduling.

### First come first served (FCFS) scheduling.

- One of the simplest scheduling algorithm.
- As the name implies, the processes are executed in the order of arrival in the ready queue which means the process that enters the ready queue first gets CPU first.
- It is non preemtive algorithm. Therefore once a process gets the CPU, it retains control until it blocks or terminates.
- Implementation of Ready queue is managed as FIFO queue.

### Advantage

It is simple and easy to understand.

### Disadvantage

If processes with higher burst time arrived before processes with smaller burst time then smaller processes have to wait for a long time for long processes to release CPU.

Consider the following set of processes that arrived at time 0 with the length of the CPU burst time given in milisecond.

process	Burst time
P <sub>1</sub>	24
P <sub>2</sub>	3
P <sub>3</sub>	3

Gantt chart: suppose processes arrived in order P<sub>1</sub>, P<sub>2</sub> and P<sub>3</sub>

P <sub>1</sub>	P <sub>2</sub>	P <sub>3</sub>
24	27	30

process	Burst time (AT)	Arrival time (T <sub>0</sub> )	Finish time (T <sub>f</sub> )	TAT	WT	RT
P <sub>1</sub>	24	0	24	24	0	0
P <sub>2</sub>	3	0	27	27	24	24
P <sub>3</sub>	3	0	30	30	27	27

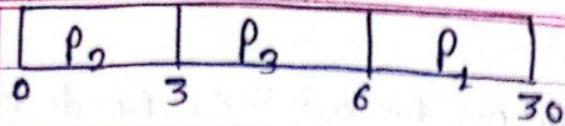
Now

$$\text{Average turn around time (ATAT)} = \frac{(24 + 27 + 30)}{3} = 27$$

$$\text{Average waiting time (AWT)} = \frac{(0 + 24 + 27)}{3} = 17 \text{ ms}$$

Suppose processes arrived in order P<sub>2</sub>, P<sub>3</sub>, P<sub>1</sub>

Process	Burst time
P <sub>2</sub>	3
P <sub>3</sub>	3
P <sub>1</sub>	24



process	Burst time $\Delta T$	Arrival time ( $T_0$ )	Finish time ( $T_f$ )	TAT	WT	RT
P <sub>2</sub>	3	0	3	3	0	0
P <sub>3</sub>	3	0	6	6	3	3
P <sub>1</sub>	24	0	30	30	6	6

NOW

$$\text{Average turn around time (ATAT)} = \frac{(3+6+30)}{3} = 13 \text{ ms}$$

$$\text{Average waiting time (AWT)} = \frac{(0+6+3)}{3} = 3 \text{ ms}$$

### Shortest Job First (SJF) scheduling.

- Also called shortest process next (SPN) or shortest requires next (SRN).
- It is a scheduling algorithm that schedules the processes according to the length of CPU burst they required.
- When the CPU is available, it is assigned to the process that has smallest next CPU burst.
- If the next burst of two processes are the same, first come first serve is used.
- Shortest job first scheduling can be either preemtive or non-preemptive.

### Advantage

- Shortest job are favored.
- It gives minimum average waiting time for a given set of processes.

### Disadvantage

→ It is difficult to implement as it needs to know the length of CPU burst a processes in advanced.

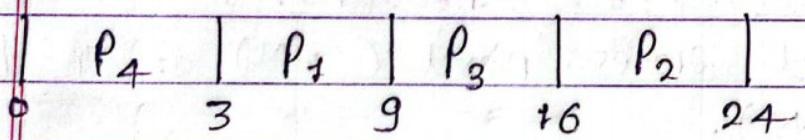
examples

consider the following set of processes that arrived at time 0 with the length of CPU burst in millisecond.

process	Burst time	Arrival time
P <sub>1</sub>	6	0
P <sub>2</sub>	8	0
P <sub>3</sub>	7	0
P <sub>4</sub>	3	0

so

Gantt chart



process	Burst time	Arrival time	final time	TAT	WT	RT
P <sub>1</sub>	6	0	9	9	3	3
P <sub>2</sub>	8	0	24	24	76	76
P <sub>3</sub>	7	0	16	16	9	9
P <sub>4</sub>	3	0	3	3	0	0

$$\text{Average turn around time (ATAT)} = \frac{9+24+16+3}{4} = 13 \text{ ms}$$

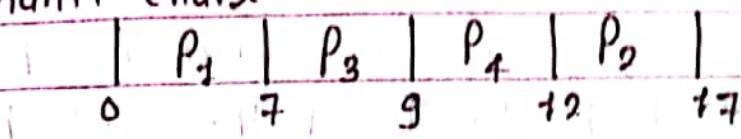
$$\text{Average waiting time (AWT)} = \frac{3+76+9+0}{4} = 7 \text{ ms}$$

Response time = CPU time - arrival time

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process	Burst time	AT
P <sub>1</sub>	7	0
P <sub>2</sub>	5	1
P <sub>3</sub>	2	3
P <sub>4</sub>	3	4

Gantt chart



process	Burst time	Arrival time	Final time	TAT	WT	RT
P <sub>1</sub>	7	0	7	7	0	0
P <sub>2</sub>	5	1	12	11	11	11
P <sub>3</sub>	2	3	14	6	4	4
P <sub>4</sub>	3	4	17	8	5	5

NOW,

$$ATAT = \left( \frac{7+11+6+8}{4} \right) = 9.25 \text{ ms}$$

$$AWT = \left( \frac{0+11+4+5}{4} \right) = 5 \text{ ms.}$$

### Shortest remaining time first (SRTF) . (preemtive)

shortest remaining time first (SRTF) is a preemtive version of shortest job first (SJF). In this case , the scheduler always choose the process that has shortest remaining processing time . when a new process going the ready queue , the scheduler compares the remaining time of executing process and new process. if the new process has the least CPU burst time , the scheduler select that job and allocate CPU , otherwise it continue with the old process.

**Advantage**

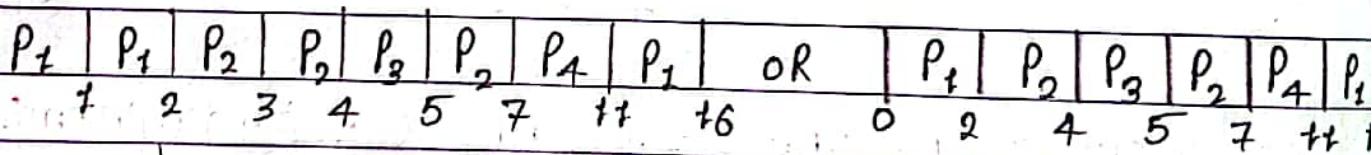
- SRTF does not have the bias in favour of long process found in FCFS.

**Disadvantage**

- There is a risk of starvation of longer process.

- Q. Consider the following set of processes that arrive at given time, with the length of the CPU burst given in millisecond.
- ② calculate AWT and ATAT.

process	Arrival time	Burst time	
P <sub>1</sub>	0	7	
P <sub>2</sub>	2	4	
P <sub>3</sub>	4	7	
P <sub>4</sub>	5	4	

SOLN**Gantt chart:**

Process	Arrival time	Burst time	final time	TAT	WT	RT
P <sub>1</sub>	0	7	7	7	0	0
P <sub>2</sub>	2	4	7	5	2	0
P <sub>3</sub>	4	7	11	7	0	0
P <sub>4</sub>	5	4	9	6	2	2

$$\therefore \text{ATAT} = \left( \frac{7+5+7+6}{4} \right) = 7 \text{ ms}$$

$$\text{AWT} = \left( \frac{9+7+0+2}{4} \right) = 3 \text{ ms}$$

## Priority scheduling

- ① Priority scheduling can be either preemptive or non-preemptive.
- ② A preemptive priority scheduling algorithm will preempt the CPU if the priority of newly arrived process is higher than the priority of the currently running process.
- ③ Non-preemptive scheduling algorithm will simply put the new process at the head of ready queue.
- ④ Equal priorities are scheduled in FCFS order.

### Advantage

process are executed on the basis of priority so higher priority does not need to wait for long which save time.

### Disadvantage

If high priority process use of a lot of CPU time, lower priority process may ~~stop~~ <sup>starve</sup> and be postponed indefinitely.

for eg, Let us consider following 5 process with arrival time zero (0) ms and length of the CPU burst given in millisecond consider one is highest priority.

Calculate, Average waiting time (AWT)

Average turn around time (ATAT)

process	Burst Time	priority	[Non preemptive way]
P <sub>1</sub>	40	3	
P <sub>2</sub>	1	1	
P <sub>3</sub>	2	4	
P <sub>4</sub>	1	5	
P <sub>5</sub>	5	2	

### Gantt chart:

P <sub>2</sub>	P <sub>5</sub>	P <sub>1</sub>	P <sub>3</sub>	P <sub>4</sub>
0	1	6	16	18

process	Burst time	Finish time	Arrival time	TAT	WT
P <sub>1</sub>	4	10	0	4	6
P <sub>2</sub>	1	11	0	1	0
P <sub>3</sub>	2	13	0	3	16
P <sub>4</sub>	1	14	0	4	18
P <sub>5</sub>	5	19	0	9	12

$$ATAT = (16 + 1 + 18 + 9 + 6) / 5 = 12 \text{ ms}$$

$$AWT = (6 + 0 + 16 + 18 + 12) / 5 = 8.2 \text{ ms}$$

### Preemptive way

Process	Burst time	Priority	Arrival time
P <sub>1</sub>	4	3	0
P <sub>2</sub>	1	1	1
P <sub>3</sub>	2	4	2
P <sub>4</sub>	1	5	4
P <sub>5</sub>	5	2	6

### Gantt chart:

P <sub>1</sub>	P <sub>2</sub>	P <sub>3</sub>	P <sub>4</sub>	P <sub>5</sub>	P <sub>1</sub>	P <sub>5</sub>	P <sub>2</sub>	P <sub>3</sub>	P <sub>4</sub>
0	1	2	3	4	6	11	16	18	19

process	Burst time	Priority	Arrival time	Finish time	ATAT	WT
P <sub>1</sub>	4	3	0	10	16	16
P <sub>2</sub>	1	1	1	2	2	1
P <sub>3</sub>	2	4	2	14	18	16
P <sub>4</sub>	1	5	4	15	19	15
P <sub>5</sub>	5	2	6	21	5	0

NOW.

$$ATAT = \frac{16+1+16+15+5}{5} = 10.6 \text{ ms}$$

$$AWT = \frac{6+0+14+14+0}{5} = 6.8 \text{ ms}$$

Consider a set of three processes  $P_1, P_2, P_3$  with their priorities and arrival time as given below. calculate AWT for preemptive priority based algorithm.

process	Burst time	priority	Arrival time
$P_1$	10	3	0
$P_2$	5	2	1
$P_3$	2	1	2

where '1' denotes highest priority and '3' denotes lowest priority.

Gantt chart

$P_1$	$P_2$	$P_3$	$P_2$	$P_1$
1	2	4	8	17

process	Burst time	Arrival time	Finish time	ATAT	WT
$P_1$	10	0	17	17	7
$P_2$	5	1	8	7	2
$P_3$	2	2	4	2	0

NOW

$$AWT = \frac{7+2+0}{3} = 3 \text{ ms}$$

Consider the set of processes with arrival time (in ms), CPU burst time (in ms) and priority 0 is the highest priority using preemptive scheduling algorithms.

Process	AT	Burst time	Priority
P <sub>1</sub>	0	11	2
P <sub>2</sub>	5	28	0
P <sub>3</sub>	12	2	3
P <sub>4</sub>	2	10	1
P <sub>5</sub>	9	16	4

Gantt chart

P <sub>1</sub>	P <sub>4</sub>	P <sub>2</sub>	P <sub>4</sub>	P <sub>3</sub>	P <sub>3</sub>	P <sub>5</sub>
0	2	5	33	40	49	51

process	AT	Burst time	finish time	TAT	WT
P <sub>1</sub>	0	11	49	49	38
P <sub>2</sub>	5	28	33	28	0
P <sub>3</sub>	12	2	51	39	37
P <sub>4</sub>	2	10	40	38	28
P <sub>5</sub>	9	16	67	58	42

$$ATAT = \frac{49+28+39+38+58}{5} = 42.4 \text{ ms.}$$

$$AWT = \frac{38+0+37+28+42}{5} = 29 \text{ ms.}$$

### Round Robin scheduling.

- Most widely used preemptive scheduling algorithm.
- Specially design for time sharing system.
- It considers all processes equally important and treats in an impartial manner.
- Each process in the ready queue gets a fixed amount of CPU time. (generally from 10-100ms) known as time slice or time quantum for its execution.

→ CPU is assigned to the process on the basis of FCFS for a fixed amount of time called quantum time. After the time quantum expire the running process is preempted and send to the ready queue and then processor is assigned to the next arrive process. This process continues in circular fashion.

### Advantages:

- Every process gets an equal share of the CPU
- Round Robin is cyclic in nature, so there is no starvation.

### Disadvantages:

- This method spends more time on context switching.
- Average waiting time is not minimal.

Q. Let us consider following three processes with arrival time 0ms and length of the CPU burst given in ms  
Consider quantum time (Q.T) = 4ms find

- (a) AWT  
(b) ATAT

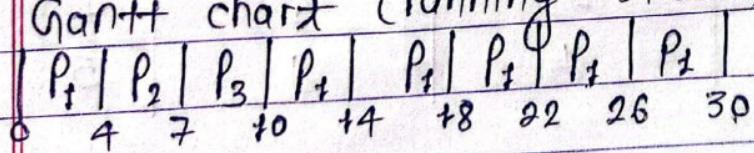
process      Burst time

P<sub>1</sub>            24

P<sub>2</sub>            3

P<sub>3</sub>            3

Gantt chart (running state)



Process	Burst time	Finish time	WT	TAT
P <sub>1</sub>	4	30	6	30
P <sub>2</sub>	3	7	4	7
P <sub>3</sub>	3	10	7	10

$$\therefore AWT = \frac{6+4+7}{3} = \frac{17}{3} = 5.66$$

$$ATAT = \frac{30+7+10}{3} = \frac{47}{3} = 15.66$$

Q.	process	Arrival time	Burst time	Quantum time = 2
	P <sub>1</sub>	0	5	
	P <sub>2</sub>	4	4	
	P <sub>3</sub>	2	2	
	P <sub>4</sub>	4	1	

Ready queue

P<sub>1</sub> | P<sub>2</sub> | P<sub>3</sub> | P<sub>4</sub> | P<sub>1</sub> | P<sub>2</sub> | P<sub>3</sub>

Running queue

P<sub>1</sub> | P<sub>2</sub> | P<sub>3</sub> | P<sub>4</sub> | P<sub>1</sub> | P<sub>2</sub> | P<sub>3</sub>

process	Arrival time	Burst time	Finish time	TAT	WT
P <sub>1</sub>	0	5	5	5	0
P <sub>2</sub>	4	4	9	5	4
P <sub>3</sub>	2	2	6	4	2
P <sub>4</sub>	4	1	9	5	4

NOIO,

$$ATAT = \frac{5+5+4+5}{4} = 7.75$$

$$AWT = \frac{0+4+2+4}{4} = 4.75$$

- # Highest Response ratio next (HRRN) scheduling.
- Non-preemptive scheduling algorithm that schedules the process according to their response ratio.
  - Whenever the CPU becomes available, the process having the highest value of response ratio among all the ready processes is scheduled next.
  - Jobs gain higher priority the longer they wait, which prevent indefinite postponement (process starvation).
  - The response ratio of a process in the queue is computed by using the following eqn.

Response Ratio (RR) =  $\frac{\text{time since arrived} + \text{CPU burst time}}{\text{CPU burst time}}$

### Advantage

- Improves upon SJF scheduling
- consider how long process has been waiting

### Disadvantage

Does not support external priority system process are scheduled by using internal priority system.

- Q. Let us consider following three processes with given arrival and length of the CPU burst is given in ms calculate

a) AWT

ATAT

process	Arrival time	Burst time	Priority
P <sub>1</sub>	0	7	3
P <sub>2</sub>	2	4	1
P <sub>3</sub>	3	4	2

$$RR = (WT + BT) / BT$$

$$RR \text{ for } P_2 = \frac{(7-2) + 4}{4} = 2.25$$

$$RR \text{ for } P_3 = \frac{(7-3) + 4}{4} = 2$$

Gantt chart

	$P_1$	$P_2$	$P_3$
0	7	11	15

process	Arrival time	Burst time	finish time	TAT	WT
$P_1$	0	7	7	7	0
$P_2$	2	4	11	9	5
$P_3$	3	4	15	12	8

$$ATAT = \frac{7+9+12}{3} = 9.33$$

$$AWT = \frac{0+5+8}{3} = 4.33$$

Q.	process	Arrival time	Burst time	
	$P_1$	1	3	
	$P_2$	3	6	
	$P_3$	5	8	
	$P_4$	7	4	
	$P_5$	8	5	

$I_r$	$P_1$	$P_2$	$P_4$	$P_5$	$P_3$
0	1	4	10	14	19

$$\text{Response ratio for } P_3 = \frac{(10-5)+8}{8} = 1.625$$

$$\text{RR for } P_4 = \frac{(10-7)+4}{4} = 1.75$$

$$\text{RR for } P_5 = \frac{(10-8)+5}{5} = 1.40$$

again

$$\text{RR for } P_3 = \frac{(14-5)+8}{8} = 2.125$$

$$\text{RR for } P_5 = \frac{(14-8)+5}{5} = 2.2$$

process	Arrival time	burst time	finish time	TAT	WT
P <sub>1</sub>	2	3	4	3	0
P <sub>2</sub>	3	6	70	7	1
P <sub>3</sub>	5	8	27	22	14
P <sub>4</sub>	7	4	11	7	3
P <sub>5</sub>	8	5	19	11	6

Now,

$$ATAT = \frac{3+7+22+7+11}{5} = 10$$

$$AWT = \frac{0+1+14+3+6}{5} = 4.8$$