Modelling the Suzuki-Kasami Algorithm with the SPIN Model Checker

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Abstract In 1985, Suzuki and Kasami presented two distributed algorithms for mutual exclusion. In this paper, the claim of mutual exclusion, deadlock and starvation freedom are tested using model checking techniques. Using the spin model checker, a counterexample to deadlock freedom was found for one of the algorithms, and a modification is proposed that guarantees deadlock freedom.

1 Introduction

Model checking is the process of automatically verifying the correctness of a system description with respect to its specifications, by examining a model corresponding to the system. The result of model checking is in general a certificate of correctness, or a counterexample showing why the specifications are not met.

There are many tools for model checking available, with different approaches to model checking and with different strengths and weaknesses. The SPIN Model checker (typically written Spin) is a widely used tool for model checking distributed and concurrent systems. The spin model checker was used mainly because both the tested pseudo code and the spin modelling language (promela) has a syntax close to that of C.

1.1 Suzuki and Kasami's Algorithm

The Suzuki/Kasami algorithm[3] (henceforth abbreviated SKA), is a distributed mutual exclusion-algorithm that requires at most N message exchanges for every mutual exclusion invocation between N computer nodes.

Each of the N nodes between which mutual exclusion is to be realized runs two processes P1 and P2. Each node has two local arrays RN and LN and a queue Q, all of which are accessible by P1 and P2.

The array RN holds the latest received REQUEST identifier for each other node. Similarly, LN holds the REQUEST identifier of the latest carried out REQUEST of all the other nodes, of which the node has been notified. Since the algorithm is distributed, the local arrays RN and LN are not the same for each node, and for each node represent only the information of which the node has been informed. The local queue Q holds the nodes that are currently requesting the privilege, in the order the requests have been received.

When a node $node_i$ wants to enter its critical section with its main process P, it needs to have the PRIVILEGE to do so. In case the node already holds the privilege, it enters the critical section directly, without informing the other processes or updating the arrays RN and LN. Otherwise a REQUEST(i, n) message is sent to all other nodes, where i is the nodes identifier (index) and n is the REQUEST identifier.

A requesting node will wait until it receives a PRIVILEGE message, before accessing its critical section. When it is done, it updates its queue by appending all the nodes requesting, that are not already in the queue. Then, a PRIVILEGE is sent to the node first in Q, and that node is removed from the queue.

In process P2 REQUEST(i,n) messages are received. The array RN is updated, so that the corresponding entry RN[i] is set to n. If the node currently holding the PRIVILEGE is not itself waiting for the privilege, it will forward the PRIVILEGE to $node_i$ by sending a PRIVILEGE(Q, LN) message.

The pseudo-code proposed in the original paper is listed in appendix A.

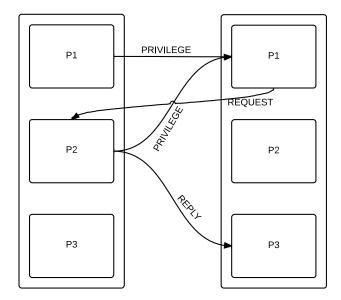


Figure 1: Two nodes running the SKAM algorithm, where the node to the left holds the privilege. The arrow heads point towards the receiver of the messages.

1.2 Suzuki and Kasami's Modified Algorithm

The SKA algorithm has the disadvantage of REQUEST identifier numbers being unbounded. In a modified version of the SKA algorithm (henceforth abbreviated SKAM), Suzuki and Kasami propose a slightly changes to the above algorithm in order to bound the REQUEST identifiers by a value L but with the cost of additional message exchanges.

In the SKAM algorithm a third process P3 is added to each node, and a new message type REPLY. When a node n_1 receives a REQUEST with the REQUEST identifier L from node n_3 , it will send a REPLY message back to the sending node and set RN[3] to 0. The process P3 receives the REPLY messages and increments the variable replyCount (local to the node). The idea is that when a node has finished its L:th critical section, it must wait until a REPLY message has been received from all other processes before it can give the PRIVILEGE to the next process. The replies are thus a guarantee that all other processes have updated their RN-values to 0.

2 Problem Formulation

The SKA and SKAM algorithms are claimed to guarantee mutual exclusion, starvation freedom and deadlock freedom. The purpose of this project is to investigate these claims and the additional property of being free from unnecessary delay, using model checking techniques.

3 Model of ska and skam in Promela

Promela is a modelling language with a relatively small set of predefined types and function. Because of this, a model of the pseudo code in appendix A must be implemented using the set of instructions available in promela.

3.1 Nodes

In the SKA algorithm, every node has two processes which have shared variables. This style requires different levels of variable access, i.e. variables that can be accessed by several processes (meaning it is not local), but not by all processes (meaning it is not global). Since only global and local variables exist in promela, the model uses global variables for these shared variables. The global variables are seen

perceived to belong to a node, and must only be accessed by processes within that node. To ensure this, every node has a node identifier, and every process is passed this identifier as an argument.

The convention is, that for every shared variable, an array of those variables are created - one for each node. The i:th array cell belongs to node i, and is the only cell in the array that processes of node i may access.

As an example, consider the boolean variable requesting belonging to node i, which must be accessible both by function P1(i) and P2(i). In promela, an array requesting[N] is created, and the processes may only access the cell requesting[i].

3.2 Queue

A queue data structure could be implemented in several ways in promela, naturally suited for different applications. We note that in the SKA algorithm, the key features of the queue is first that values are read *first-in first-out* (FIFO) and second the ability to determine whether a certain value is already in the queue or not.

The promela channel data type is a FIFO data structure, but it provides no means of determining if a value is already in the queue or not. To surmount this problem, a new *Queue* data structure is defined;

```
typedef Queue {
    chan ch = [N] of {short};
    bool inQ[N];
}
```

This implementation draws upon the fact that a queue in this setting, needs at most store N values, since values may only be added if they are not already in the queue, and the range of numbers that are to be stored are the node indexes ranging from 0 to N-1.

To add a new value n to the queue, it must first be checked that the value is already in the queue (inQ[n] is false). Then the value is added to the channel ch, and inQ[n] be set to true.

To remove the top value from the queue, the opposite is done. The value n is read from the channel ch, and inQ[n] is set to false, indicating that the value n is not in the queue.

3.3 Messages

Messages in promela can be sent using the built-in channel datatype (chan). There are different types of messages being sent, PRIVILEGE REQUEST. To model this, new datatypes were defined as follows;

```
typedef REQUEST{
chan ch = [N] of {byte, byte};

typedef \texttt{PRIVILEGE}{
    chan ch = [N] of {Queue, Array};
}
```

In the model, a REQUEST is created for each node. When a node i requests the PRIVILEGE with a REQUEST identifier n, it will write the values (i,n) to the REQUEST channel of each process other than its own. As only one process has the PRIVILEGE at any time, only one instance of PRIVILEGE needs be created. When a nodei wants to send the PRIVILEGE to another node j, node i will write its current Queue and its current LN-array to the PRIVILEGE channel.

Since the PRIVILEGE channel can be read by any node, there need be a way to determine which process is to read this channel. This is solved by a global array havePrivilege[N], that is set to false for all indices except of that belonging to the current holder of the privilege. When the process i above has sent its message on the channel, it will set (havePrivilege[i]) to false and (havePrivilege[j]) to true. This indicates to process j that it can read the PRIVILEGE channel.

REPLY messages are implemented as a regular channel, writing and reading to the channel correspond to sending and receiving, and the values are of no importance.

3.4 Event Handling

The process P2 (and in SKAM also P3) is to be ran when a message is received. This indicates a type of event handler that does not exist in promela. To overcome this problem, we first note that the strength in using a event handler is that it does not rely on busy waiting, i.e. a loop that continuously checks whether a certain predicate is true before it continues. In a classic programming language such a C, busy waiting requires much processing power. In promela though, an if-statement or a loop for which no predicate is true will always wait until the predicate is true without using busy waiting. This means that busy waiting in promela is not at all "busy", and therefore process P2 can be implemented as a loop with the predicate nempty(REQUESTING[i]).

It is easy to see that the loop will be run once for each message received, and since Spin does not always execute the loop as soon as it is possible, arbitrarily long "time" may pass until a REQUEST is actually processed. On the other hand, messages can only be received in the order they are sent whereas in reality a message can be delayed without affecting other messages. To solve this issue, the loop can be extended with the non-deterministic choice of reading a message and write it back to the end of the queue, in effect delaying the message.

4 Properties as LTL-Formulae

Due to the REQUEST identifier numbers, the SKA algorithm has the disadvantage of unboundedness, meaning that the state space of the model is infinite. There are techniques to overcome these problem, for example to only check the validity of properties within a finite portion of the infinite transition system. Another solution is to model an abstraction of the algorithm, in order to bound the state space (using the fact that the actual REQUEST number is not interesting, only whether it is smaller than some other value or not).

In this paper, the former approach was used to bound the searched portion of the transition system. The models were checked both for two and three processes running in parallell, and with the value of L set to 2 (in the SKAM model).

WHY CAN A MODEL WITH FOUR PROCESSES NOT EXHIBIT MORE BEHAVIOURS THAN A MODEL WITH THREE PROCESSES? DISKUSSION?

4.1 Deadlock Freedom

With the spin model checker, deadlocked states are considered to be erroneous states and are included in a standard set of checks run by spin. Thus no extra effort is required to determine the absence of such states, as it will be done automatically unless the model checker is not told to ignore invalid end states. Deadlock freedom can also be shown explicitly for the SKAM model by checking the LTL-formula []!timeout. timeout is a predefined global read-only name in the promela language, and is true only if no active process is able to proceed.

The corresponding LTL-formula for the SKA model simulating two nodes is !timeout U (RN[0].ind[0] > $2 \mid \mid RN[1].ind[1] > 2$).

Here RN[0].ind[0] denotes the value of RN[0] in the node with node identifier 0. This formula says that no deadlocks can occur, while the REQUESTnumbers of all processes is less than 2.

4.2 Starvation Freedom

Starvation freedom is the property that no node is consistently over run by the other processes, i.e. every process will get its turn eventually. This is a liveness property, and needs some degree of fairness to be checked for situations that are reasonable. For example, a node that never requests to enter the critical section will never enter its critical section. Using fairness properties, this kind of irrelevant examples can be excluded. Fairness assumptions can easily be expressed in LTL, in particular it can be expressed that if a process n_i wants to enter its critical section continuously (that is weak fairness), it will always be able to eventually: $\$ []requesting[i] $\$ []

The corresponding LTL formula for the SKA model simulating two processes is (requesting[i] -> <>(havePrivilege[i])) U (RN[0].ind[0] > 2 || RN[1].ind[1] > 2). This formula says that if the

node with node identifier i requests the privilege, it will eventually get the privilege, while the REQUEST numbers of all processes is less than 2. It is worth noting that expressed in this way, the property is no longer a liveness property but a safety property.

4.3 Mutual Exclusion

Mutual exclusion is the property that at most one node can be executing critical code at the same time. In promela, LTL formula can use built-in *labels* to check properties such as []!(P1[0]@critSection && P1[1]@critSection). Another way to check for mutual exclusion is to add a shared variable, which is increased by one each time a process enters the critical section, and decreased by one each time it leaves the critical section. The LTL formula to be checked is then []counter < 2.

The corresponding LTL formula for the SKA model simulating two processes is (counter < 2) U $(RN[0].ind[0] > 2 \mid \mid RN[1].ind[1] > 2)$.

4.4 Freedom of Unnecessary Delay

This describes the property that a process that wishes to enter its critical section must not be delayed unless another process is already in the critical section. In contrast to the terms *Starvation freedom* and *deadlock* that are well-defined term, it is unclear what is to be considered "Unnecessary" delay. With a rigid definition of "Unnecessary", this property is trivially false - a token-based algorithm must at least wait for the token (i.e. the privilege). On the other hand, a too tolerant definition would make the property trivially true - if a process needs to wait at some point, then surely it is necessary to wait.

Let freedom of *unnecessary delay* imply that when a node holds the privilege, it can enter the critical section without any additional communication with other nodes.

5 Results

5.1 Deadlock Freedom

The SKAM algorithm was shown not to be free from deadlocks, but the deadlock is directly related to the modifications extending SKA to SKAM, and would not be present in the SKA algorithm. The problem can occur when a process $P1_n$ requests the PRIVILEGE for the L:th time; after receiving the PRIVILEGE process must wait additionally until the other processes have sent REPLYmessages. The indicator of having received replies is a counter replyCount, which is set again to zero when the replies have been received. The deadlock can occur in the next iteration if none of the other processes have requested. If this happens, the process $P1_n$ can attempt to enter the critical section again, and does not need to increase increase its current REQUEST number. As the REQUEST number is unchanged, the process must again wait for the REPLY messages of the other processes, but these have already been sent and received. This marks a deadlock state, where the process holding the PRIVILEGE is waiting infinitely, and no other process can get the privilege.

This problem can be solved rather simply, by adding a local variable nreceived to the process P1. The variable is set to 0 whenever a REQUEST is made, but said to 1 when all replies have been received. When the process P1 iterates again after its L:th request, it will only wait for replies if the indicator nreceived is not set.

Using this small extension the model was shown to be free from deadlocks. All further model checking results are based on the deadlock-free model. Note that it has been assumed that a nodes execute infinitely often (at least the node holding the privilege); without this non-termination assumption, a deadlock might occur [1][2].

5.2 Starvation Freedom and Mutual Exclusion

The tests show that the SKAM algorithm satisfies mutual exclusion, and is free from starvation. The same results hold for SKA, when checked in a bounded region of the transition system.

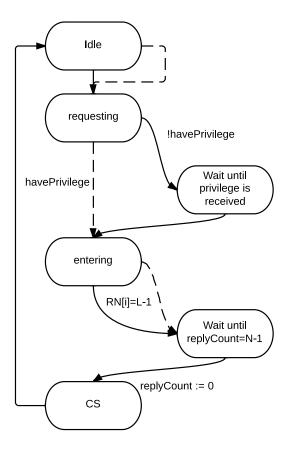


Figure 2: The behaviour of node $P1_n$ when a deadlock occurs. A continuous line marks a transition in the first iteration, a dashed line a transition in the second iteration. The example begins in the state idle.

5.3 Freedom of Unnecessary Delay

As defined in 4.4, freedom of unnecessary delay implies that a node holding the PRIVILEGE may enter the critical section without any additional communication with other nodes. This property does not hold for the SKAM algorithm, since every L:th round the node with the PRIVILEGE must first wait for REPLY messages from the other nodes.

Promela provides the keyword dstep, which can be used to check this property. A block of code written within a dstep block must be executed deterministically; if the process must wait for a message to be received or for a condition to be true, the code is *blocked*. In spin, a block within a dstep is perceived as an error. By enclosing the code following the point where the PRIVILEGE is received within a dstep block, this property can in effect be checked along side another property, for example while checking for starvation freedom.

The results show that the SKA algorithm satisfies this property, and that the SKAM algorithm satisfies this property whenever the REQUEST identifier of a node is not L-1.

6 Discussion

7 references

References

1. Ogata, K., Futatsugi, K.: Analysis of the Suzuki-Kasami Algorithm with SAL Model Checkers. In: Proceedings of the 2005 The Fifth International Conference on Computer and Information Technology (CIT 05)

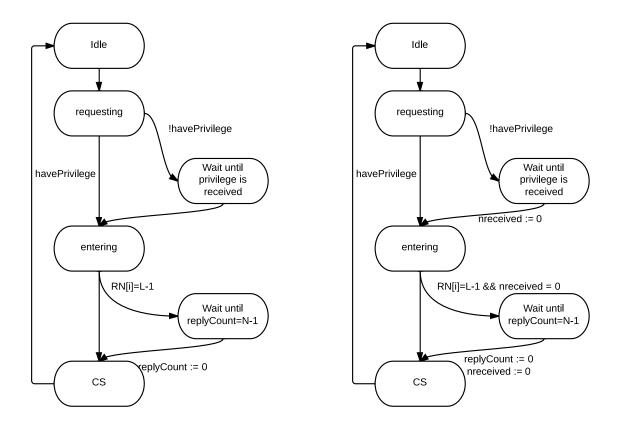


Figure 3: Simplified flow chart of the SKAM algorithm (left). Flow chart after extending the model with a variable nreceived

- 2. Ogata, K., Futatsugi, K.: Analysis of the Suzuki-Kasami Algorithm with the Maude Model Checker. In: Proceedings of the 12th Asia-Pacific Software Engineering Conference (APSEC 05)
- 3. Suzuki, I., Kasami, T.: A Distributed Mutual Exclusion Algorithm. In: ACM Transactions on Computer Systems, Vol. 3, No. 4, November 1985, Pages 344-349.

A Appendix A

```
1
     const I: Integer; (* the identifier of this node *)
 2
       var HavePrivilege, Requesting: bool;
       j, n: integer;
 3
       Q: queue of integer;
 4
 5
       RN, LN: array[l . . N] of integer;
 6
       (* The initial values of the variables are:
 7
       Have\texttt{PRIVILEGE}= true in node 1, false in all other nodes;
 8
       Requesting = false; Q = empty; RN[j] = -1, \ j = 1, 2, \dots, \ N; \ \ LN[j] = -1, \ j = 1, 2, \dots, \ N; \ *)
 9
10
11
12
       procedure Pl;
13
       begin
14
         Requesting := true;
15
         if not Have\texttt{PRIVILEGE}then
16
            RN[I] := RN[Z] + 1;
17
            for all j in 11, 2, . . . , NJ - \{Z\} do
18
```

```
19
             Send REQUEST(1, RN[I]) to node j;
20
           Wait until PRIVILEGE(Q, LN) is received;
21
           Have \text{TRIVILEGE} := true
22
23
24
         Critical Section;
25
26
         LN[Z] := RN[Z];
27
         for all j in 11, 2, . . . , N) - [I) do
           if not in(Q, j) and (RN[jJ=LN[j]\,+\,1) then
28
29
             Q:=\mathrm{append}(Q,\,j);
30
         \mathbf{if} \; \mathbf{Q} \; \mathbf{f} \; \mathbf{empty} \; \mathbf{then}
31
         begin
           Have \text{TRIVILEGE} := false;
32
           Send PRIVILEGE(tail(Q), LN) to node head(Q)
33
34
35
         Requesting := false
36
37
       procedure P2; (* REQUEST(j,n) is received; P2 is indivisible *)
38
39
         RN[j] := \max(RN[j], n);
40
         if Have \text{TRIVILEGE} and not Requesting and (RN[j] = LN[j] + 1) then
41
42
           Have \text{TRIVILEGE} := false;
43
           Send PRIVILEGE(Q, LN) to node j
44
         end
45
46
      end,
```

Listing 1.1: Suzuki and Kazami's algorithm