```
*************************************
;* FUNCTION NAME: FIR
    Regs Modified : A0,A3,A4,A5,B0,B4,B5,B6,B7,SP,B31
Regs Used : A0,A3,A4,A5,B0,B3,B4,B5,B6,B7,SP,B31
    Local Frame Size : 0 Args + 24 Auto + 0 Save = 24 byte
FIR:
           ______
     .dwcfi cfa offset, 0
      .dwcfi save reg to reg, 228, 19
               .D2 SP,24,SP
                                      ; |136|
      .dwcfi cfa_offset, 24
$C$DW$46
           .dwtag DW_TAG_variable, DW_AT_name("index")
      .dwattr $C$DW$46, DW_AT_TI_symbol_name("_index")
      .dwattr $C$DW$46, DW AT type(*$C$DW$T$42)
      .dwattr $C$DW$46, DW AT location[DW OP breg31 4]
$C$DW$47
           .dwtag DW TAG variable, DW AT name("xBuff")
      .dwattr $C$DW$47, DW_AT_TI_symbol_name("_xBuff")
      .dwattr $C$DW$47, DW AT type(*$C$DW$T$45)
      .dwattr $C$DW$47, DW_AT_location[DW_OP_breg31 8]
           .dwtag DW_TAG_variable, DW_AT_name("yVal")
$C$DW$48
      .dwattr $C$DW$48, DW AT TI symbol name(" yVal")
      .dwattr $C$DW$48, DW AT type(*$C$DW$T$17)
      .dwattr $C$DW$48, DW_AT_location[DW_OP_breg31 16]
           .dwtag DW_TAG_variable, DW_AT_name("i")
$C$DW$49
      .dwattr $C$DW$49, DW AT TI symbol name(" i")
      .dwattr $C$DW$49, DW_AT_type(*$C$DW$T$10)
      .dwattr $C$DW$49, DW AT location[DW OP breg31 24]
         MV
                        B4,A3
                                      ; |136|
                 .L1X
                                     ; |136|
\prod
                 .D2T1 A4,*+SP(4)
         STB
         STW
                .D2T1 A3,*+SP(8)
                                   ; |136|
      .dwpsn file "../lab_files/lab2.c",line 137,column 12,is_stmt
               .L1 A5:A4 ; |137|
         ZERO
                .D2T1
         STDW
                      A5:A4,*+SP(16)
                                      ; |137|
      .dwpsn file "../lab_files/lab2.c",line 139,column 9,is_stmt
                                      ; |139|
         ZERO
                 .L2 B4
                .D2T2 B4,*+SP(24)
                                       ; |139|
         STW
      .dwpsn file "../lab_files/lab2.c",line 139,column 14,is_stmt
                 .S2 32,B5
                                 ; |139|
         MVK
                                       ; |139|
                        B4,B5,B0
         CMPLT
                 .L2
                       $C$L11,5
                                      ; |139|
  [!B0]
         BNOP
              .51
         ; BRANCHCC OCCURS {$C$L11} ; |139|
    SOFTWARE PIPELINE INFORMATION
      Disqualified loop: Software pipelining disabled
$C$L10:
$C$DW$L$_FIR$2$B:
      .dwpsn file "../lab_files/lab2.c",line 141,column 9,is_stmt
                .D2T2 *+SP(4),B5 ; |141|
          LDB
                 .D2T2
                        *+SP(8),B6
          LDW
                                        ; |141|
         MVKL
                .S2 filterCoeff,B31
```

```
_filterCoeff,B31
*+B31[B4],B4
          MVKH
                  .S2
          LDW
                  .D2T2
                                          ; |141|
          NOP
                         *+B6[B5],B6
          LDH
                  .D2T2
                                          ; |141|
          NOP
                  .L2
                         B6,B5
                                          ; |141|
          INTSP
                         *+SP(16),B7:B6
          LDDW
                  .D2T2
                                         ; |141|
          NOP
                         B4,B5,B4
          MPYSP
                  .M2
                                         ; |141|
          NOP
                         3
          SPDP
                  .52
                         B4,B5:B4
                                        ; |141|
          NOP
                         B5:B4,B7:B6,B5:B4 ; |141|
          ADDDP
                  .L2
          NOP
                         B5:B4,*+SP(16) ; |141|
          STDW
                  .D2T2
      .dwpsn file "../lab_files/lab2.c",line 139,column 29,is_stmt
                  .D2T2
                         *+SP(24),B5 ; |139|
          LDW
          LDB
                  .D2T2
                         *+SP(4),B4
                                         ; |139|
          NOP
                  .L2
          ADD
                         1,B5,B4
                                          ; |139|
SUB
                  .S2
                         B4,1,B5
                                         ; |139|
                                      ; |139|
                         B4,*+SP(24)
          STW
                  .D2T2
\prod
                                          ; |139|
          EXTU
                  .S2
                         B5,27,27,B4
          STB
                 .D2T2
                         B4,*+SP(4)
                                        ; |139|
      .dwpsn file "../lab_files/lab2.c",line 139,column 14,is_stmt
          LDW
                  .D2T2
                         *+SP(24),B4 ; |139|
                                         ; |139|
          MVK
                  .S1
                         32,A3
          NOP
                         3
          CMPLT
                         B4,A3,A0
                                        ; |139|
                  .L1X
                                         ; |139|
                         $C$L10,5
  [ A0]
          BNOP
                  .S1
                                        ; |139|
          ; BRANCHCC OCCURS {$C$L10}
$C$DW$L$_FIR$2$E:
** -----
                   *
$C$L11:
      .dwpsn file "../lab_files/lab2.c",line 143,column 5,is_stmt
          LDDW
                  .D2T1
                        *+SP(16),A5:A4 ; |143|
          NOP
                         4
          DPSP
                         A5:A4,A4
                                         ; |143|
                 .L1
          NOP
      .dwpsn file "../lab_files/lab2.c",line 144,column 1,is_stmt
                 .S2
          ADDK
                         24,SP
                                         ; |144|
      .dwcfi cfa_offset, 0
      .dwcfi cfa_offset, 0
$C$DW$50
            .dwtag DW TAG TI branch
      .dwattr $C$DW$50, DW_AT_low_pc(0x00)
      .dwattr $C$DW$50, DW_AT_TI_return
                                          ; |144|
          RETNOP .S2
                         B3,5
          ; BRANCH OCCURS {B3}
                                          ; |144|
```

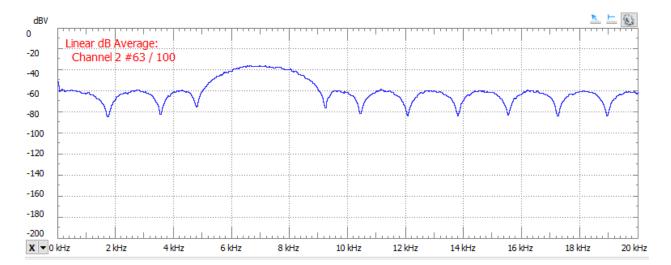


Figure 3 Ladder FIR FFT



Figure 4 Lattice FIR FFT

Questions

- 1. What are the memory requirements and computational requirements of the two implementations in number of locations and number of computations?
- 2. How do the two frequency responses compare?
- 3. Which do you think is the "best" implementation. Why?
- 4. Look at the assembly code. Do you think you could optimize it to run more efficiently? How?

4. Design a lattice filter using reflection coefficients computed from the filter weights from procedure 1. The computation of the reflection coefficients can be done in a program on the host (do not use Matlab to compute the reflection coefficients) or in the 'C67 program itself. You can check that the coefficients are correct by comparing your computed values to coefficients computed by Matlab. Implement the lattice filter on the 'C67. (Note that a circular buffer is not needed for the lattice filter.) Print the frequency response.

Remember that the derivation of the lattice filter in your book assumes the first filter coefficient, α_0 , is 1. Since your filter (probably) does not meet this condition, you can add 1.0 to the beginning of the filter coefficients, and subtract the input from the output and produce a 1-sample delayed FIR output.

Again, save and print out the assembly code created by the compiler.

```
int main(void)
    int16 t xBuff[FILTER SIZE] = { 0 };
    int16 t newVal=0;
    float gDelayed[FILTER_SIZE] = {0};
    float g[FILTER SIZE]={0};
      int8_t xIndex = 0x0;
    float yVal = 0;
                                // Sine Sample
    float k,gDel;
    int16 t temp;
    int8_t i=0;
#ifdef LATTICE
    float alpha[FILTER_SIZE], beta[FILTER_SIZE];
    float kVal[FILTER_SIZE] = { 0 };
    memcpy(alpha,filterCoeff,sizeof(filterCoeff));
    ComputeK(alpha, FILTER SIZE-1,beta, kVal);
#endif
    Init();
    // Infinite loop: Each loop reads/writes one sample to the left and right
channels.
    while (1)
        //write out v value
#ifndef LATTICE
        xBuff[x0Index] = newVal;
        yVal = FIR(x0Index, xBuff);
        x0Index = (x0Index + 0x1) & 0x1F;
#else
        g[0]=newVal;
        yVal=newVal;
//
        for(i=1; i<FILTER_SIZE/2; i++){</pre>
```

```
k=kVal[i];
            gDel=gDelayed[i-1];
            g[i]=k*yVal+gDel;
            yVal=yVal+k*gDel;
        }
#endif
       while (!CHKBIT(MCASP->SRCTL12, RRDY)) { }
           temp = MCASP->RBUF12; // read next value from left channel
       while (!CHKBIT(MCASP->SRCTL11, XRDY)) { }
           MCASP -> XBUF11 = 0;
           // write output to left channel
#ifdef LATTICE
           for(i=FILTER_SIZE/2; i<FILTER_SIZE; i++){</pre>
                        k=kVal[i];
                        gDel=gDelayed[i-1];
                        g[i]=k*yVal+gDel;
                       yVal=yVal+k*gDel;
           memcpy(gDelayed,g,sizeof(g));
           yVal=yVal-newVal;
#endif
       while (!CHKBIT(MCASP->SRCTL11, XRDY)) { }
         MCASP->XBUF11 = (int16 t) yVal; //(int16 t) xBuff[xIndex];
       while (!CHKBIT(MCASP->SRCTL12, RRDY)) { }
           newVal=MCASP->RBUF12;
                // write 0 to right channel
    }
}
void ComputeK(float *alpha, int m, float *beta, float *k)
{
    int i;
    k[m] = alpha[m];
    if (m == 0)
    {
        return;
    //reverse coeff for beta
    for(i=0;i<=m;i++){</pre>
        beta[i]=alpha[m-i];
    }
     beta=memcpy(&beta[0],&alpha[0],FILTER SIZE*sizeof(float));
    //computes z^i coefficients of A_(m-1)
    for (i = 0; i < m; i++)</pre>
    {
        alpha[i] = (alpha[i] - k[m] * beta[i])
                / (1 - k[m] * k[m]);
    //compute K (m-1)
    ComputeK(alpha, m - 1,beta,k);
}
```