



鑫清显科技

Clear-victory

C320240-7

用 户 手 册

深圳市鑫清显科技有限公司

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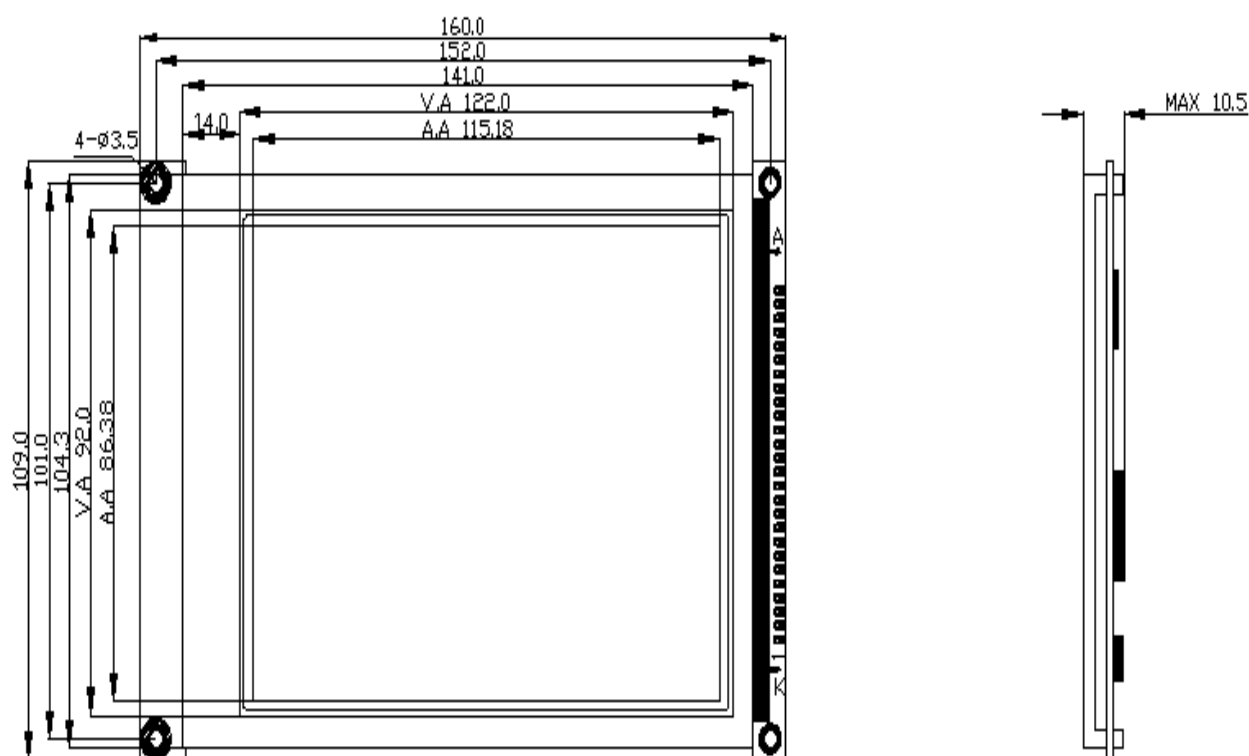
一、概述

C320240-7 是一种图形点阵液晶显示器,它由控制器、行驱动器/列驱动器及 320×240 全点阵液晶显示器组成.可完成图形显示,也可以显示汉字.

主要技术参数和性能:

1. 显示类型: STN, 负显;
2. 显示内容: 240(行)×320(列) 点;
3. 驱动方式: 1/240 DUTY, 1/17BIAS;
4. 电源 VDD: +5V;
5. 背光: LED white;
6. 视角: 6 O'CLOCK;

二、外形尺寸图

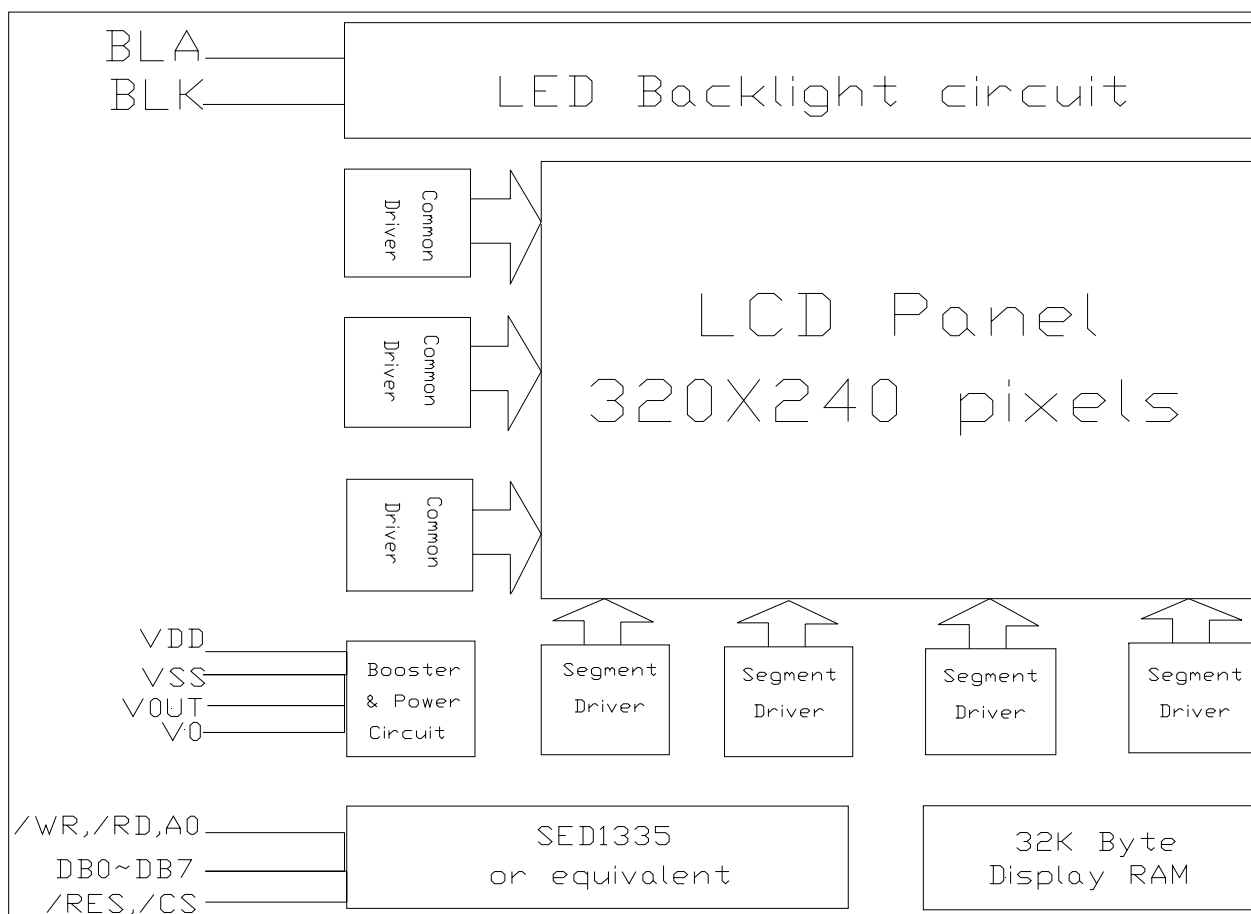


1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26
VSS	VDD	V0	A0	WR	RD	D0	D1	D2	D3	D4	D5	D6	D7	CS	REST	VEE	A	DCLK	C/S	DIN	DOUT	PEN	BUSY	IN3	IN4

三、模块的外部接口

Pin no	Symbol	Level	Function
1	VSS		模块/背光电源负极
2	VDD		模块/背光电源正极。+5V
3	V0		LCD 对比度调节，取 VEE 电压
4	A0	I	数据指令线
5	WR	I	写信号
6	RD	I	读信号
7	D0	I/O	数据线
8	D1	I/O	数据线
9	D2	I/O	数据线
10	D3	I/O	数据线
11	D4	I/O	数据线
12	D5	I/O	数据线
13	D6	I/O	数据线
14	D7	I/O	数据线
15	CS	I	片选控制
16	REST	I	复位信号
17	VEE		LCD 电压输出或者输入（默认输出）用于调节对比度
18	A		空脚
19	DCLK	I	触屏 IC7843 时钟信号
20	C/S	I	触屏 IC 片选
21	DIN	I	触屏 IC 指令输入
22	DOUT	O	触屏 IC 数据输出
23	PENIRQ	I	保留，有 100k 上拉
24	BUSY	O	触屏 IC 忙标识
25	IN3		预留中断
26	IN4		预留中断

四、模块逻辑框图



五、电气参数

1. ELECTRICAL CHARACTERISTIC:

Item	Symbol	Standard value			Unit
		Min	Typ	Max	
Power supply for logic	VDD	-0.3	—	7.0	V
Power supply for LCD	VEE	VDD-28	—	VDD+0.3	V
Input voltage	Vin	-0.3	—	VDD+0.3	V
Operating temperature	Top	-20	—	75	°C
Storage temperature	Tstg	-65	—	150	°C

DC Electrical Characteristics(1) ($V_{SS}=0V$, $V_{DD}=4.5$ to $5.5V$, $T_a=-20^{\circ}C$ to $+75^{\circ}C$)

Characteristic	Symbo l	Condition	Min	Typ	Max	Unit
Operating Voltage(1)	V_{DD}		4.5	5.0	5.5	
Register data retention voltage	V_{OH}		2.0	-	6.0	
High level input voltage	V_{IHT}	-	$0.5V_{DD}$	-	V_{DD}	
T Low level input voltage	V_{ILT}	-	V_{SS}	-	$0.2V_{DD}$	
T High level output voltage	V_{OHT}	$I_{OH}=-5.0mA$	2.4	-	-	
L Low level output voltage	V_{OLT}	$I_{OL}=5.0mA$	-	-	$V_{SS}+0.4$	
High level input voltage	V_{IHC}		$0.8V_{DD}$	-	V_{DD}	V
C Low level input voltage	V_{ILC}		V_{SS}	-	$0.2V_{DD}$	
M High level output voltage	V_{OHC}	$I_{OH}=-2.0mA$	$V_{DD}-0.4$	-	-	
S Low level output voltage	V_{OLC}	$I_{OL}=2.0mA$	-	-	$V_{SS}+0.4$	
S Positive trigger threshold voltage	V_{T+}		$0.5V_{DD}$	$0.7V_{DD}$	$0.8V_{DD}$	
M Negative trigger threshold voltage	V_{T-}		$0.2V_{DD}$	$0.3V_{DD}$	$0.5V_{DD}$	
Input leakage current	I_{LI}	$V_{IN}=V_{DD}/V_{SS}$	-	0.05	2.0	μA
Output leakage current	I_{LO}		-	0.10	5.0	
Average operating current	I_{OPR}	$f_{OSC}=10MHz, No-load 256X200dot$	-	11	15	mA
Standby current	I_Q	Sleep $X_G, /CS, /RD=V_{DD}$	-	0.05	20	μA
Oscillation frequency	f_{OSC}	AT X' tal	1.0	-	10.0	MHz
External clock frequency	f_{CL}	Duty 47.5%	1.0	-	10.0	
Feed back resistance	R_f		0.5	1.0	3.0	M Ω

DC Electrical Characteristics(2) ($V_{SS}=0V$, $V_{DD}=2.7$ to $4.5V$, $T_a=-20^{\circ}C$ to $+75^{\circ}C$)

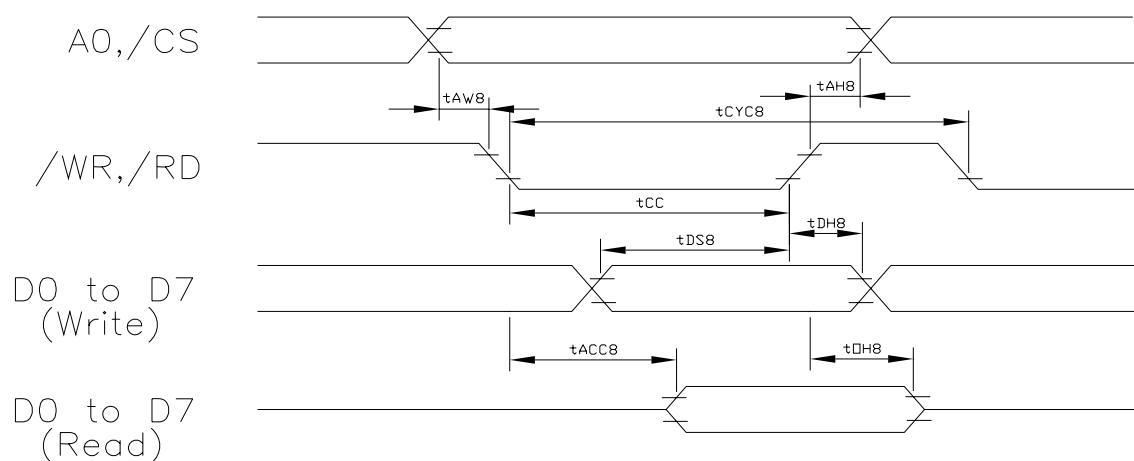
Characteristic	Symb ol	Condition	Min	Typ	Max	Unit
Operating Voltage	V_{DD}		2.7	3.5	4.5	
Register data retention voltage	V_{OH}		2.0	-	6.0	V

T T L	High level input voltage	V_{IHT}	-	$0.8V_{DD}$	-	V_{DD}	
	Low level input voltage	V_{ILT}	-	V_{SS}	-	$0.2V_{DD}$	
	High level output voltage	V_{OHT}	$I_{OH} = -3.0mA$	$V_{DD}-0.4$	-	-	
	Low level output voltage	V_{OLT}	$I_{OL} = 3.0mA$	-	-	$V_{SS}+0.4$	
C M O S	High level input voltage	V_{IHC}		$0.8V_{DD}$	-	V_{DD}	
	Low level input voltage	V_{ILC}		V_{SS}	-	$0.2V_{DD}$	
	High level output voltage	V_{OHC}	$I_{OH} = -1.0mA$	$V_{DD}-0.4$	-	-	
	Low level output voltage	V_{OLC}	$I_{OL} = 1.0mA$	-	-	$V_{SS}+0.4$	
S C H M I T T	Positive trigger threshold voltage	V_{T+}		$0.5V_{DD}$	$0.7V_{DD}$	$0.8V_{DD}$	
	Negative trigger threshold voltage	V_{T-}		$0.2V_{DD}$	$0.3V_{DD}$	$0.5V_{DD}$	
	Input leakage current	I_{LI}	$V_{IN} = V_{DD}/V_{SS}$	-	0.05	2.0	μA
	Output leakage current	I_{LO}		-	0.10	5.0	μA
	Average operating current	I_{OPR}	$f_{OSC} = 6.1MHz, N$ o-load 256X200dot	-	3.5 ($V_{DD} = 3.5V$)	7.0	mA
	Standby current	I_Q	Sleep $X_G = CS = RD = V_D$	-	0.05	20	μA
	Oscillation frequency	f_{OSC}	AT X' tal	1.0	-	8.0	MHz
	External clock frequency	f_{CL}	Duty 47.5%	1.0	-	8.0	MHz
	Feed back resistance	R_f		0.7	-	4.0	MΩ

六、SED1335 时序图

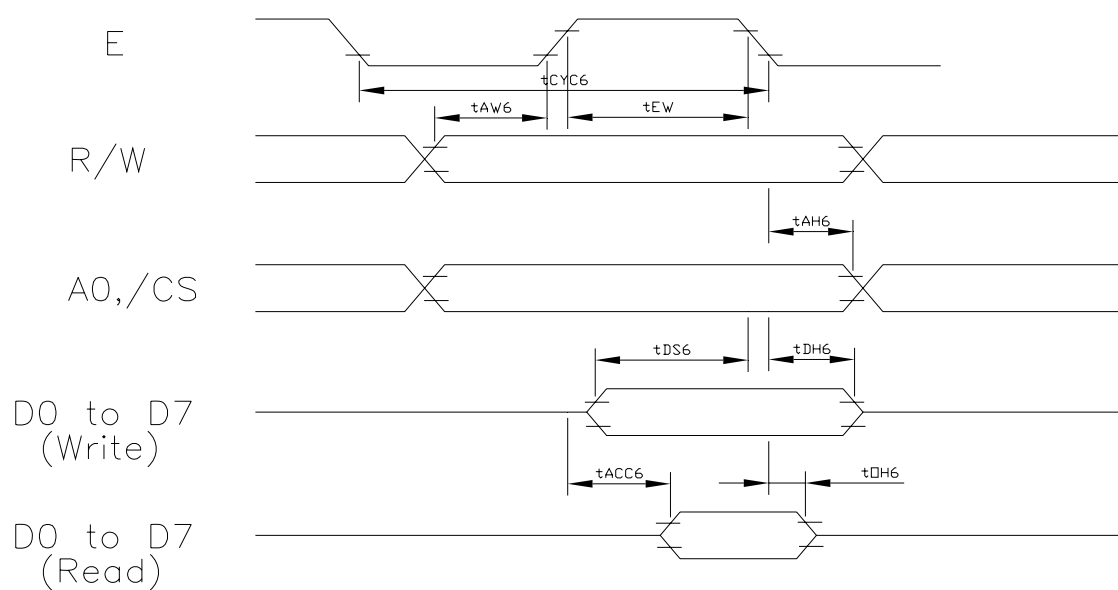
1. 8080 family interface timing

Signal	Symbol	Parameter	VDD=4.5~5.5V		VDD=2.7~4.5V		Unit
			min	max	min	max	
A0,CS	tAH8	Address hold time	10	-	10	-	ns
	tAW8	Address setup time	0	-	0	-	ns
/WR,/RD	tCYC8	System cycle time	see not	-	see not	-	ns
	tCC	Strobe pulsewidth	120	-	150	-	ns
D0 to D7	tDS8	Data setup time	120	-	120	-	ns
	tDH8	Data hold time	5	-	5	-	ns
	tACC8	/RD access time	-	50	-	80	ns
	tOH8	Output disable time	10	50	10	55	ns



2. 6800 family interface timing

Signal	Symbol	Parameter	VDD=4.5~5.5V		VDD=2.7~4.5V		Unit
			min	max	min	max	
A0,/CS R/W	t_{CYC6}	System cycle time	see not	-	see not	-	ns
	t_{AW6}	Address setup time	0	-	0	-	ns
	t_{AH6}	Address hold time	0	-	0	-	ns
D0 to D7	t_{DS6}	Data setup time	100	-	120	-	ns
	t_{DH6}	Data hold time	0	-	0	-	ns
	t_{AH6}	Output disable time	10	50	10	75	ns
	t_{ACC6}	Access time	-	85	-	130	ns
E	t_{EW}	Enable pulsewidth	120	-	150	-	ns







LCALL DELAY

LJMP L0100

ORG 0100H

L0100: CLR CS

SETB R_D

MOV P1, #00H

SETB RST

NOP

NOP

NOP

CLR RST

LCALL DELAY

LCALL DELAY

SETB RST

MOV A, #40H ;系统设置

LCALL S02E5

MOV A, #30H ;正常运行 无边界 单屏 8X8 字

体 内部字符

LCALL S0305

MOV A, #87H ;B 型驱动 字符体宽度为 8

LCALL S0305

MOV A, #87H ;字符体高度为 8

LCALL S0305

MOV A, #27H ;每行字符数

LCALL S0305

MOV A, #2BH ;驱动频率常数 43

LCALL S0305

MOV A, #0EFH ;240 行

LCALL S0305

MOV A, #28H ;41 位字节

LCALL S0305

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MOV    A,    #00H
LCALL  S0305

MOV    A,    #44H    ;显示区域设置
LCALL  S02E5

MOV    A,    #00H    ;第一区首地址为 0000H
LCALL  S0305

MOV    A,    #00H
LCALL  S0305

MOV    A,    #0EFH    ;第一区所控制的行数 240
LCALL  S0305

MOV    A,    #0B0H    ;第二区首地址为 04B0H
LCALL  S0305

MOV    A,    #04H
LCALL  S0305

MOV    A,    #0EFH    ;第二区所控制的行数 240
LCALL  S0305

MOV    A,    #30H    ;第三区首地址为 2A30H
LCALL  S0305

MOV    A,    #2AH
LCALL  S0305

MOV    A,    #00H    ;第四区首地址为 3000H
LCALL  S0305

MOV    A,    #30H
LCALL  S0305

MOV    A,    #5AH    ;点位移设置
LCALL  S02E5

MOV    A,    #00H
LCALL  S0305

MOV    A,    #5BH    ;显示合成方式
LCALL  S02E5

```



三区文本	MOV	A,	#04H	;二重合成 逻辑或 一区图形
	LCALL	S0305		
	MOV	A,	#58H	;显示状态设置
	LCALL	S02E5		
	MOV	A,	#04H	;光标关闭 第一区显示
	LCALL	S0305		
	MOV	A,	#59H	;开显示
	LCALL	S02E5		
	LCALL	D_TEXT		;显示图形
	MOV	A,	#46H	;光标指针设置
	LCALL	S02E5		
	MOV	A,	#0B0H	;地址为 04B0H
	LCALL	S0305		
	MOV	A,	#04H	
	LCALL	S0305		
	LCALL	DELAY		
	LCALL	DELAY		
	LCALL	DELAY		
;	LJMP	L0100		
D_TEXT:	MOV	A,	#46H	;光标指针设置
	LCALL	S02E5		
	MOV	A,	#00H	
	LCALL	S0305		
	MOV	A,	#00H	



```
LCALL    S0305

MOV      A,      #4CH    ;光标移动方向设置
LCALL    S02E5
MOV      DPTR,   #ADATA
MOV      A,      #42H    ;数据写入设置
LCALL    S02E5
MOV      R5,     #1EH

TEST1:   MOV      R3,     #08H
TEST2:   MOV      R4,     #28H
TEST3:   MOV      A,     #00H

MOVC     A, @A+DPTR
LCALL    S0305
INC      DPTR
DJNZ     R4,     TEST3
DJNZ     R3,     TEST2
DJNZ     R5,     TEST1
RET

S02E5:   MOV      P1,     A      ;写指令
SETB     A0
CLR      R_W
NOP
SETB     R_W
RET

S0305:   MOV      P1,     A      ;写数据或参数
CLR      A0
CLR      R_W
NOP
SETB     R_W
```



RET

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DELAY:  MOV    R3,    #01H
TM2:    MOV    R4,    #0FFH
TM1:    MOV    R6,    #0FFH
TM3:    DJNZ   R6,    TM3
        DJNZ   R4,    TM1
        DJNZ   R3,    TM2
        RET
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ADATA:

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000H,000H,000H,000H,

DB



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DB

007H,003H,007H,003H,007H,003H,007H,003H,007H,003H,007H,003H,007H,003H,007H,0AFH,

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DB

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DB

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DB

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DB

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DB

000H,000H,000H,000H,000H,000H,000H,000H,000H,000H,000H,000H,000H,000H,000H,

DB



000H,000H,000H,000H,000H,000H,000H,000H,000H,000H,000H,
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H,0E0H,0E0H,0F8H,0FEH,

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H,000H,000H,000H,000H,

DB

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003H,003H,001H,000H,

DB

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H,03CH,000H,000H,000H,

DB

000H,000H,000H,000H,000H,000H,000H,000H,000H,000H,000H,000H,
000H,000H,000H,000H,

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DB



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H,000H,000H,000H,003H,

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000H,000H,000H,000H,

DB

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DB

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DB

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003H,001H,003H,001H,

DB



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DB

000H,000H,000H,000H,000H,000H,0AEH,0FFH,0FFH,0FFH,0FFH,007
H,001H,003H,003H,001H,

DB

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DB

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DB



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DB

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000H,000H,000H,080H,

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000H,000H,080H,080H,

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H,000H,000H,000H,000H,

DB

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000H,000H,000H,000H,

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000H,000H,000H,000H,000H,000H,000H,000H,000H,000H,000H,
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DB

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H,000H,080H,080H,0E0H,

DB



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000H,000H,000H,000H,

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000H,000H,00FH,007H,

DB

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DB

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H,000H,000H,000H,000H,

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000H,000H,000H,000H,000H,000H,000H,000H,000H,000H,000H,000H,
003H,00FH,007H,00FH,

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H,000H,002H,007H,007H,

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001H,003H,00FH,03FH,03FH,0FCH,0F8H,0FEH,0FFH,0FFH,0FFH,0FF
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DB



03CH,07EH,03FH,03FH,007H,007H,001H,000H,000H,0FAH,0FFH,0FFH,0FFH,0FFH,0FDH,0FCH,

DB

0F4H,000H,000H,000H,000H,000H,000H,000H,000H,000H,000H,000H,000H,000H,000H,

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000H,000H,000H,000H,000H,000H,000H,003H,003H,007H,003H,007H,0AFH,0FFH,0FFH,0FFH,

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DB

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003H,007H,001H,000H,000H,020H,0FFH,0FFH,0FFH,0FFH,05FH,000H,000H,000H,0C0H,0E0H,

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000H,000H,000H,000H,

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H,000H,000H,000H,000H,

DB



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DB

003H,001H,00AH,03CH,01EH,03CH,01EH,0FCH,0FCH,0FEH,0FFH,0FF
H,0FFH,0FFH,07DH,000H,

DB

000H,000H,000H,000H,000H,000H,00EH,03FH,01FH,03FH,01FH,004H,
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000H,000H,000H,000H,

DB

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DB

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H,000H,000H,000H,000H,

DB

000H,000H,000H,000H,000H,000H,000H,000H,000H,000H,000H,
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DB

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001H,003H,001H,001H,

DB



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000H,000H,000H,000H,

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000H,000H,000H,000H,080H,0E1H,0E1H,0E1H,0C1H,000H,000H,000
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