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# DS-011 Pixhawk Revision v5X Standard

Revision: 0.3.0

Revision date: Apr 14, 2020

#### Abstract

This document is the formal version of the Pixhawk industry standard that includes all aspects of the hardware standard required to build compatible autopilots.



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# **Document Revisions**

Revision	Editor	Reviewer	Comments
0.1.0	Lorenz Meier	David Sidrane	Initial specification
0.2.0	Lorenz Meier	David Sidrane	Addition of FMUv6X draft
0.3.0	Lorenz Meier	David Sidrane	Split up into focused documents

## **Contact and Public Developer Call**

This standard is being developed on a <u>public developer call</u>. For further questions, please contact the maintainer of the standard, <u>lorenz@px4.io</u>.

#### **Trademark Guideline**

Pixhawk is a registered trademark and is used to mark and protect the consistent use of this standard. The requirements for this are covered in this document: Trademark Guideline

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## Flight Management Unit Standards

• FMUv1: No product name (2012, 168 MHz M4)

• FMUv2: Pixhawk 1 (2013, 168 MHz M4)

• FMUv3: Pixhawk 2 (2015, 168 MHz M4, redundant sensors)

• FMUv4: Pixracer (2015, 168 MHz M4)

• FMUv4X: Pixhawk 3 Pro (2017, 168 MHz M4, redundant sensors)

FMUv5: Pixhawk 4 (2018, 200 MHz M7)

• FMUv5X: Pixhawk 5X (2019, 200 MHz M7, temp-calibrated, redund. sensors)

• FMUv6: Pixhawk 6 (2019, 400-600 MHz H7)

• FMUv6X: Pixhawk 6X (2020, 400-600 MHz H7, calibrated, redund. sensors)

#### Interface Standards

Pixhawk connector standards v2 (2015-)

o Connector: JST GH

o Pinout: Pixhawk connector pinout

• Pixhawk Autopilot Bus (PAB)

Connector: 100-pos Hirose DF40

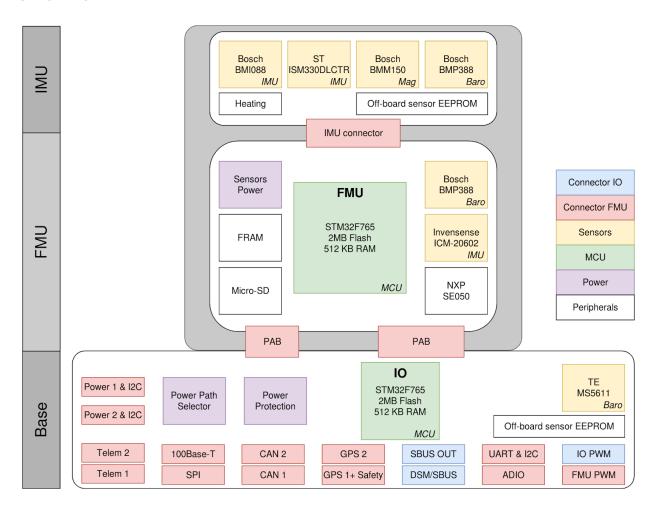
o Connector: 50-pos Hirose DF40

## **Pixhawk Autopilot Form Factor**

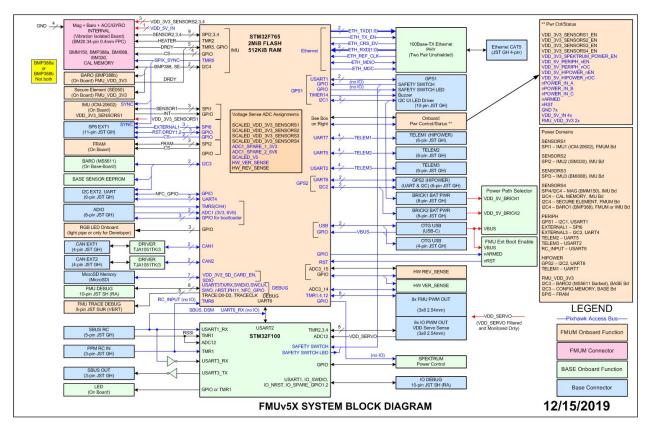
This processor pinout has to be used in conjunction with the <u>Pixhawk Autopilot Bus</u> Standard.

## FMUv5X Summary

#### Overview



## **Detailed Block Diagram**



The FMUv5X generation brings the proven features from FMUv5 to a hardened form factor.

- Secure element for secure authentication of the drone (SE050, I2C4)
- Ethernet interface for high-speed mission computer integration
- Three redundancy domains: Completely isolated sensor domains with separate buses and separate power control.
- Redundant sensors on separate buses, allowing continuous operation while losing a complete redundancy domain.
  - Bosch BMI088 accelerometer (SPI4, redundancy domain #1, vibration isolated)
  - o Invensense ICM-20602 (SPI1, redundancy domain #2)
  - ST Micro ISM330 (SPI5, redundancy domain #3, vibration isolated)
  - Bosch BMM150 compass (I2C4, redundancy domain #1, vibration isolated)
  - Bosch BMP388 pressure sensor (I2C4, redundancy domain #1)
  - GPS external mag + baro #1 (I2C1, redundancy domain #2)
  - GPS external mag + baro #2 (I2C2, redundancy domain #3)
  - High accuracy barbed baro (I2C1, redundancy domain #2)
  - Calibration EEPROM for baseboard sensors (I2C1)
  - On-IMU calibration EEPROM memory for high-accuracy sensors (I2C4)
- Automated sensor calibration eliminating varying signals and temperature
- Operating temperature -40 to +85°C
- FRAM memory for configuration data (SPI2)

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- Extensive power monitoring
  - o Two smart batteries on SMBus or more on UAVCAN
  - 5V rail monitoring
  - o 3.3V rail monitoring for CPU
  - o 3.3V rail monitoring for each sensor domain
- External sensor bus (SPI5)
- Temperature calibration: Every board is calibrated for temperature from -25 to +85 degrees
- Redundant power supply: The autopilot can be powered from up to three power sources and every sensor set is powered by an independent LDO with independent power control
- Battery-backed real time clock for running security applications without GPS coverage
- For NFC one external I2C port needs to have an additional GPIO line and 5V to supply the external NFC reader.

#### Full FMUv5X Pinout

The official pinout is covered in this pinout sheet.

0	PA	0	ADC1_IN0	Α	SCALED_VDD_3V3_SENSORS1
1	PA	1	ETH_REF_CLK	Ε	ETH_REF_CLK
2	PA	2	ETH_MDIO	Ε	ETH_MDIO
3	PA	3	USART2_RX	U	USART2_RX_TELEM3
4	PA	4	ADC1_IN4	Α	SCALED_VDD_3V3_SENSORS2
5	PA	5	SPI1_SCK	S	SPI1_SCK_SENSOR1_ICM20602
6	PA	6	SPI6_MISO	S	SPI6_MISO_EXTERNAL1
7	PA	7	ETH_CRS_DV	Ε	ETH_CRS_DV
8	PA	8	TIM1_CH1	Т	FMU_CH4
9	PA	9	USB_OTG_FS_VBUS	В	VBUS
10	PA	10	TIM1_CH3	Т	FMU_CH2
11	PA	11	USB_OTG_FS_DM	В	USB_D_N
12	PA	12	USB_OTG_FS_DP	В	USB_D_P
13	PA	13	SWDIO	D	FMU_SWDIO
14	PA	14	SWCLK	D	FMU_SWCLK
15	PA	15	PA15	G	SPI6_nCS2_EXTERNAL1
16	РВ	0	ADC1_IN8	Α	SCALED_VDD_3V3_SENSORS3
17	РВ	1	ADC1_IN9	Α	SCALED_V5
18	РВ	2	SPI3_MOSI	S	SPI3_MOSI_SENSOR3_BMI088
19	РВ	3	SPI6_SCK	S	SPI6_SCK_EXTERNAL1
20	РВ	4	SPI1_MISO	S	SPI1_MISO_SENSOR1_ICM20602
21	РВ	5	SPI1_MOSI	S	SPI1_MOSI_SENSOR1_ICM20602
22	РВ	6	CAN2_TX	С	CAN2_TX
23	РВ	7	I2C1_SDA	Ι	I2C1_SDA_BASE_GPS1_MAG_LED_PM1
24	РВ	8	I2C1_SCL	Ι	I2C1_SCL_BASE_GPS1_MAG_LED_PM1

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25  PB 9  UART5_TX	
27       PB       11       ETH_TX_EN       E       ETH_TX_EN         28       PB       12       CAN2_RX       C       CAN2_RX         29       PB       13       ETH_TXD1       E       ETH_TXD1         30       PB       14       USART1_TX       U       USART1_TX_GPS1         31       PB       15       USART1_RX       U       USART1_RX_GPS1         32       PC       0       ADC1_IN10       A       ADC1_6V6         33       PC       1       ETH_MDC       E       ETH_MDC         34       PC       2       ADC1_IN12       A       SCALED_VDD_3V3_SENSORS4         35       PC       3       ADC1_IN13       A       ADC1_3V3         36       PC       4       ETH_RXD0       E       ETH_RXD0         37       PC       5       ETH_RXD1       E       ETH_RXD1         38       PC       6       USART6_TX       U       USART6_RX_FROM_IORC_INPUT	
28       PB       12       CAN2_RX       C       CAN2_RX         29       PB       13       ETH_TXD1       E       ETH_TXD1         30       PB       14       USART1_TX       U       USART1_TX_GPS1         31       PB       15       USART1_RX       U       USART1_RX_GPS1         32       PC       0       ADC1_IN10       A       ADC1_6V6         33       PC       1       ETH_MDC       E       ETH_MDC         34       PC       2       ADC1_IN12       A       SCALED_VDD_3V3_SENSORS4         35       PC       3       ADC1_IN13       A       ADC1_3V3         36       PC       4       ETH_RXD0       E       ETH_RXD0         37       PC       5       ETH_RXD1       E       ETH_RXD1         38       PC       6       USART6_TX       U       USART6_RX_FROM_IORC_INPUT	
29       PB       13       ETH_TXD1       E       ETH_TXD1         30       PB       14       USART1_TX       U       USART1_TX_GPS1         31       PB       15       USART1_RX       U       USART1_RX_GPS1         32       PC       0       ADC1_IN10       A       ADC1_6V6         33       PC       1       ETH_MDC       E       ETH_MDC         34       PC       2       ADC1_IN12       A       SCALED_VDD_3V3_SENSORS4         35       PC       3       ADC1_IN13       A       ADC1_3V3         36       PC       4       ETH_RXD0       E       ETH_RXD0         37       PC       5       ETH_RXD1       E       ETH_RXD1         38       PC       6       USART6_TX       U       USART6_RX_FROM_IORC_INPUT	
31       PB       15       USART1_RX       U USART1_RX_GPS1         32       PC       0       ADC1_IN10       A ADC1_6V6         33       PC       1       ETH_MDC       E ETH_MDC         34       PC       2       ADC1_IN12       A SCALED_VDD_3V3_SENSORS4         35       PC       3       ADC1_IN13       A ADC1_3V3         36       PC       4       ETH_RXD0       E ETH_RXD0         37       PC       5       ETH_RXD1       E ETH_RXD1         38       PC       6       USART6_TX       U USART6_TX_TO_IONC         39       PC       7       USART6_RX       U USART6_RX_FROM_IORC_INPUT	
32       PC 0       ADC1_IN10       A ADC1_6V6         33       PC 1       ETH_MDC       E ETH_MDC         34       PC 2       ADC1_IN12       A SCALED_VDD_3V3_SENSORS4         35       PC 3       ADC1_IN13       A ADC1_3V3         36       PC 4       ETH_RXD0       E ETH_RXD0         37       PC 5       ETH_RXD1       E ETH_RXD1         38       PC 6       USART6_TX       U USART6_TX_TO_IONC         39       PC 7       USART6_RX       U USART6_RX_FROM_IORC_INPUT	
33 PC 1 ETH_MDC	
34       PC 2       ADC1_IN12       A SCALED_VDD_3V3_SENSORS4         35       PC 3       ADC1_IN13       A ADC1_3V3         36       PC 4       ETH_RXD0       E ETH_RXD0         37       PC 5       ETH_RXD1       E ETH_RXD1         38       PC 6       USART6_TX       U USART6_TX_T0_I0NC         39       PC 7       USART6_RX       U USART6_RX_FROM_I0RC_INPUT	
35 PC 3 ADC1_IN13 A ADC1_3V3  36 PC 4 ETH_RXD0 E ETH_RXD0  37 PC 5 ETH_RXD1 E ETH_RXD1  38 PC 6 USART6_TX U USART6_TX_TO_IONC  39 PC 7 USART6_RX U USART6_RX_FROM_IORC_INPUT	
36 PC 4 ETH_RXD0 E ETH_RXD0  37 PC 5 ETH_RXD1 E ETH_RXD1  38 PC 6 USART6_TX U USART6_TX_TO_IONC  39 PC 7 USART6_RX U USART6_RX_FROM_IORC_INPUT	
37 PC 5 ETH_RXD1 E ETH_RXD1  38 PC 6 USART6_TX U USART6_TX_TO_IONC  39 PC 7 USART6_RX U USART6_RX_FROM_IORC_INPUT	
38 PC 6 USART6_TX U USART6_TX_TO_IONC  39 PC 7 USART6_RX U USART6_RX_FROM_IORC_INPUT	
PC 7 USART6_RX U USART6_RX_FROM_IORC_INPUT	
AO DO O HADTE DTO WE HADTE DTO TELEVO	
40 PC 8 UART5_RTS V UART5_RTS_TELEM2	
41 PC 9 UART5_CTS V UART5_CTS_TELEM2	
42 PC 10 SPI3_SCK S SPI3_SCK_SENSOR3_BMI088	
43 PC 11 SPI3_MISO S SPI3_MISO_SENSOR3_BMI088	
44 PC 12 PC12 G nARMED	
45 PC 13 PC13 G VDD_3V3_SD_CARD_EN	
46 PC 14 OSC32_IN X 32KHZ_IN	
47 PC 15 0SC32_0UT X 32KHZ_0UT	
48 PD 0 CAN1_RX C CAN1_RX	
49 PD 1 CAN1_TX C CAN1_TX	
50 PD 2 UART5_RX V UART5_RX_TELEM2	
51 PD 3 USART2_CTS U USART2_CTS_TELEM3	
52 PD 4 USART2_RTS U USART2_RTS_TELEM3	
53 PD 5 USART2_TX U USART2_TX_TELEM3	
54 PD 6 SDMMC2_CLK SD SDMMC2_CLK	
55 PD 7 SDMMC2_CMD SD SDMMC2_CMD	
56 PD 8 USART3_TX U USART3_TX_DEBUG	
57 PD 9 USART3_RX U USART3_RX_DEBUG	
58 PD 10 PD10 G FMU_nSAFETY_SWITCH_LED_OUT	
59 PD 11 PD11 G SPI6_DRDY1_EXTERNAL1	
60 PD 12 PD12 G SPI6_DRDY2_EXTERNAL1	
61 PD 13 TIM4_CH2 T FMU_CH5	
62 PD 14 TIM4_CH3 T FMU_CH6	
63 PD 15 PD15 G VDD_3V3_SENSORS2_EN	
64 PE 0 UART8_RX V UART8_RX_GPS2	
65 PE 1 UART8_TX V UART8_TX_GPS2	
66 PE 2 PE2 D TRACECLK	
67 PE 3 PE3 G nLED_RED	
68 PE 4 PE4 G nLED_GREEN	

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69	PE	5	PE5	G	nLED_BLUE
70	PE	6	SPI4_MOSI	S	SPI4_MOSI_SENSOR4_BMM150
71	PE	7	PE7	G	VDD_3V3_SENSORS3_EN
72	PE	8	UART7_TX	٧	UART7_TX_TELEM1
73	PE	9	UART7_RTS	٧	UART7_RTS_TELEM1
74	PE	10	UART7_CTS	٧	UART7_CTS_TELEM1
75	PE	11	TIM1_CH2	Т	FMU_CH3
76	PE	12	SPI4_SCK	S	SPI4_SCK_SENSOR4_BMM150
77	PE	13	SPI4_MISO	S	SPI4_MISO_SENSOR4_BMM150
78	PE	14	TIM1_CH4	Т	FMU_CH1
79	PE	15	PE15	G	VDD_5V_PERIPH_nOC
80	PF	0	I2C2_SDA	I	I2C2_SDA_BASE_GPS2_MAG_LED_PM2
81	PF	1	I2C2_SCL	Ι	I2C2_SCL_BASE_GPS2_MAG_LED_PM2
82	PF	2	PF2	G	SPI1_DRDY1_ICM20602
83	PF	3	PF3	G	SPI4_DRDY1_BMM150_DRDY
84	PF	4	ADC3_IN14	Α	HW_VER_SENSE
85	PF	5	ADC3_IN15	Α	HW_REV_SENSE
86	PF	6	UART7_RX	٧	UART7_RX_TELEM1
87	PF	7	SPI5_SCK	S	SPI5_SCK_FRAM
88	PF	8	SPI5_MISO	S	SPI5_MISO_FRAM
89	PF	9	TIM14_CH1	Т	BUZZER_1
90	PF	10	PF10	G	SPI6_nRESET_EXTERNAL1
91	PF	11	SPI5_MOSI	S	SPI5_MOSI_FRAM
92	PF	12	PF12	G	VDD_5V_HIPOWER_nEN
93	PF	13	PF13	G	VDD_5V_HIPOWER_nOC
94	PF	14	I2C4_SCL	I	I2C4_SCL_FMU
95	PF	15	I2C4_SDA	I	I2C4_SDA_FMU
96	PG	0	PG0	G	HW_VER_REV_DRIVE
97	PG	1	PG1	G	nPOWER_IN_A
98	PG	2	PG2	G	nPOWER_IN_B
99	PG	3	PG3	G	nPOWER_IN_C
100	PG	4	PG4	G	VDD_5V_PERIPH_nEN
101	PG	5	PG5	G	I2C4_DRDY1_BMP388
102	PG	6	PG6	G	PG6
103	PG	7	PG7	G	SPI5_nCS1_FRAM
104	PG	8	PG8	G	VDD_3V3_SENSORS4_EN
105	PG	9	SDMMC2_D0	SD	SDMMC2_D0
106	PG	10	SDMMC2_D1	SD	SDMMC2_D1
107	PG	11	SDMMC2_D2	SD	SDMMC2_D2
108	PG	12	SDMMC2_D3	SD	SDMMC2_D3
109	PG	13	ETH_TXD0	Е	ETH_TXD0
110	PG	14	SPI6_MOSI	S	SPI6_MOSI_EXTERNAL1
111	PG	15	PG15	G	ETH_POWER_EN
112	РН	0	OSC_IN	Х	16_MHZ_IN
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113	PH	1	OSC_OUT	Χ	16_MHZ_OUT
114	РН	2	PH2	G	VDD_3V3_SPEKTRUM_POWER_EN
115	РН	3	PH3	G	NFC_GPIO
116	РН	4	PH4	G	FMU_SAFETY_SWITCH_IN
117	РН	5	PH5	G	SPI2_nCS1_ISM330
118	РН	6	TIM12_CH1	Т	FMU_CH7
119	РН	7	I2C3_SCL	Ι	I2C3_SCL_BASE_MS5611_BARBED_EXTERNAL1
120	РН	8	I2C3_SDA	Ι	I2C3_SDA_BASE_MS5611_BARBED_EXTERNAL1
121	РН	9	TIM12_CH2	Т	FMU_CH8
122	РН	10	TIM5_CH1	Т	SPIX_SYNC
123	РН	11	PH11	G	PH11
124	РН	12	TIM5_CH3	Т	SPI2_DRDY2_ISM330_INT2
125	РН	13	UART4_TX	٧	UART4_TX
126	PH	14	UART4_RX	٧	UART4_RX
127	РН	15	PH15	G	SPI4_nCS1_BMM150
128	ΡI	0	TIM5_CH4	Т	FMU_CAP1
129	ΡI	1	SPI2_SCK	S	SPI2_SCK_SENSOR2_ISM330
130	ΡI	2	SPI2_MISO	S	SPI2_MISO_SENSOR2_ISM330
131	ΡI	3	SPI2_MOSI	S	SPI2_MOSI_SENSOR2_ISM330
132	ΡI	4	PI4	G	SPI3_nCS1_BMI088_ACCEL
133	ΡI	5	TIM8_CH1_IN	Т	FMU_PPM_INPUT
134	ΡI	6	PI6	G	SPI3_DRDY1_BMI088_INT1_ACCEL
135	ΡI	7	PI7	G	SPI3_DRDY2_BMI088_INT3_GYRO
136	ΡI	8	PI8	G	SPI3_nCS2_BMI088_GYR0
137	ΡI	9	PI9	G	SPI1_nCS1_ICM20602
138	ΡI	10	PI10	G	SPI6_nCS1_EXTERNAL1
139	ΡI	11	PI11	G	VDD_3V3_SENSORS1_EN