Encoding Sample

Overview

Encoding Sample works with Intel[®] Media Server Studio 2017 - SDK for Linux* Server (hereinafter referred to as "SDK").

It demonstrates how to use the **SDK** API to create a simple console application that performs preprocessing and encoding of an uncompressed video stream according to a specific video compression standard. Also the sample shows how to integrate user-defined functions for video processing (on example of picture rotation plug-in) into **SDK** encoding pipeline.

The sample can work together with Intel[®] Media Server Studio – HEVC Decoder & Encoder (hereinafter referred to as "HEVC").

Note: To run HEVC, please read the instructions in the "HEVC Plugin" section carefully.

Features

Encoding Sample supports the following video formats:

Format type	
input (uncompressed)	YUV420, NV12
	H.264 (AVC, MVC – Multi-View Coding), H.265 (with HEVC), MPEG-2 video, JPEG*/Motion JPEG, HEVC (High Efficiency Video Coding)

Note: For format YUV420, Encoding Sample assumes the order Y, U, V in the input file.

Hardware Requirements

See <install-folder>\Media Samples Guide.pdf.

Software Requirements

See <install-folder>\Media Samples Guide.pdf.

How to Build the Application

See <install-folder>\Media Samples Guide.pdf.

To enable V4L2 option during compilation, set --enable-v4l2=yes option while running build.pl

Running the Software

See <install-folder>\Media Samples Guide.pdf.

The executable file requires the following command-line switches to function properly:

h264 h265 mpeg2 mvc jpeg	Output video type. The use of option h265 is possible only if HEVC installed. The option –q is mandatory in case of JPEG encoding.
-i <inputfile></inputfile>	Input (uncompressed) video file, name and path. In case of MVC -i option must be specified for each input YUV file. Only 2 views are supported
-o <output></output>	Output (compressed) video file, name and path
-w <width></width>	Width of input video frame
-h <height></height>	Height of input video frame

The following command-line switches are optional:

-p guid	32-character hexadecimal guid string. Optional for in-box plugins, required for user-decoder ones (HEVC, f.e.).
-path path_to_plugin	Path to decoder plugin (works only in pair with '-p' option and requires guid to be specified).
-vaapi	Use VAAPI surfaces
-nv12	Signals that the input stream is in NV12 color format
-yuy2	Signals that the input stream is in YUY2, works for JPEG encode only
-p010	Signals that the input stream is in P010, works for HEVC encode only
-ec::p010	Signals that an encoder should use P010 surfaces. If input has different FourCC vpp will be enabled. Works for HEVC encode only.
-tff bff	Signals that the input stream is interlaced (top bottom field first). If this option is not specified, progressive stream is expected.
-hw	Use platform-specific implementation of SDK (default)
-sw	Use software implementation of SDK (platform-specific implementation is used by default)
-b <bitrate></bitrate>	Bitrate of the encoded stream in Kbits/second, supported for all encoders except JPEG*/ Motion JPEG
-f <framerate></framerate>	Frame rate of the encoded stream (30 by default)
-g <size></size>	GOP size (default 256)
-vbr	Variable bitrate control
-idr_interval <size></size>	IDR interval: default 0 means every I is an IDR, 1 means every other I frame is an IDR, etc
-CodecProfile <profile></profile>	Specifies codec profile
	,

-CodecLevel <level></level>	Specifies codec level	
-GopOptFlag:closed	Encoder generates closed GOP. Frames in this GOP do not use frames in previous GOP as reference.	
-GopOptFlag:strict	Encoder strictly follows given GOP structure as defined by parameter GopPicSize, GopRefDist etc.	
-InitialDelayInKB <size></size>	The HRD decoder starts decoding after the buffer reaches the initial size InitialDelayInKB, which is equivalent to reaching an initial delay of InitialDelayInKB*8000/TargetKbps ms	
-BufferSizeInKB <size></size>	Represents the maximum possible size of any compressed frames	
-MaxKbps <size></size>	For variable bitrate control, specifies the maximum bitrate at which the encoded data enters the Video Buffering Verifier buffer	
-u <quality, speed,<br="">balanced></quality,>	Target usage (balanced by default). This parameter specifies a trade-off between quality and speed. Supported for all encoders except JPEG*/Motion JPEG	
-q	The mandatory quality parameter for JPEG/Motion JPEG encoder (not valid for other encoders). In range [1,100], 100 is the best quality.	
-la	Use the look ahead bitrate control algorithm (LA BRC) for H.264 encoder. Supported only with −hw library on processors with Intel [®] Iris [™] Pro Graphics, Intel [®] Iris [™] Graphics or Intel [®] HD Graphics 4200+ Series.	
-lad	Depth parameter for the LA BRC, the number of frames to be analyzed before encoding. In range [10,100].	
-cqp	Use constant quantization parameter (CQP BRC) bitrate control method (by default constant bitrate control method is used), should be used along with -qpi, -qpp, -qpb.	
-qpi	Constant quantizer for I frames (if bitrace control method is CQP). In range [1,51]. 0 by default, i.e.no limitations on QP.	
-qpp	Constant quantizer for P frames (if bitrace control method is CQP). In range [1,51]. 0 by default, i.e.no limitations on QP.	
-qpb	Constant quantizer for B frames (if bitrace control method is CQP). In range [1,51]. 0 by default, i.e.no limitations on QP.	
-qsv-ff	Enable QSV-FF mode	
-num_slice	Number of slices in each video frame. 0 by default. If num_slice equals zero, the encoder may choose any slice partitioning allowed by the codec standard.	
-mss	Maximum slice size in bytes. Supported only with harware library (-hw) and H.264 encoder. This option is not compatible with -num_slice.	
-dstw <width></width>	Width of encoded video frame. If specified and the value here is different from the value specified with -w, the encoder invokes video preprocessing (VPP) for scaling (resizing).	
-dsth <height></height>	Height of encoded video frame. If specified and the value here is different from the value specified with –h, the encoder invokes video preprocessing (VPP) for scaling (resizing).	

-angle 180	Invokes sample plug-in for 180 degrees picture rotation.	
	CPU implementation is used by default. Rotate plugin module sample_rotate_plugin.so must be available when running the application with this option.	
-opencl	Invokes Intel®OpenCL™ implementation of 180 degrees picture rotation. Rotate plugin module libsample_plugin_opencl.so must be available. File ocl_rotate.cl must exist in the local folder when running the application with this option.	
-async	Depth of asynchronous pipeline. default value is 4. must be between 1 and 20.	
-bref	Arrange B frames in B pyramid reference structure	
-nobref	Do not use B-pyramid (by default the decision is made by library)	
-gpucopy:: <on,off></on,off>	Enable/disable GPU Copy functionality	
-re	Enable region encode mode. Works only with HEVC encoder	
-r <distance></distance>	Distance between I- or P- key frames (1 means no B-frames)	
-x <numrefs></numrefs>	Number of reference frames. By default it is set to 1	
-timeout	Encode in a loop not less than specific time in seconds. Performs complete input stream encoding on every iteration. Output file frames amount can be bigger than in input due to buffered frames in encoder. Output file is rewrote every iteration	
-uncut	Do not cut output file in looped mode (in case of -timeout option)	
-membuf	Read specific amount of frames into memory and encode them in a loop during the time specified in -timeout option. Can be used as encoder benchmark	
-gpb: <on,off></on,off>	GPB control. Turn this option OFF to make HEVC encoder use regular P-frames instead of GPB.	
-n <frames number=""></frames>	Tumber of frames to process	
-mfs <maxframesize></maxframesize>	Maximum frame size in bytes. Supported only with h264 and heve codec for VBR mode.	
-dump	Dump MSDK components configuration to the file in text form.	
-signal: <mode></mode>	Represents transfer matrix coefficients for mfxExtVideoSignalInfo. 0 - unknown, 1 - BT709, 2 - BT601	
-?	Print help	

If V4L2 support is enabled during compilation, additional options are available:

-d	Device video node (eg: /dev/video0)
-p	Mipi Port number (eg: Port 0)
-m	Mipi Mode Configuration [PREVIEW/CONTINUOUS/STILL/VIDEO]
-uyvy	Input Raw format types V4L2 Encode
-YUY2	Input Raw format types V4L2 Encode
-i::v4l2	Enable v4l2 mode

Below are examples of a command-line to execute **Encoding Sample**:

```
$ sample_encode h264 -i input.yuv -o output.h264 -w 720 -h 480 -b 10000 -f
30 -u quality -d3d -hw
$ sample_encode mpeg2 -i input.yuv -o output.mpeg2 -w 1920 -h 1080 -b 15000
-u speed -nv12 -tff -hw
$ sample_encode h264 -i input.yuv -o output.h264 -w 1920 -h 1080 -dstw 360 -
dsth 240 -b 1000 -u balanced -hw
```

Note 1: You need to have **HEVC** installed to run with h265 codec. In case of HW library it will firstly try to load HW HEVC plugin in case of failure - it will try SW one if available.

Tip:

To achieve better performance, use input streams in NV12 color format. If the input stream is in YUV420 format, each frame is converted to NV12 which reduces overall performance.

HEVC Plugin

HEVC codec is implemented as a plugin unlike codecs such as MPEG2 and AVC. After you install the HEVC package, you will see the following plugins installed – HEVC Decode SW, HEVC Encode SW and HEVC Encode GACC (GACC is Graphics ACCelerated plugin that uses both CPU and GPU for execution).

Our samples load the SW HEVC plugins, unless the GUI for the GACC counterparts are specified using "-p" parameter. For example, the following command-lines will use the SW HEVC Decode and Encode plugin respectively:

```
$ sample_decode h265 -i input.265 -o output.yuv
$ sample_encode h265 -i input.yuv -o output.h265 -w 720 -h 480 -b 10000 -f
30 -u quality
```

(Note: HEVC Decode GACC plugin is not available on Linux).

To run the HEVC GACC plugins, you have to specify the "-p" parameter to the GUID. For example, the following command-lines will use the HEVC GACC Decode and Encode plugin:

```
$ sample_decode h265 -i input.265 -o output.yuv -p
33a61cb4c27454ca8d85dde757c6f8e

$ sample_encode h265 -i input.yuv -o output.h265 -w 720 -h 480 -b 10000 -p
e5400a06c74d41f5b12d430bbaa23d0b
```

To run HEVC plugins in 10 bit mode, you have to specify the "-ec::p010" or "-p010" option:

```
$ sample_encode h265 -i input.yuv -o output.h265 -w 720 -h 480 -b 10000 -
ec::p010

$ sample_encode h265 -i input.p010 -o output.h265 -w 720 -h 480 -b 10000 -
p010
```

Known Limitations

- Not all combinations of optional switches are supported. If the option -angle 180 or -opencl is specified, options -tff|bff, -dstw, -dsth, -d3d and MVC output are not available.
- Encoding Sample if run with -opencl option requires input video frame width to be aligned by 4.

- In case of using HEVC plugin (h265 video type), plugin type (hardware or software) used by default is set depending on -sw or -hw sample options. However, hardware HEVC plugins work on specific platforms only. To force usage of specific HEVC plugin implementation, please use -p option with proper plugin GUID.
- Sample may not function properly on systems that have a non-Intel VGA controller as the first (primary) because Intel device is not first in the list.

To workaround this issue, swap names of DRI device files:

- $\$ cd /dev && mv card0 tmp && mv card1 card0 && mv tmp card1 and do the same for the files control64/65 and renderD128/129
- · HECV 10Bit encoding works with HEVC SW plugin only due to library limitations

Legal Information

INFORMATION IN THIS DOCUMENT IS PROVIDED IN CONNECTION WITH INTEL PRODUCTS. NO LICENSE, EXPRESS OR IMPLIED, BY ESTOPPEL OR OTHERWISE, TO ANY INTELLECTUAL PROPERTY RIGHTS IS GRANTED BY THIS DOCUMENT. EXCEPT AS PROVIDED IN INTEL'S TERMS AND CONDITIONS OF SALE FOR SUCH PRODUCTS, INTEL ASSUMES NO LIABILITY WHATSOEVER AND INTEL DISCLAIMS ANY EXPRESS OR IMPLIED WARRANTY, RELATING TO SALE AND/OR USE OF INTEL PRODUCTS INCLUDING LIABILITY OR WARRANTIES RELATING TO FITNESS FOR A PARTICULAR PURPOSE, MERCHANTABILITY, OR INFRINGEMENT OF ANY PATENT, COPYRIGHT OR OTHER INTELLECTUAL PROPERTY RIGHT.

UNLESS OTHERWISE AGREED IN WRITING BY INTEL, THE INTEL PRODUCTS ARE NOT DESIGNED NOR INTENDED FORANYAPPLICATION IN WHICH THE FAILURE OF THE INTEL PRODUCT COULD CREATE A SITUATION WHERE PERSONAL INJURY OR DEATH MAY OCCUR.

Intel may make changes to specifications and product descriptions at any time, without notice. Designers must not rely on the absence or characteristics of any features or instructions marked "reserved" or "undefined." Intel reserves these for future definition and shall have no responsibility whatsoever for conflicts or incompatibilities arising from future changes to them. The information here is subject to change without notice. Do not finalize a design with this information.

The products described in this document may contain design defects or errors known as errata which may cause the product to deviate from published specifications. Current characterized errata are available on request.

Contact your local Intel sales office or your distributor to obtain the latest specifications and before placing your product order.

Copies of documents which have an order number and are referenced in this document, or other Intel literature, may be obtained by calling 1-800-548-4725, or by visiting *Intel's Web Site*.

MPEG is an international standard for video compression/decompression promoted by ISO. Implementations of MPEG CODECs, or MPEG enabled platforms may require licenses from various entities, including Intel Corporation.

Intel, the Intel logo, Intel Core are trademarks or registered trademarks of Intel Corporation or its subsidiaries in the United States and other countries.

Optimization Notice

Intel's compilers may or may not optimize to the same degree for non-Intel microprocessors for optimizations that are not unique to Intel microprocessors. These optimizations include SSE2, SSE3, and SSE3 instruction sets and other optimizations. Intel does not guarantee the availability, functionality, or effectiveness of any optimization on microprocessors not manufactured by Intel.

Microprocessor-dependent optimizations in this product are intended for use with Intel microprocessors. Certain optimizations not specific to Intel microarchitecture are reserved for Intel microprocessors. Please refer to the applicable product User and Reference Guides for more information regarding the specific instruction sets covered by this notice.

Notice revision #20110804

OpenCL and the OpenCL logo are trademarks of Apple Inc. used by permission by Khronos.

Copyright © Intel Corporation

^{*} Other names and brands may be claimed as the property of others.