

NETWORK THEORY LAB REPORT

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November 17, 2023

Abstract

This is the abstract section of your lab report. Provide a brief summary of the objectives, methods, and key findings of your experiments.

1 Introduction

2 Experiment 1: Low Pass Filter

2.1 Objective

To determine the input/output characteristics of a low pass filter and calculate its cut-off frequency.

2.2 Instruments

- Oscilloscope
- Function generator
- EC2 MVM6 box

2.3 Theory

Introduction

Low-pass filters play a crucial role in signal processing, allowing the passage of low-frequency signals while attenuating higher frequencies. This experiment aims to analyze the input/output characteristics of a low-pass filter and determine its cut-off frequency (f_c), which signifies the point at which the filter starts attenuating the input signal significantly.

Low-Pass Filter Characteristics

The transfer function of a first-order low-pass filter is given by:

$$H(f) = \frac{1}{1 + j \left(\frac{f}{f_c} \right)}$$

where f is the input frequency, f_c is the cut-off frequency, and j is the imaginary unit.

Cut-Off Frequency

The cut-off frequency is a critical parameter in understanding low-pass filter behavior. It is related to the time constant (τ) of the filter by the equation:

$$f_c = \frac{1}{2\pi\tau}$$

A low-pass filter can be implemented using an operational amplifier (op-amp) as the active element and resistors and capacitors as passive elements. The generic filter diagram is shown in Figure ??, where $Z1 - Z5$ indicate generic impedances. The input-output formula for the general diagram is given by:

$$\frac{V_o}{V_{in}} = \frac{Z2 \cdot Z4 \cdot Z5}{Z1 \cdot Z3 \cdot Z5 + Z1 \cdot Z2 \cdot Z5 + Z1 \cdot Z2 \cdot Z3 + Z2 \cdot Z3 \cdot Z5 - Z2 \cdot Z1 \cdot Z4} \quad (1)$$

This relation assumes ideal conditions such as infinite input impedance and infinite amplification of the operational amplifier, leading to a virtual ground.

For a specific low-pass filter with resistors ($Z1$, $Z3$, and $Z5$) and capacitors ($Z2$ and $Z4$), the input-output formula becomes:

$$\frac{V_o}{V_{in}} = \frac{R2 \cdot R3 \cdot C1 \cdot C2}{R3 + R2 \cdot (1 + j\omega R1 \cdot C1)} \quad (2)$$

where ω is the angular frequency of the input signal.

The cut-off frequency F_c is determined by the values of the passive components:

$$F_c = \frac{1}{2\pi R1 \cdot R2 \cdot C1 \cdot C2} \quad (3)$$

The gain G_0 of the filter at DC (frequency $f = 0$ Hz) is given by:

$$G_0 = -\frac{R3}{R1} \quad (4)$$

Experiment Setup

The experiment utilizes an oscilloscope, function generator, and the EC2 MVM6 box. The function generator provides the input signal, and the oscilloscope measures the output signal. The EC2 MVM6 box contains the low-pass filter circuit under investigation.

Procedure

1. Connect the function generator output to the input of the low-pass filter circuit.
2. Connect the output of the low-pass filter circuit to the input channel of the oscilloscope.
3. Set the function generator to produce a sine wave with varying frequencies.
4. Measure and record the amplitude of the output signal at different input frequencies.

Expected Results

The input/output characteristics of the low-pass filter are expected to show a gradual attenuation of the output amplitude as the input frequency increases. The cut-off frequency can be determined by analyzing the point at which the attenuation becomes significant.

Significance

Understanding low-pass filter behavior is crucial in applications such as audio signal processing, communication systems, and control systems. Determining the cut-off frequency allows for the design of filters tailored to specific frequency ranges, optimizing electronic system performance.

2.4 Results

Q1: What is the approximate amplification of the filter at 10 Hz?

1. 1
2. 9
3. 15
4. 50
5. Infinite

Q2: What is the main reason for this amplification value?

- 1 the value of R_{22}
- 2 the value of C_{10}
- 3 the value of R_{23}
- 4 the value of the ratio $\frac{R_{23}}{R_{20}}$
- 5 the value of the product $R_{22} \cdot C_{10}$

Input Frequency (Hz)	Output Amplitude (Vpp)
10	9.1
20	9.2
40	8.8
60	8.7
80	8.6
100	8.0
150	6.3
200	4.5
500	2.5

Table 1: Input frequency and output amplitude measurements.

Plot all the values obtained into figure A15.4 and draw the input/output characteristic of the filter
 From this graph calculate the cut-off frequency of the filter, defined as the frequency at which the amplification drops to 0.707 times the maximum.

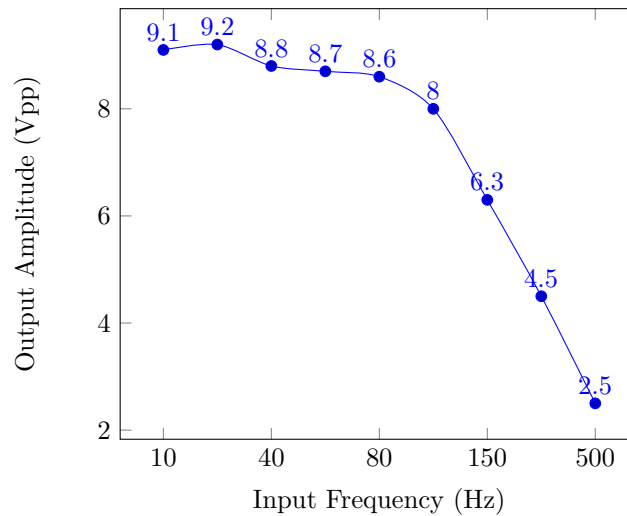


Figure 1: Input frequency vs. output amplitude (A15.4)

Turn switch S22 “ON”

- Now measure the cut-off frequency of the filter again.

Q3: What do you notice?

- 1 the cut-off frequency is increased
- 2 the cut-off frequency is decreased
- 3 the output voltage is constant
- 4 the output voltage is a square wave
- 5 the output voltage is a triangular wave

Q4: What is the cause of this?

- 1 the output is short-circuited
- 2 the inputs are short-circuited
- 3 the capacitor C10 has increased
- 4 the capacitor C10 has decreased
- 5 the amplifier has positive feedback

2.5 Conclusion

2.6 References

3 Experiment 2: Superposition of A.C and D.C. Voltages in linear and Non-linear Circuits

3.1 Objective

To demonstrate the effect of applying ac and dc voltages to the same circuit simultaneously.

3.2 Instruments

- Oscilloscope
- Low voltage DC power supply
- 12V center tapped transformer
- 1k Ω resistor
- Diode

3.3 Linear circuits - Demonstration of Superposition

Theory

The superposition theorem states that the response in any branch of a linear circuit with multiple independent sources is the algebraic sum of the responses caused by each independent source acting alone, while all other independent sources are turned off. The superposition theorem is used to solve circuits by hand by breaking the circuit into smaller parts and analyzing each part individually. The superposition theorem is also used in circuit simulation software such as SPICE for the same purpose. The superposition theorem is a fundamental principle in linear circuit analysis that simplifies the analysis of complex circuits. It states that in a linear circuit containing multiple independent sources, the response (voltage or current) at any element is equal to the algebraic sum of the responses caused by each independent source acting alone, with all other sources turned off.

Statement of the Superposition Theorem

Consider a linear circuit with multiple independent sources. Let S_1, S_2, \dots, S_n be the independent sources, and R be the response (voltage or current) at a specific element in the circuit. The total response R_{total} at the element is given by the sum of the responses caused by each source individually:

$$R_{\text{total}} = R_{S_1} + R_{S_2} + \dots + R_{S_n}$$

where R_{S_i} is the response due to the i -th source acting alone, with all other sources turned off.

Applicability

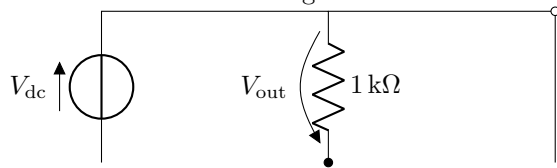
The superposition theorem is applicable only to linear circuits, where the circuit elements exhibit linear relationships between voltage and current. It is particularly useful in simplifying the analysis of circuits with multiple sources, allowing engineers to focus on individual sources and their effects.

Significance

The superposition theorem provides a powerful tool for simplifying circuit analysis, especially in circuits with multiple sources. By breaking down a complex circuit into simpler sub-circuits, engineers can better understand and design electronic systems with improved accuracy.

Procedure

Construct the circuit in figure 1



4.1.2 Connect the primary of the transformer to the 50Hz, 240V mains. Do not turn the dc voltage on yet but connect the supply into the circuit

NOTE: The dc supply must be well bypassed with capacitors. However if a reregulated supply is used, the bypass capacitors are not necessary.

4.1.3 Measure and record the ac voltage V on the VTVM and observe the voltage V , across the $1\text{ k}\Omega$ resistor on the scope. Sketch the waveform observed (at least 2 copies). You are advised to calibrate the vertical scale of the oscilloscope at 5 volts per division. Record and measure the peak to peak ac voltage across R .

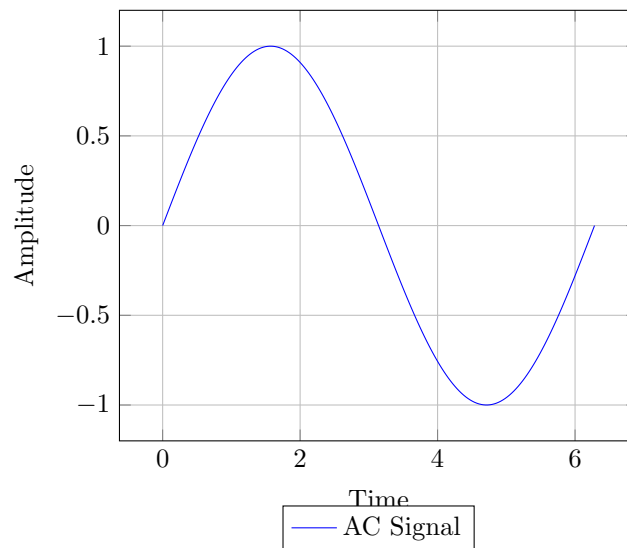


Figure 2: AC Signal Plot

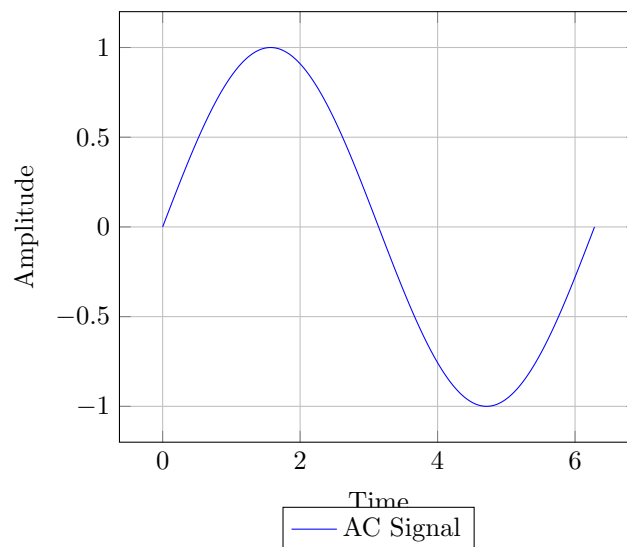


Figure 3: V across R

4.1.4 Turn off the ac power supply leaving the transformer secondary connected. Apply 12V dc to terminals X-Y. Measure and record the dc voltage accross R.

4.1.5 Now with the 12V dc still applied to the circuit, apply 240V ac to the transformer primary again. Measure the dc voltage accross R, observe the waveform of the voltage accross R on the scope sketch the waaveform observed What is the peak to peak voltage accross R measured

NB: In sketching the waveform for V_o make sure to include both ac and dc components.

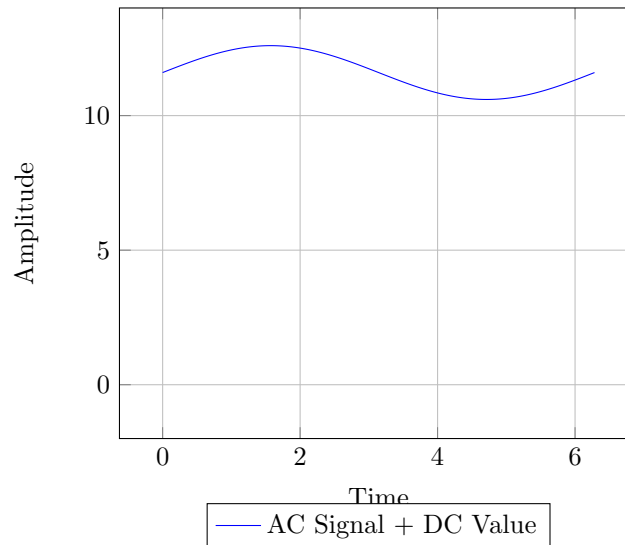


Figure 4: AC Signal Superposed with DC Value

4.1.6 Now with both ac and dc applied, readjust the dc voltage to 5 volts and measure V_{dc} and V_{ac} Sketch the waveform for V_o observed on the scope use the same

3.4 Discussion

State whether the dc level has any significant effect on the part of value of the ac voltage

What happens to the dc level when the ac voltage is turned off?

What basic property of linear circuits is demonstrated by this experiment?

State this principle and briefly explain what it means

3.5 Non-linear circuits

Theory

The superposition theorem is a fundamental concept in linear circuit analysis, allowing for the simplification of complex circuits with multiple independent sources. However, it is crucial to understand that superposition is valid only for linear circuits, where the relationship between voltage and current is linear. In nonlinear circuits, components exhibit non-linear behavior, and applying superposition directly may lead to inaccuracies.

Nonlinear Components

Nonlinear components, such as diodes and transistors, introduce non-linear relationships between voltage and current. Unlike resistors, these components do not follow Ohm's law, making the application of superposition challenging. To investigate the limitations of the superposition theorem in nonlinear circuits, consider a circuit with a nonlinear element, such as a diode. Design the circuit to include multiple independent sources and resistors along with the nonlinear component.

Significance

This experiment aims to highlight the limitations of applying superposition to nonlinear circuits. By comparing the superposition-based predictions with actual measurements, you can illustrate the impact of non-linear elements on the accuracy of the superposition theorem.

Procedure

5.1.1 Construct the circuit in figure 2.

5.1.2 Connect the primary of the transformer to the 50Hz, 240V mains. Do not turn the dc voltage on yet but connect the supply into the circuit.

5.1.3 With the vertical input of the oscilloscope calibrated to 5V per division, connect the scope common to M and the probe to L. Sketch the wave form observed (atleast 2 cycles)

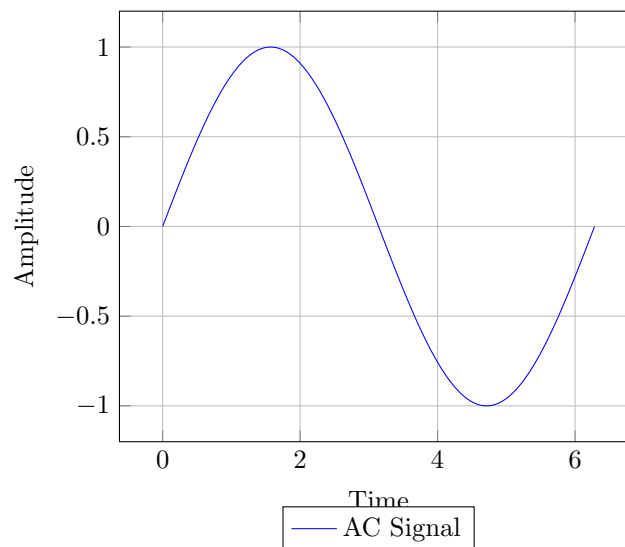


Figure 5: AC Signal Plot

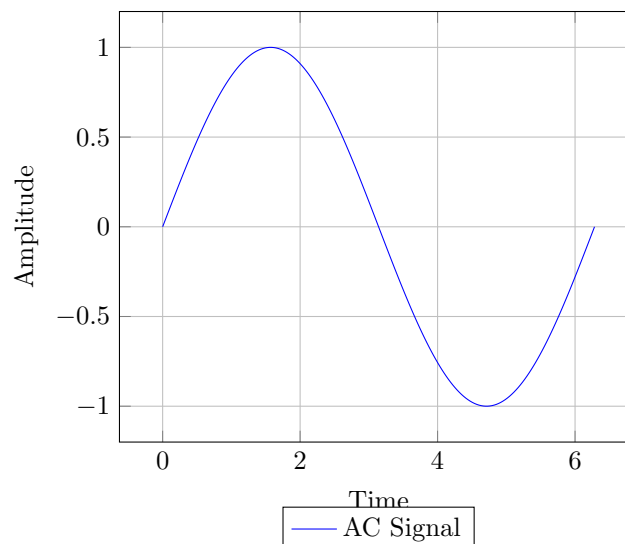


Figure 6: V across R

5.1.4 with the scope common still at N, connect the probe to M. Sketch the waveform observed

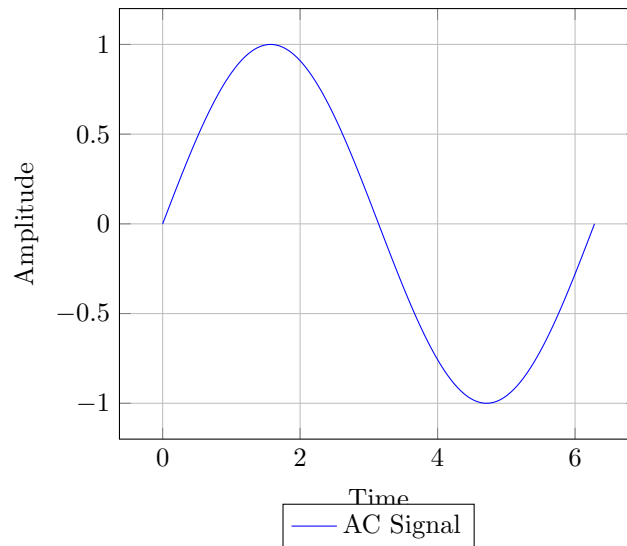


Figure 7: V accross R

5.1.5 Next move the scope common to M and connect the probe to L. Sketch the waveform observed

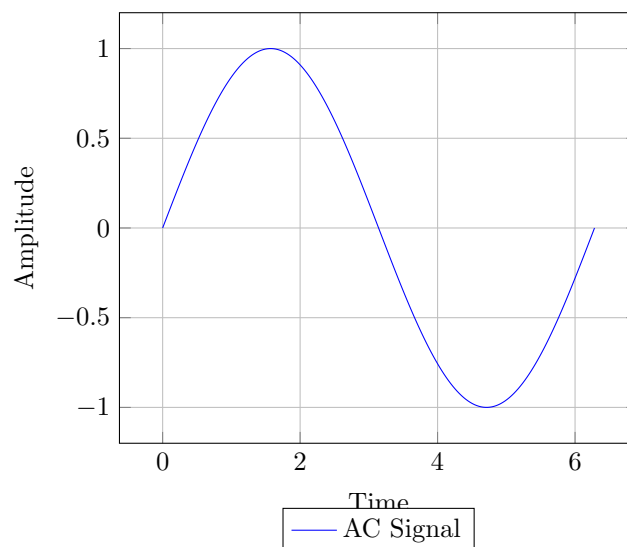


Figure 8: V accross R

5.1.6 Now with the ac still applied to the circuit, apply 5V dc accross the terminals L-N.

5.1.7 With the scope common still at N, connect the probe to L. Sketch the waveform observed.

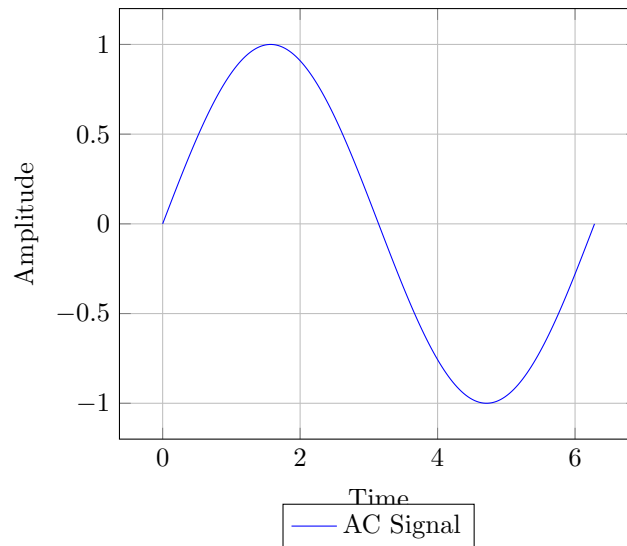


Figure 9: V accross R

5.1.8 Next move the scope common to M and connect the probe to L. Sketch the waveform observed.

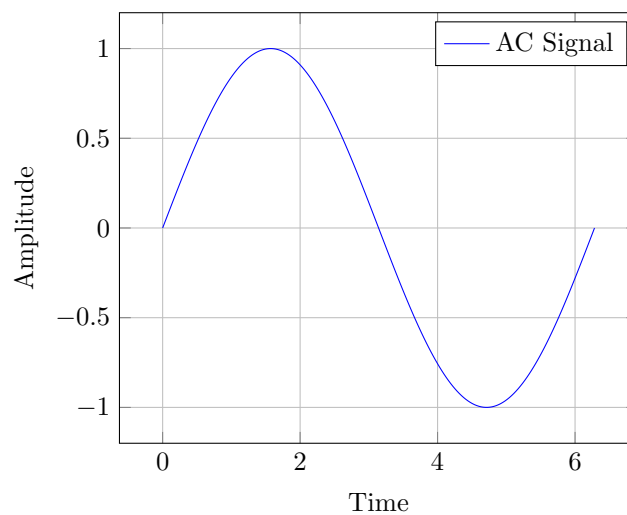


Figure 10: V accross R

Discussion

Conclusion

While the superposition theorem is a powerful tool in linear circuit analysis, its direct application to nonlinear circuits can lead to errors. Understanding these limitations is crucial for accurate analysis and design of circuits containing nonlinear components.