



COLLEGE OF ENGINEERING, DESIGN, ART AND TECHNOLOGY
DEPARTMENT OF ELECTRICAL AND COMPUTER ENGINEERING

BACHELOR OF SCIENCE IN ELECTRICAL ENGINEERING

SHAWAL MBALIRE 21/U/0851

APPLIED ANALOGUE ELECTRONICS LAB REPORT

| Group Members | | |
|----------------------|-----------------------------|----------------------------|
| S/N | Name | Registration Number |
| 1 | Amutuhire Judith | 22/U/5773 |
| 2 | Aine Mugabe Hillary | 22/U/5683 |
| 3 | Aransiola Oyindamola Serena | 21/X/20031/PS |
| 4 | Mbalire Shawal | 21/U/0851 |
| 5 | Keyuyne Jordie | 20/U/0449 |
| 6 | Ankwasa Derrick | 22/U/5780 |
| 7 | Kasigwa Isabella | 22/U/6075 |

Instructor: Mr. Robinson Ntege

Sunday 10th November, 2024

Contents

| | | |
|----------|--|-----------|
| 1 | Experiment to investigate of the Operation of RC Oscillators. | 1 |
| 1.1 | Objectives | 1 |
| 1.2 | List of Apparatus | 1 |
| 1.3 | Basic Theory | 1 |
| 1.3.1 | RC Oscillators | 1 |
| 1.3.2 | The Wien Bridge Oscillator | 1 |
| 1.3.3 | Applications of RC Oscillators in Analog Electronics | 2 |
| 1.4 | Procedures | 2 |
| 1.4.1 | Procedure 1 - RC oscillator | 2 |
| 1.4.2 | Procedure 2 - The wein bridge oscillator | 5 |
| 1.4.3 | Procedure 3 - The square wave | 6 |
| 1.4.4 | Questions | 6 |
| 2 | Experiment to Demonstrate Amplification with Resistive and Tuned Loads using Class C Amplifiers | 7 |
| 2.1 | Objectives | 7 |
| 2.2 | Instruments | 7 |
| 2.3 | Basic Theory | 7 |
| 2.3.1 | Applications | 8 |
| 2.3.2 | Limitations | 8 |
| 2.3.3 | Operation | 8 |
| 2.4 | Experiments | 9 |
| 2.4.1 | Amplifier with resistive load | 9 |
| 2.4.2 | Procedures | 9 |
| 2.4.3 | Questions | 9 |
| 2.4.4 | Tuned load | 10 |
| 2.4.5 | Procedures | 10 |
| 2.4.6 | Questions | 11 |
| 2.5 | Summary Questions | 11 |
| 3 | Errors | 12 |
| 3.1 | Sources of Errors | 12 |
| 3.2 | Error minimization | 12 |
| 3.3 | Error Minimization | 12 |
| 4 | Conclusion | 13 |

List of Figures

| | | |
|----|--|----|
| 1 | Basic RC circuit | 3 |
| 2 | RC oscillators | 3 |
| 3 | wave form at point A | 3 |
| 4 | Wave form from point B | 4 |
| 5 | wave form from point C | 4 |
| 6 | wave form from point D | 4 |
| 7 | The wein bridge oscillator | 5 |
| 8 | wave form for wein bridge oscillator | 5 |
| 9 | The square wave | 6 |
| 10 | class C amplifier | 7 |
| 11 | amplifier with resistive load | 9 |
| 12 | tuned load | 10 |

1 Experiment to investigate of the Operation of RC Oscillators.

1.1 Objectives

1. To understand the Working Principle of RC Oscillators
2. To analyze the Wien Bridge Oscillator Circuit

1.2 List of Apparatus

1. Oscilloscope
2. +/- 12V dual power supply
3. UA 741 OP AMP
4. 3 $10k\Omega$ resistors
5. 1 $2k\Omega$, $1k\Omega$, $470k\Omega$, $4.7k\Omega$ resistors
6. 1 $1k\Omega$ variable resistor
7. 1 $2.2nF$, $1nF$ capacitors
8. 2 $12nF$ capacitors

1.3 Basic Theory

1.3.1 RC Oscillators

RC oscillators are fundamental circuits used to generate sine wave signals in analog electronics. These oscillators rely on an RC network, where resistors (R) and capacitors (C) create a phase shift that enables oscillation at a particular frequency. This type of oscillator, commonly referred to as a phase-shift oscillator, produces a sine wave output by using regenerative feedback. In this configuration, the RC network provides a feedback path with a phase shift of 180 degrees, which complements the additional 180 degrees phase shift introduced by an inverting amplifier (often an operational amplifier or transistor). When the total phase shift reaches 360 degrees (or 0 degrees), sustained oscillations occur.

The phase shift in an RC network depends on the time constant ($\tau = RC$) of each stage. Typically, an RC oscillator uses three or more RC stages to achieve the necessary 180-degree phase shift in the feedback loop. The frequency of oscillation f for a three-stage RC oscillator can be approximated as:

$$f = \frac{1}{2\pi RC\sqrt{6}}$$

where R and C are the resistance and capacitance of each RC stage, respectively.

These oscillators are commonly used in low- to mid-frequency applications and serve as the basis for many signal generation and timing applications in analog electronics, including audio frequency generators and function generators.

1.3.2 The Wien Bridge Oscillator

The Wien bridge oscillator is a widely used RC oscillator circuit that allows for stable, tunable frequency generation, commonly producing frequencies from a few Hz up to several MHz. It uses a Wien bridge network in its feedback loop, consisting of resistors and capacitors arranged in a bridge

configuration. This network provides the necessary phase shift and frequency-selective properties for oscillation. The frequency of oscillation f in a Wien bridge oscillator is given by:

$$f = \frac{1}{2\pi RC}$$

where R and C represent the resistance and capacitance in the bridge network.

The Wien bridge oscillator's main advantage is its frequency stability and simplicity. It can be adjusted to produce different frequencies by changing either the resistors or capacitors in the feedback network. Often, variable capacitors or variable resistors (also called gang capacitors or gang resistors) are used to make the frequency adjustable, which is useful in applications that require tunable frequency generation, such as audio oscillators and test equipment.

In practice, the Wien bridge oscillator requires an amplifier with a gain slightly above 3 to maintain oscillations. Modern implementations often use an operational amplifier with an automatic gain control circuit (like a light bulb or diode-based network) to stabilize the output amplitude. This oscillator is prized for producing low-distortion sine waves, making it ideal for audio signal generation and testing applications.

1.3.3 Applications of RC Oscillators in Analog Electronics

RC oscillators are used in a variety of applications within analog electronics, especially where stable, low- to mid-frequency sine wave generation is needed. Typical applications include:

1. Audio signal generators: RC oscillators are used to generate test tones and signals for audio equipment testing and calibration.
2. Function generators: These devices use RC oscillators to produce waveforms for testing and measurement in electronics labs.
3. Filter design and analysis: RC oscillators help analyze the behavior of analog filters by providing input signals of known frequencies.
4. Timing and control circuits: RC oscillators are also used in circuits requiring periodic signals or timing control, such as in clocks and timers.
5. Signal processing and synthesis: RC oscillators can serve as basic waveform generators for sound synthesis and signal processing tasks in audio and telecommunications.

By understanding the principles and configurations of RC and Wien bridge oscillators, engineers can effectively design circuits that generate precise and stable sine wave signals across a wide range of frequencies.

1.4 Procedures

1.4.1 Procedure 1 - RC oscillator

1. Construct the circuit of figure:1 below

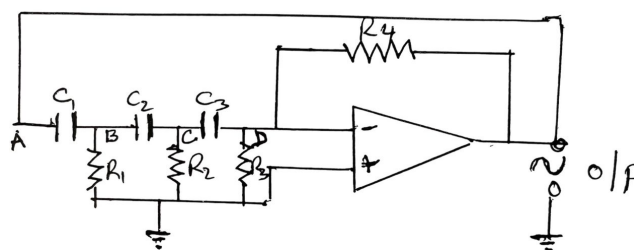


Figure 1: Basic RC circuit

$$C_1 = C_2 = C_3 = 2.2nF$$

$$R_1 = R_2 = R_3 = 10k\Omega$$

$$R_4 = 470k\Omega$$

$$f = \frac{1}{2\pi RC\sqrt{6}}$$

2. Observe the waveform

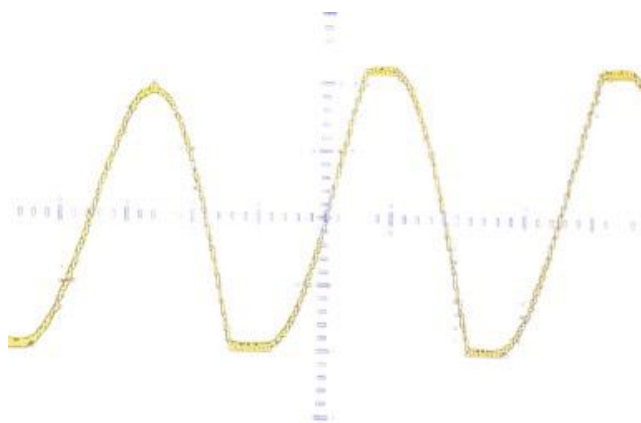


Figure 2: RC oscillators

3. Compare the wave forms at points ABCD

- at point A

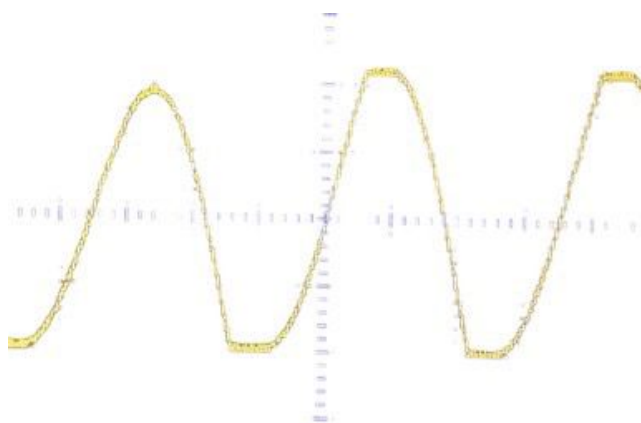


Figure 3: wave form at point A

- at point B



Figure 4: Wave form from point B

- at point C

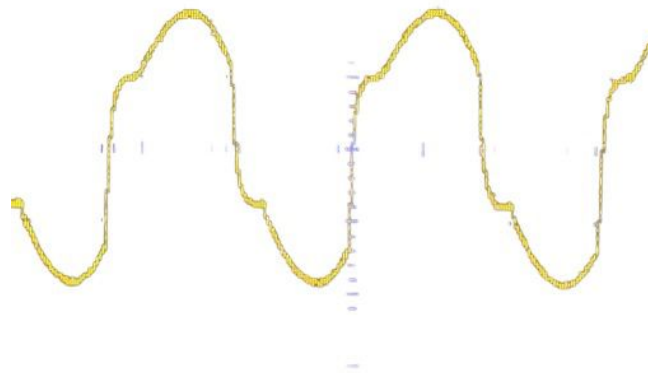


Figure 5: wave form from point C

- at point D

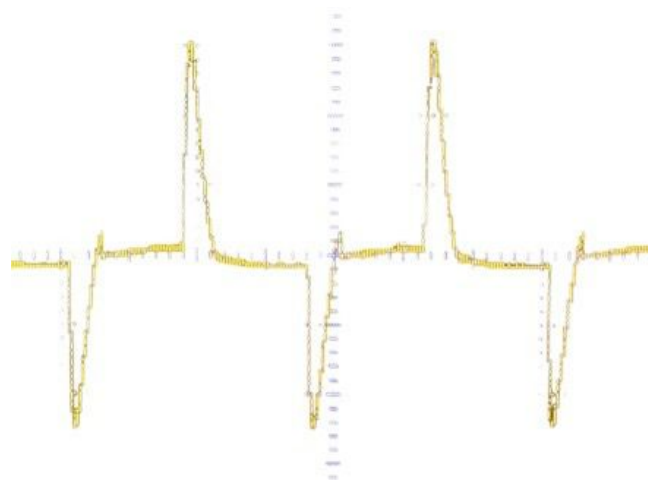


Figure 6: wave form from point D

4. Compare the frequency output generated with the theoretical frequency using the formula given below

for the practical frequency:

$$f = 2.878kHz$$

Theoretical frequency:

$$f = \frac{1}{2\pi RC\sqrt{6}}$$

$$f = \frac{1}{2\pi(10k)(2.2n)\sqrt{6}}$$

$$f = 2.95kHz$$

1.4.2 Procedure 2 - The wein bridge oscillator

1. Construct the circuit of the figure:7 below

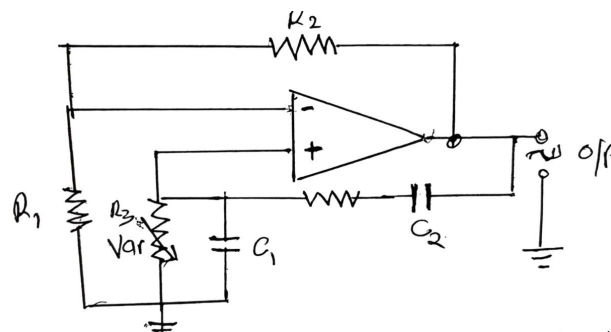


Figure 7: The wein bridge oscillator

Vary R_3 for output

$R_1 = 1k\Omega$, $R_2 = 2.2k\Omega$, $R_3 = 1k\Omega$ variable

$C_1 = C_2 = 12nF$

2. Observe the wave form generated

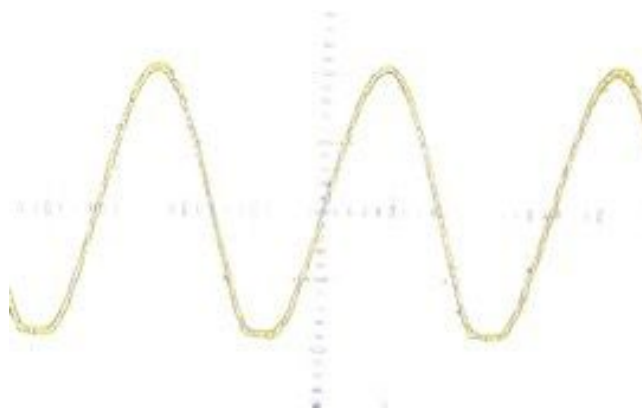


Figure 8: wave form for wein bridge oscillator

3. Compare the waveform of the phase shift generated and the Wien bridge oscillator

The wave form produced are very similar yet produced with different setups

1.4.3 Procedure 3 - The square wave

1. Construct the circuit of the figure:9 below

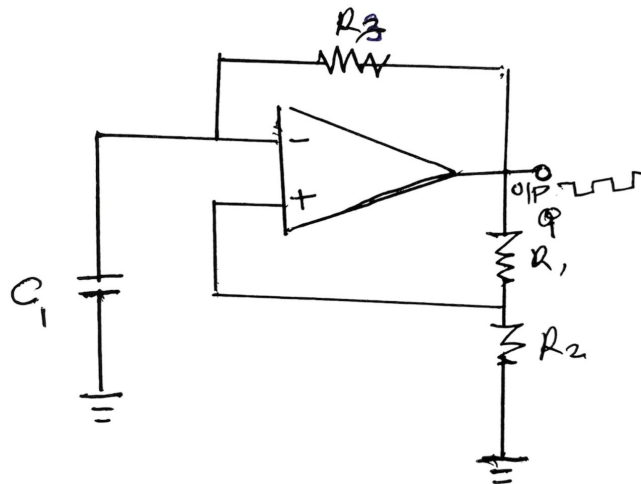


Figure 9: The square wave

$$R_1 = R_2 = 10k\Omega$$

$$R_3 = 4.7k\Omega$$

$$C_1 = 1nF$$

1.4.4 Questions

What values of the resistor R_1 and C_1 would make an output of 100kHz?

$$f = \frac{1}{2\pi RC}$$

Given:

$$R = 10k\Omega, \quad f = 10kHz$$

we can rearrange the formula to solve for C :

$$C = \frac{1}{2\pi Rf}$$

Substituting in the values:

$$C = \frac{1}{2\pi \times 10k\Omega \times 10kHz}$$

$$C \approx 1.59nF$$

$\therefore R = 10k\Omega$ and $C \approx 1.59nF$ will produce $f \approx 10kHz$.

2 Experiment to Demonstrate Amplification with Resistive and Tuned Loads using Class C Amplifiers

2.1 Objectives

1. Amplification with resistive loads:
 - (a) analysis of bias circuits
 - (b) inspection of the current wave-form in the load
 - (c) measurement of the conduction angles as a function of the biasing
2. Amplification with tuned loads:
 - (a) calculation of the resonant frequency f_0
 - (b) use as frequency multiplier

2.2 Instruments

1. function generator
2. oscilloscope
3. multimeter

2.3 Basic Theory

Class C amplifiers are designed to operate with the transistor biased below the cutoff point, conducting for less than half of each input signal cycle.

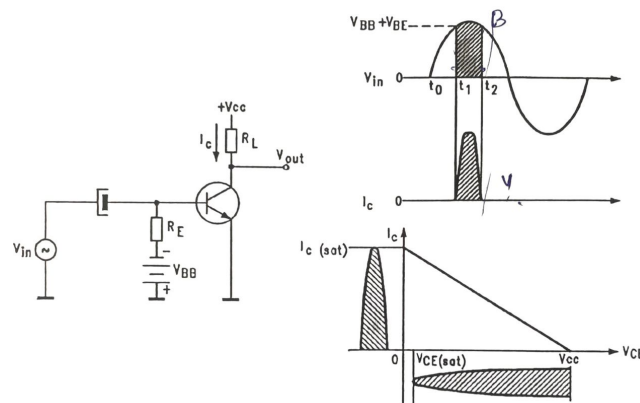


Figure 10: class C amplifier

This mode of operation results in a highly distorted, pulsed output, which requires filtering to achieve a clean output waveform. These amplifiers are inherently non-linear and are unsuitable for applications where low distortion is required.

Class C amplifiers are highly efficient, often achieving over 80%, as the transistor only conducts for a small portion of each cycle, minimizing power dissipation. They are typically used with a resonant LC load (inductor-capacitor circuit) tuned to the desired frequency, which filters the pulsed output to

create a sinusoidal signal. The resonant frequency f of the LC circuit is given by:

$$f = \frac{1}{2\pi\sqrt{LC}}$$

2.3.1 Applications

Class C amplifiers are commonly used in:

- RF Transmitters: Amplifying high-frequency signals for radio and radar systems.
- Oscillators: Maintaining oscillations at specific frequencies in communication systems.
- FM and AM Transmitters: Amplifying carrier signals for frequency and amplitude modulation.

2.3.2 Limitations

The limitations of Class C amplifiers include:

- High Distortion: Non-linearity results in significant harmonic distortion.
- Limited Frequency Range: Most suitable for high frequencies, requiring large components for low frequencies.
- Need for Resonant Loads: Dependence on resonant loads adds complexity to the circuit.

In summary, Class C amplifiers are ideal for high-efficiency, high-frequency applications where distortion can be tolerated or filtered out.

2.3.3 Operation

When a sine wave of voltage $v(t) = V_M \sin(\omega t)$, is connected to the input of the amplifier, the current $i(t)$ through the load R_L is different from zero in the conduction range $T = t_2 - t_1$, which corresponds to a conduction angle $\phi = \phi_2 - \phi_1$, where $\phi = \omega T$.

In a class C amplifier the angle ϕ is less than 180 degrees, and depends on the transistor bias.

The class C amplifier does not dissipate power in static conditions $I_{CQ} = 0$, while the power dissipated in dynamic conditions depends on the amplitude of the signal $v(t)$ and the conduction angle ϕ , reducing this angle, the efficiency increases, and can take values approaching 100%. Actually, we cannot reduce the conduction angle ϕ too much, because the overall power decreases too.

The train of pulses constituting the load current $i(t)$ represents a non-sinusoidal, periodic function. The period of this function equals the input signal period. Using a Fourier series, the load current can be represented by an infinite sum of sine waves:

$$i(t) = I_{CQ} + i_1 \sin(\omega t) + i_2 \sin(2\omega t) + \dots$$

If a resonant circuit is used as load, tuned at an harmonic of the fundamental, this amplifier can be used as frequency multiplier. Since the amplitude of the higher harmonics falls rapidly as frequency increases, the main amplification will be obtained at the fundamental frequency, i.e. at $f = \frac{\omega}{2\pi}$.

A class C amplifiers operate at very high efficiency, but is only used to amplify a single frequency. It cannot be used for the linear amplification. For these reasons, it is used with a resonant load to extract the main frequency, or possibly one of its harmonics.

2.4 Experiments

2.4.1 Amplifier with resistive load

2.4.2 Procedures

1. Set V_{cc} adjustable power supply to 12V.
2. Insert jumpers $J_1, J_3, J_{10}, J_{11}, J_{15}, J_{17}, J_{26}$, to produce the circuit of figure:11

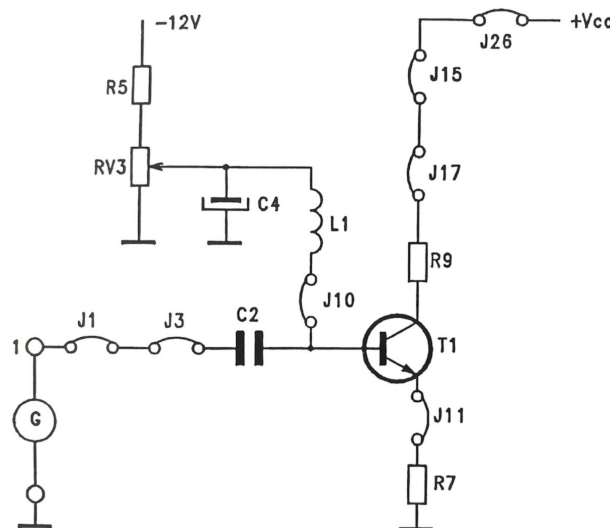


Figure 11: amplifier with resistive load

3. adjust R_{V3} , and consider how the bias voltage on the base of transistor T_1 varies
4. connect the function generator at terminals 1 and ground with a sine 20 KHz and $1V_{pp}$
5. reduce the frequency of the input signal, and check the resulting signal on base of T_1 with oscilloscope
6. adjust R_{V3} to set the voltage on the base of T_1 to 0V
7. set the sine input signal to 20 kHz
8. increase the amplitude of the input signal, observing the signal on the collector of T_1 and across R_7
9. in particular, analyze the case in which the positive peak of the input voltage, applied across the base of T_1 , exceeds the threshold 0.6-0.7V of the transistor

When the input voltage exceeds the transistor threshold voltage it starts conducting generating small voltage pulses across the resistance R_7 . As the conduction angle is less smaller than 180 degrees, the amplifier is in class C

10. adjust R_{V3} to negatively bias the base of T_1 and check the behavior of the voltage across R_7
11. vary R_{V3} , and note the conduction angle of the output signal

2.4.3 Questions

1. How does the amplitude vary with frequency?
 - ☐ it remains constant
 - ☐ it is always zero

- ☐ it decreases
- ☒ it increases
- ☐ it has a square-wave behavior

The inductance L_1 separates the bias circuit, consisting of R_{V3} and R_5 , from the ac input signal. In fact the impedance of an inductance is proportional to frequency, so the higher the frequency, the higher will be reactance of L_1 , and the better will be the separation. The capacitor C_4 is used to short-circuit any remaining signals across L_1 .

2. What happens to the output voltage on R_7 as the base bias of T_1 is continuously reduced?
 - ☐ the peak amplitudes of the output signal increase
 - ☒ the amplitude of the output peaks decrease
 - ☐ the output signal frequency progressively increases
 - ☐ there is no output change
 - ☐ the phase shift between the input and output signal progressively increases
3. Comparing the conduction angles obtained with different bias conditions, we can say that:
 - ☐ the conduction angle stays unchanged
 - ☐ the conduction angle increases when the voltage on the base of T_1 decreases
 - ☒ the conduction angle decreases when the voltage on the base of T_1 decreases

2.4.4 Tuned load

2.4.5 Procedures

1. Remove jumper J_{17} and insert J_{16} , to produce the circuit of figure:12

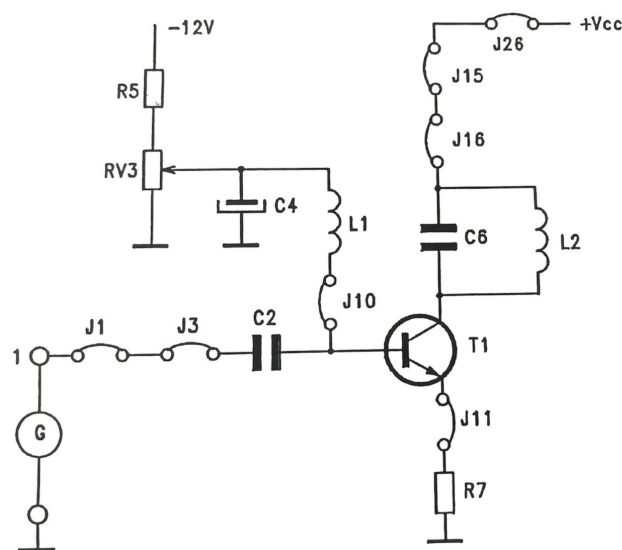


Figure 12: tuned load

2. adjust RV_3 to obtain a base bias voltage of 0 V
3. calculate the resonant frequency of the tuned circuit L_2 - C_6 , using the relation $f_o = \frac{1}{2\pi\sqrt{LC}}$, if $L_2 = 4\mu H$ and $C_6 = 680nF$
4. apply a sine signal of $2V_{pp}$ -amplitude and frequency f_o to the input
5. examine the wave-form of the signal across R_7 (proportional to the current through the transistor) and also the signal on the collector

- adjust the input frequency to obtain the max. amplitude on the T_1 collector (terminal 3)

When the frequency of the input signal is equal to the resonant frequency of the LC circuit, the output signal amplitude is max and the waveform is nearly distortionless. The amplifier is said to be tuned.

- slowly decrease the input signal frequency, until it is halved, while observing the behavior of the output signal on the T_1 collector (terminal 13)
- in particular, analyze what happens when the input frequency near $\frac{f_o}{2}$

As the frequency decreases, the amplitude of the output voltage slowly drops if $\frac{f_o}{2} < f < f_o$, the output has a behavior which is not sinusoidal anymore. Continuing to reduce the frequency, the second harmonic of the input signal clearly appears

As the output frequency in this case is double the input signal frequency, the circuit can be used as frequency multiplier.

2.4.6 Questions

Turn switch S_5 "ON"

- Noting the change in amplifier operation, we can say that:
 - ☒ the tuned output circuit has been changed
 - ☐ the base bias has been changed
 - ☐ the resistance R_7 has been reduced
 - ☐ the power supply voltage VCC has been decreased
 - ☐ the transistor T_1 has been short-circuited between base and collector

Turn S_5 "OFF"

2.5 Summary Questions

- The operation in class C of an amplifier is characterized by conduction angles:
 - ☐ greater than 180 degrees
 - ☐ equal to 180 degrees
 - ☒ less than 180 degrees
 - ☐ equal to 360 degrees
 - ☐ equal to 270 degrees
- Class C amplification produces a signal distortion which is:
 - ☐ very small
 - ☒ very big
 - ☐ similar to the one produced by class A
 - ☐ similar to the one produced by class B
 - ☐ similar to the one produced by class AB
- The efficiency of a class C amplifier :
 - ☒ depends on the conduction angle and takes very high values on average
 - ☐ is always very low
 - ☐ is always equal to 1

☐ is close to 25%

☐ is equal to 50%

4. A tuned load in an amplifiers operating in class C can be used to produce:

☐ a frequency divider

☒ a frequency multiplier

☐ a half-wave rectifier

☐ a voltage stabilizer

☐ a current limiter

3 Errors

Errors refer to the deviations or differences between the measured value and the true value of a quantity.

Errors can arise due to limitations in the equipment, environmental factors, the method of measurement, or human factors, and they impact the accuracy and reliability of results.

3.1 Sources of Errors

1. Oscillators: Potential sources of errors include:

- (a) Increased resistance within the connecting probes/wires
- (b) Power fluctuations within some components
- (c) Loose connections

2. Class C Amplifiers: Potential sources of errors include:

- (a) biasing issues
- (b) component tolerances
- (c) temperature effects
- (d) mismatched load
- (e) loose connections

3.2 Error minimization

3.3 Error Minimization

To improve the accuracy of the experimental results and reduce errors, specific steps can be taken in circuit design and setup. The following are recommended practices for minimizing errors in different components used in the experiment:

1. **Oscillators**

- **Limit Resistive Elements:** Reducing the number of resistive elements in the circuit helps minimize potential sources of resistance-based error and improves signal stability.

- **Ensure Tight Connections:** Loose connections can cause fluctuating voltages and other errors in the oscillator circuit. Ensuring all connections are secure reduces noise and improves the reliability of the signal.

2. Class C Amplifiers

- **Calibrate Devices Accurately:** Regular calibration of measuring devices and components within the Class C amplifier circuit is essential to minimize calibration-related errors, which can lead to inaccuracies in gain and frequency response.

4 Conclusion

By constructing and analyzing these circuits, we gained hands-on experience with signal generation and amplification. The experiments provided insights into the importance of minimizing errors through careful circuit design and calibration.

Overall, this practical approach enhanced our understanding of analogue electronics, reinforcing theoretical knowledge and highlighting the applications and limitations of oscillators and Class C amplifiers in real-world scenarios.

References

- [1] D. A. Bell, *Electronic Devices and Circuits*. New York: Oxford University Press, 5th ed., 2008.
- [2] A. S. Sedra and K. C. Smith, *Microelectronic Circuits*. New York: Oxford University Press, 7th ed., 2015.