IC Design HW2

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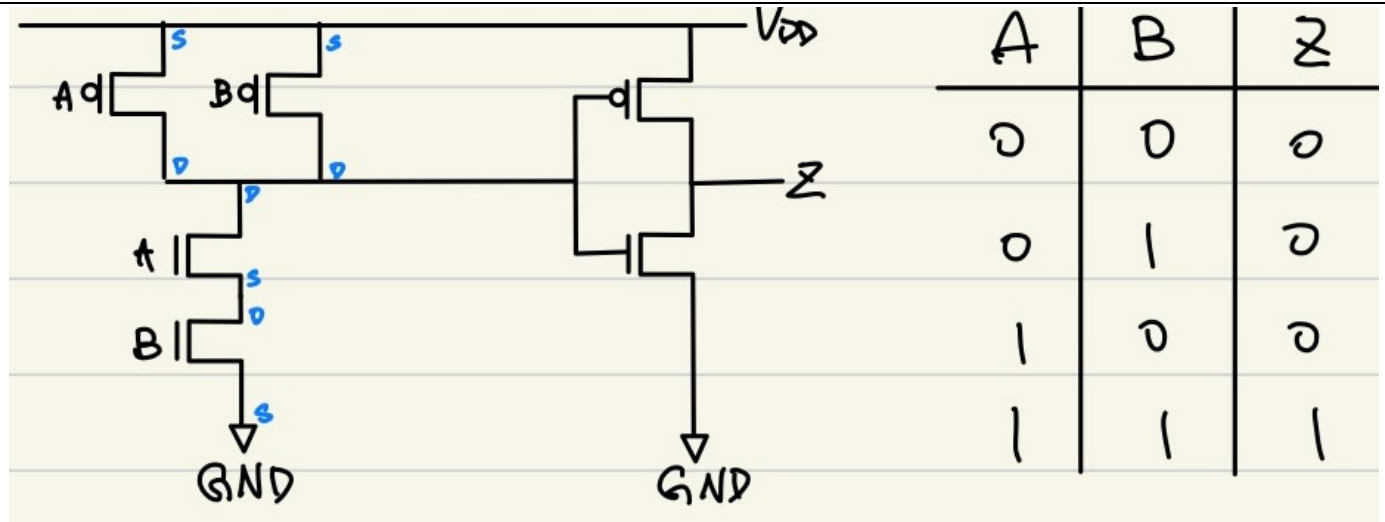
(6) And2
Input waveform: v(ina), v(inb)
Output waveform: v(out)
Cells: nand2, inverter

Transistor-level diagram & Truth table

A	B	Z
0	0	0
0	1	0
1	0	0
1	1	1
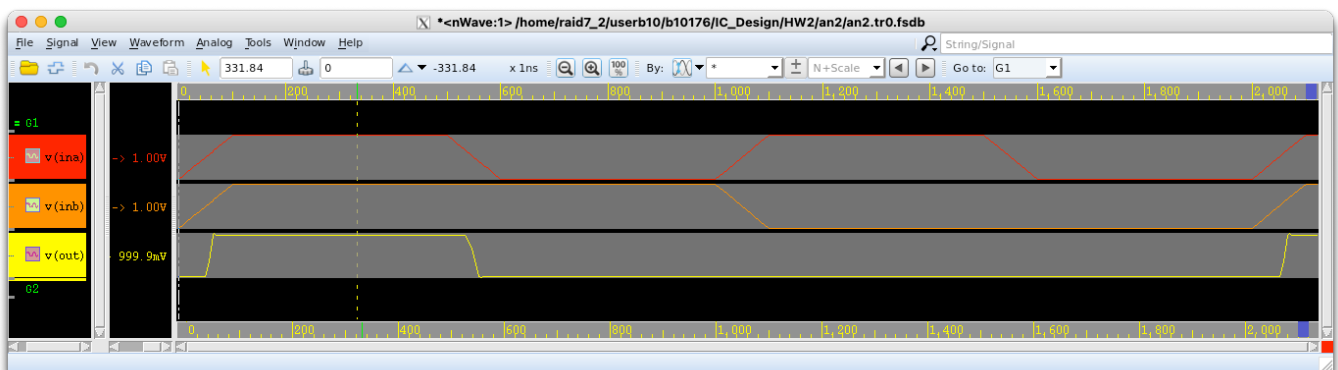
Waveform

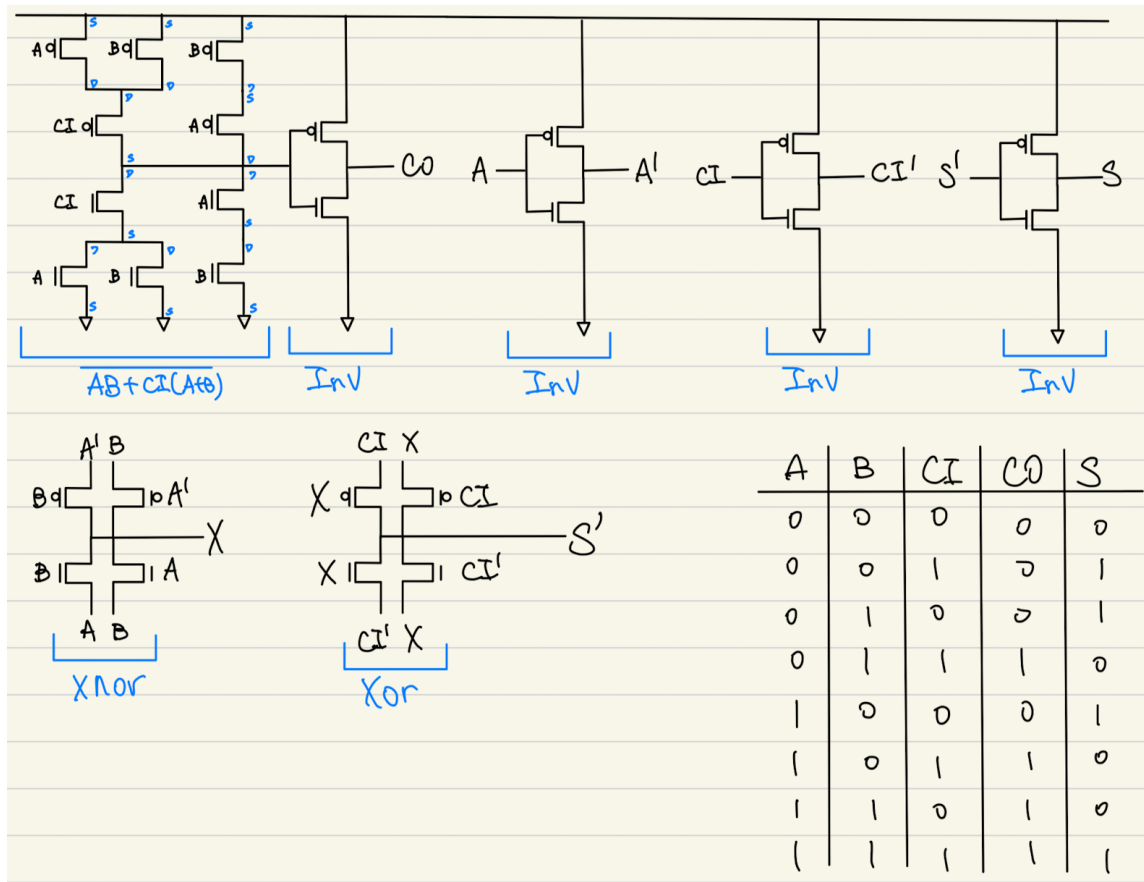| (10) FA1 |
|---|

Input waveform: v(ina), v(inb), v(inci)

Output waveform: v(outco), v(outs)

Cells: (AB+CI(A+B))', inverter*4, xnor, xor

<div align="center">Transistor-level diagram & Truth table</div>

A	B	CI	CO	S
0	0	0	0	0
0	0	1	0	1
0	1	0	0	1
0	1	1	1	0
1	0	0	0	1
1	0	1	1	0
1	1	0	1	0
1	1	1	1	1

<div align="center">Waveform</div>