



Assignment 1: Chapter 1 - Introduction

Total Points: 100; **and Deadline:** January/29/2023, 11:59 PM.

Note – Cheating and Plagiarism: Cheating and plagiarism are not permitted in any form and they cause certain penalties. The instructor reserves the right to fail culprits.

Deliverable: All of your responses to the questions of assignment should be included in a single compressed file to be uploaded to the Gannon University (GU) – Blackboard Learn environment.

Question. Provide short answers (i.e., no more than five lines on average with the font size of 12) for the following items. The grade for each item is **20 points**.

1. Specify different types of secure processor architectures and the differences between them.
2. Mention the factors that have caused the increase demand for having secure processor architectures.
3. Determine overarching design goal of secure architectures.
4. Discuss the impossibility of providing security solely based on software for processor architectures.
5. Specify different types of side-channel information and their usages.