

Course Content

[Build Content](#)[Assessments](#)[Tools](#)[Partner Content](#)

Ask the Professor Course Related Questions

Enabled: Statistics Tracking

Please use this discussion forum to ask me questions related to course material or expectations. By asking questions here, we can share the question and the answer with the full class. To post a question, click on Create Thread. You may want to subscribe to the Discussion Forum in order to receive an email message that something has been posted in the forum. You can subscribe to the forum by clicking on the subscribe button.



CYENG225_SP23_Lecture01_Chapter01

Enabled: Statistics Tracking

Attached Files: [Chapter 01.pdf](#) (1.354 MB)
 [CYENG225_Lecture_10Jan2023.pdf](#) (337.489 KB)



CYENG225 Assignment1_SP23

Enabled: Statistics Tracking

Attached Files: [CYENG225_Assignment1_SP23.pdf](#) (137.434 KB)



CYENG225_SP23_Lecture02_Chapter02

Enabled: Statistics Tracking

Attached Files: [Chapter 02.pdf](#) (9.504 MB)
 [CYENG225_Lecture_12Jan2023.pdf](#) (351.691 KB)



CYENG225_SP23_Lecture03_Chapter03

Enabled: Statistics Tracking

Attached Files: [Chapter 03.pdf](#) (8.464 MB)
 [CYENG225_Lecture_Chapter03.pdf](#) (744.951 KB)



CYENG225 Assignment2_SP23

Enabled: Statistics Tracking

Attached Files: [CYENG225_Assignment2_SP23.pdf](#) (600.706 KB)
 [GEM5 HELP.pdf](#) (66.473 KB)
 [gem5 Installation Guide.pdf](#) (158.244 KB)
 [GEM5 Installation.pdf](#) (96.944 KB)
 [GEM5 Summary.pdf](#) (71.557 KB)
 [Links for gem5 Installation.pdf](#) (113.777 KB)
 [gem5 Step-by-Step Installation.pdf](#) (266.676 KB)
 [gem5 Step-by-Step Installation.webm](#) (136.582 MB)



Digital Logic Design (Review) - Additional Materials

Enabled: Statistics Tracking

Attached Files: [Readings_01.pdf](#) (2.374 MB)
 [Readings_02.pdf](#) (214.802 KB)
 [Readings_03.pdf](#) (3.114 MB)
 [Slides_01.pdf](#) (1.395 MB)
 [Slides_02.pdf](#) (608.788 KB)
 [Slides_03.pdf](#) (1.216 MB)
 [Slides_04.pdf](#) (1.048 MB)



Computer Architecture (Review) - Additional Materials

Enabled: Statistics Tracking

Attached Files: [Reading.pdf](#) (50.128 MB)
 [Slides_01.pdf](#) (12.678 MB)
 [Slides_02.pdf](#) (977.703 KB)
 [Slides_03.pdf](#) (6.078 MB)



VLSI Design (Introduction) - Additional Materials

Enabled: Statistics Tracking

Attached Files: [Reading.pdf](#) (110.15 MB)
 [Slides_01.pdf](#) (651.984 KB)
 [Slides_02.pdf](#) (1.177 MB)
 [Slides_03.pdf](#) (1.725 MB)
 [Slides_04.pdf](#) (289.396 KB)
 [Slides_05.pdf](#) (374.158 KB)
 [Slides_06.pdf](#) (337.735 KB)
 [Slides_07.pdf](#) (858.959 KB)
 [Slides_08.pdf](#) (880.039 KB)



CYENG225 Assignment3 SP23

Enabled: Statistics Tracking

Attached Files: [CYENG225_Assignment3_SP23.pdf](#) (152.026 KB)
 [FPGA Board Documents.zip](#) (19.331 MB)
 [Links for Verilog HDL.pdf](#) (67.923 KB)
 [UCF-EEE3342LabManual.pdf](#) (8.305 MB)
 [Verilog_Presentation_01.pdf](#) (1.37 MB)
 [Verilog_Presentation_02.pdf](#) (1.802 MB)
 [Verilog_Presentation_03.pdf](#) (980.672 KB)
 [Verilog_Presentation_04.pdf](#) (947.093 KB)
 [Verilog_Tutorial.pdf](#) (876.249 KB)
 [Vivado_Tutorial.pdf](#) (3.697 MB)
 [Part_X.v](#) (774 B)
 [Part_X_Sim.v](#) (882 B)
 [Vivado Board Configuration.jpg](#) (126.748 KB)



CYENG225_FirstExam_Scope_SP23

Enabled: Statistics Tracking

Attached Files: [CYENG225_FirstExam_Scope_SP23.pdf](#) (103.989 KB)



First Exam

Enabled: Statistics Tracking



CYENG225 Assignment4 SP23

Enabled: Statistics Tracking

Attached Files: [CYENG225_Assignment4_SP23.pdf](#) (228.438 KB)
 [FPGA-gem5 Environment.zip](#) (15.097 MB)
 [Hardware Trojan for Cryptography.zip](#) (24.818 MB)



CYENG225_SP23_Lecture04_Chapter04

Enabled: Statistics Tracking

Attached Files: [Chapter 04.pdf](#) (4.041 MB)
 [CYENG225_Lecture_Chapter04.pdf](#) (362.7 KB)



CYENG225_SP23_Lecture05_Chapter05

Enabled: Statistics Tracking

Attached Files:  [Chapter 05.pdf](#) (5.27 MB)
 [CYENG225_Lecture_Chapter05.pdf](#) (742.561 KB)



CYENG225_SP23_Lecture06_Chapter06

Enabled: Statistics Tracking

Attached Files:  [Chapter 06.pdf](#) (4.86 MB)
 [CYENG225_Lecture_Chapter06.pdf](#) (592.651 KB)



CYENG225_SP23_Lecture07_Chapter07

Enabled: Statistics Tracking

Attached Files:  [Chapter 07.pdf](#) (4.603 MB)
 [CYENG225_Lecture_Chapter07.pdf](#) (451.504 KB)



CYENG225_IDE_Tutorials_SP23

Enabled: Statistics Tracking

Attached Files:  [CYENG225_IDE_Tutorials_SP23.pdf](#) (173.563 KB)



CYENG225_UsefulResources_SP23

Enabled: Statistics Tracking

Attached Files:  [CYENG225_UsefulResources_SP23.pdf](#) (269.133 KB)



CYENG225_SP23_Lecture08_Chapter08



Enabled: Statistics Tracking

Attached Files:  [Chapter 08.pdf](#) (8.919 MB)
 [CYENG225_Lecture_Chapter08.pdf](#) (805.1 KB)



CYENG225_SP23_Lecture09_Chapter09

Enabled: Statistics Tracking

Attached Files:  [Chapter 09.pdf](#) (4.515 MB)
 [CYENG225_Lecture_Chapters_09_10.pdf](#) (234.028 KB)



CYENG225_SP23_Lecture10_Chapter10

Enabled: Statistics Tracking

Attached Files:  [Chapter 10.pdf](#) (5.897 MB)

 [CYENG225_Lecture_Chapters_09_10.pdf](#) (234.028 KB)



CYENG225_SecondExam_Scope_SP23

Enabled: Statistics Tracking

Attached Files:  [CYENG225_SecondExam_Scope_SP23.pdf](#) (104.864 KB)



CYENG225_Assignment5_SP23

Enabled: Statistics Tracking

Attached Files:  [CYENG225_Assignment5_SP23.pdf](#) (465.506 KB)

 [PUF Circuit 1.pdf](#) (302.456 KB)

 [PUF Circuit 2.pdf](#) (232.176 KB)

 [PUF Circuit 3.pdf](#) (119.146 KB)

 [PUFCircuit3_VHDL.zip](#) (4.686 KB)



Second Exam

Enabled: Statistics Tracking



CYENG225_Assignment6_SP23

Enabled: Statistics Tracking

Attached Files:  [CYENG225_Assignment6_SP23.pdf](#) (250.777 KB)



CYENG225_Assignment5_SP23

Enabled: Statistics Tracking



CYENG225_ThirdExam_Scope_SP23

Enabled: Statistics Tracking

Attached Files:  [CYENG225_ThirdExam_Scope_SP23.pdf](#) (106.361 KB)