



### **Links on Tutorials and Online Simulator for Verilog HDL**

#### Tutorial:

1. [Verilog - Wikipedia](#)
- 2 **(Important)**. [Welcome To Verilog Page \(asic-world.com\)](#)
3. [Verilog Tutorial - javatpoint](#)
4. [VLSI Design - Verilog Introduction \(tutorialspoint.com\)](#)
5. [Verilog Tutorial for beginners \(referencedesigner.com\)](#)
6. [fpga4fun.com - HDL tutorials](#)
7. [so-logic](#)

#### Online Simulator:

1. [D flip-flop - EDA Playground](#)
2. [Online Verilog Compiler \(tutorialspoint.com\)](#)
3. [Online VERILOG Compiler - Online VERILOG Editor - Run VERILOG Online - Online VERILOG Runner \(jdoodle.com\)](#)