
Report : area

Design: s35932_bench Version: H-2013.03-SP5-3 Date: Sat Nov 21 16:44:36 2015

Library(s) Used:

saed32rvt_tt1p05v25c (File: /lhome/shayan/test_dir/models/saed32rvt_tt1p05v25c.db)

69 Number of ports: Number of nets: 10746 Number of cells: 10549 Number of combinational cells: 8821 1728 Number of sequential cells: Number of macros/black boxes: 0 5871 Number of buf/inv: Number of references: 16

 Combinational area:
 20516.791094

 Buf/Inv area:
 11190.214563

 Noncombinational area:
 12296.503510

 Macro/Black Box area:
 0.000000

 Net Interconnect area:
 6638.379074

Total cell area: 32813.294603 Total area: 39451.673677

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