```
****************************************
Report : area
Design : s35932_bench
Version: K-2015.06-SP3
Date   : Sat Nov 21 16:47:31 2015
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Library(s) Used:

    saed32rvt_tt1p05v25c (File: /lhome/shayan/test_dir/models/saed32rvt_tt1p05v25c.db)

Number of ports:                 71
Number of nets:               12861
Number of cells:              12664
Number of combinational cells:    10936
Number of sequential cells:        1728
Number of macros/black boxes:         0
Number of buf/inv:                 7986
Number of references:             23

Combinational area:         29687.577380
Buf/Inv area:               20349.310215
Noncombinational area:      12296.503510
Macro/Black Box area:           0.000000
Net Interconnect area:       7436.318317

Total cell area:            41984.080889
Total area:                 49420.399206
1
```