**CACTI**

1. An integrated cache and memory access time, cycle time, area, leakage, and dynamic power model.
2. Usage 🡪 Evaluation of the memory system organizations.
3. Online Version 🡪 <http://quid.hpl.hp.com:9081/cacti>
4. Related Formulas:

* Hit Time and Miss Time are expressed in terms of number of Clock Cycles (CCs)