

## University Program Questionnaire

This form is intended to increase our understanding of your university's use of the licensed materials available through membership in the Cadence University Program. This information will help us improve our program as well as better recognize what engagement opportunities might be of interest to your faculty and students.

Member Information		
University: Gannon University (GU)	Department: Department of Electrical and Cyber Engineering (ECE)	
Product Families in Use		
<i>Select all of the following which apply to your university's research or classroom instruction:</i>		
<i>Research / Classroom</i> <input checked="" type="checkbox"/> <input checked="" type="checkbox"/> Allegro® <input checked="" type="checkbox"/> <input checked="" type="checkbox"/> AWR® <input checked="" type="checkbox"/> <input checked="" type="checkbox"/> Celsius™ <input checked="" type="checkbox"/> <input checked="" type="checkbox"/> Clarity™ <input checked="" type="checkbox"/> <input checked="" type="checkbox"/> Conformal® <input checked="" type="checkbox"/> <input checked="" type="checkbox"/> Dracula® <input checked="" type="checkbox"/> <input checked="" type="checkbox"/> EMX® <input checked="" type="checkbox"/> <input checked="" type="checkbox"/> Genus™	<i>Research / Classroom</i> <input checked="" type="checkbox"/> <input checked="" type="checkbox"/> Indago™ <input checked="" type="checkbox"/> <input checked="" type="checkbox"/> Innovus™ <input checked="" type="checkbox"/> <input checked="" type="checkbox"/> JasperGold® <input checked="" type="checkbox"/> <input checked="" type="checkbox"/> Liberate™ <input checked="" type="checkbox"/> <input checked="" type="checkbox"/> Modus DFT <input checked="" type="checkbox"/> <input checked="" type="checkbox"/> Pegasus™ <input checked="" type="checkbox"/> <input checked="" type="checkbox"/> Physical Verification System <input checked="" type="checkbox"/> <input checked="" type="checkbox"/> Quantus™	<i>Research / Classroom</i> <input checked="" type="checkbox"/> <input checked="" type="checkbox"/> Sigrity™ <input checked="" type="checkbox"/> <input checked="" type="checkbox"/> Spectre® <input checked="" type="checkbox"/> <input checked="" type="checkbox"/> Stratus™ <input checked="" type="checkbox"/> <input checked="" type="checkbox"/> Tempus™ <input checked="" type="checkbox"/> <input checked="" type="checkbox"/> Verifault <input checked="" type="checkbox"/> <input checked="" type="checkbox"/> Virtuoso® <input checked="" type="checkbox"/> <input checked="" type="checkbox"/> Voltus™ <input checked="" type="checkbox"/> <input checked="" type="checkbox"/> Xcelium™
Areas of Interest		
<i>Select all of the following which apply to your university's research or classroom instruction:</i>		
<i>Research / Classroom</i> <input checked="" type="checkbox"/> <input checked="" type="checkbox"/> 3D-IC Design <input checked="" type="checkbox"/> <input checked="" type="checkbox"/> 5G Development <input checked="" type="checkbox"/> <input checked="" type="checkbox"/> Aerospace and Defense <input checked="" type="checkbox"/> <input checked="" type="checkbox"/> AI/Machine Learning <input checked="" type="checkbox"/> <input checked="" type="checkbox"/> Analog Layout Design <input checked="" type="checkbox"/> <input checked="" type="checkbox"/> Automotive <input checked="" type="checkbox"/> <input checked="" type="checkbox"/> Biomedical	<i>Research / Classroom</i> <input checked="" type="checkbox"/> <input checked="" type="checkbox"/> Circuit Design/Simulation <input checked="" type="checkbox"/> <input checked="" type="checkbox"/> Computational Software <input checked="" type="checkbox"/> <input checked="" type="checkbox"/> Design for Test <input checked="" type="checkbox"/> <input checked="" type="checkbox"/> Digital Implementation <input checked="" type="checkbox"/> <input checked="" type="checkbox"/> Electromagnetic Solutions <input checked="" type="checkbox"/> <input checked="" type="checkbox"/> FPGA Development <input checked="" type="checkbox"/> <input checked="" type="checkbox"/> Low-Power Verification	<i>Research / Classroom</i> <input checked="" type="checkbox"/> <input checked="" type="checkbox"/> PCB Design <input checked="" type="checkbox"/> <input checked="" type="checkbox"/> Photonics <input checked="" type="checkbox"/> <input checked="" type="checkbox"/> RF/Microwave Design <input checked="" type="checkbox"/> <input checked="" type="checkbox"/> Thermal Solutions <input checked="" type="checkbox"/> <input checked="" type="checkbox"/> Verification <input checked="" type="checkbox"/> <input checked="" type="checkbox"/> VLSI <input type="checkbox"/> <input type="checkbox"/> Other: _____
Active Users		
<i>Professors at your university using Cadence tools in their research or instruction:</i>		
Names:	Academic Email Address:	
Dr. Shayan Taheri	taheri001@gannon.edu	
Form Prepared By		
Name: Dr. Shayan Taheri	Position: Assistant Professor, ECE Dept. Date: 09/05/2022	