Automated and modular refinement reasoning for concurrent programs

var x:int;

Figure 1: Program 1

Abstract

Abstract goes here.

1. Introduction

2. Overview

3. Overview

We present an overview of the CIVL language through a sequence of examples. Figure 1 shows Program 1 containing a procedure p executing concurrently with another procedure q. An execution of a CIVL program is non-preemptive; a thread explicitly yields control to the scheduler via the yield statement following which execution continues on a nondeterministically chosen thread. The yield statement has a local assertion φ attached to it. The yielding thread must establish φ when it yields and the execution of other threads must preserve φ ; these two requirements are usually known as sequential correctness and non-interference, respectively. To check these requirements, the CIVL verifier creates verification conditions, whose number is at most quadratic in the number of yield statements in the program. For example, in Program 1 each yield predicate in p must be checked against the action x:= x + 3 in q.

CIVL requires that a procedure that may potentially execute a yield statement during its execution must be annotated as yielding. This annotation is checked in a manner similar to the checking of modifies clauses; if a procedure is labeled as yielding so must all of its callers. A procedure marked as yielding is exempt from providing a modifies clause; the presence of yielding allows the caller to conclude that any global variable could have changed potentially as a result of modification by a concurrently-executing thread. A procedure not labeled as yielding is called atomic; such a procedure must supply a modifies clause as usual.

From quadratic to linear verification conditions. Figure 2 shows Program 2, a variation of Program 1 in which the procedure yield_x contains a single yield statement and p calls yield_x

```
yielding procedure yield_x(n:int)
  requires x >= n;
  ensures x >= n;
{
   yield x >= n;
}

yielding procedure p()
  requires x >= 5;
  ensures x >= 8;
{
   call yield_x(5); x := x + 1;
   call yield_x(6); x := x + 1;
   call yield_x(7); x := x + 1;
}
```

Figure 2: Program 2

```
yielding procedure yield_x(n: int)
  requires x >= n;
  ensures x >= n;
{
   yield x >= n;
}

yielding procedure p()
  requires x >= 5;
  ensures x >= 8;
{
   call yield_x(x); x := x + 1;
   call yield_x(x); x := x + 1;
}
```

Figure 3: Program 3

instead of yielding directly. If the calls to yield_x are inlined in Program 2, then we will get Program 1. Both Program 1 and 2 are verifiable in CIVL but the cost of verifying Program 2 is less because it has fewer yield statements. In fact, if it is possible to capture all interference in a concurrent program in a single yield predicate, then the trick in Program 2 can be used to verify the program with a linear number of verification conditions.

Encoding rely-guarantee specifications. Figure 3 shows Program 3, yet another variation of Programs 1 and 2 which shows how to encode a rely-guarantee-style [8] (two-state invariant) proof using CIVL's one-state yield statements. The standard rely-guarantee specification to prove the assertions in p is that the environment of p may only increase x. We can encode this in CIVL by first ex-

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```
var x:int, y:int, z:int;
stable yielding procedure incr_x()
  ensures x \ge old(x) + 1;
  yield x \ge old(x);
  x := x + 1;
 yield x \ge old(x) + 1;
stable yielding procedure yield_y()
  ensures y >= old(y);
  yield y \ge old(y);
stable yielding procedure yield_z()
  ensures z \ge old(z);
 yield z \ge old(z);
}
yielding procedure p()
  requires x == 3 \&\& y == 5 \&\& z == 7;
  call incr_x() | yield_y() | yield_z();
  assert x >= 4 \&\& y >= 5 \&\& z >= 7;
```

Figure 4: Program 4

ploiting the trick in Program 2 to factor out the yield statement in a separate procedure and then passing the current value of x as a parameter to yield_x. In fact, our implementation of CIVL requires even less work; the value of x upon entering yield_x is available in the postcondition using the syntax old(x), allowing us to write yield_x without any parameter as follows:

```
yielding procedure yield_x()
  ensures x >= old(x);
{
  yield x >= old(x);
}
```

Parallel calls. Program 4 in Figure 4 illustrates the parallel call feature of CIVL, based on the standard Owicki-Gries rules for parallel composition of threads. The statement call incr_x() | yield_y() | yield_z() in p creates three threads executing incr_x, yield_y, and yield_z respectively, yields control to the scheduler, and blocks until all three threads have terminated. For a procedure to be invoked in a parallel call, it must be annotated as stable. This annotation indicates to the CIVL verifier that the precondition and postcondition of the procedure must be stable against interference. This requirement ensures that it is safe to assume the precondition in the callee and the postcondition in the caller.

The threads created by p for yield_y and yield_z are not doing any interesting computation; their only purpose is to make available to their parent the conjunction of their respective postconditions (following Owicki-Gries rules for parallel composition). In this example, the postconditions of yield_y and yield_z preserve information about variables y and z that would otherwise be lost during the call to incr_x, whose postcondition only supplies information about x even though its yield statements potentially cause all global variables, including y and z, to change. This example shows that CIVL allows modular proof structuring by factoring out

```
type Tid;
var linear alloc:[Tid]bool;
const nil: Tid;
procedure Allocate() returns (linear tid: Tid);
  modifies alloc;
  ensures tid != nil;
var a:[Tid]int;
yielding procedure main()
  var linear tid: Tid;
  while (true) {
    call tid := Allocate();
    async call P(tid);
    yield true;
yielding procedure P(linear tid: Tid)
  requires tid != nil;
  ensures a[tid] == old(a)[tid] + 1;
  var t: int;
  t := a[tid];
  yield t == a[tid];
  a[tid] := t + 1;
```

Figure 5: Program 5

yield assertions into a collection of procedures; the declaration of incr_x can focus on changes to x, without having to explicitly preserve invariants about all other variables in the program.

Linear variables. Program 5 in Figure 6 introduces linear variables, a feature of CIVL that is useful for encoding disjointness among values contained in different variables. This example uses this feature for encoding the concept of an identifier that is unique to each thread. Program 5 contains a shared global array a indexed by an uninterpreted type Tid representing the set of thread identifiers. A collection of threads are executing procedure P concurrently. The identifier of the thread executing P is passed in as the parameter tid. A thread with identifier tid owns a [tid] and can increment it without danger of interference. The yield assertion t == a[tid] in P indicates this expectation, yet it is not possible to prove it unless the reasoning engine knows that the value of tid in one thread is distinct its value in a different thread.

Instead of building a notion of thread identifiers into CIVL, we provide a more primitive and general notion of linear variables. The CIVL type system ensures that values contained in linear variables cannot be duplicated. Consequently, the parameter tid of distinct concurrent calls to P are known to be distinct; the CIVL verifier exploits this invariant while checking for non-interference.

Program 5 also shows the mechanisms of allocation of thread identifiers, based on the use of global variable alloc, the constant nil, and the procedure Allocate. Section ?? describes values and linear variables like nil and alloc in more detail.

Refinement.

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```
var Color:int:
                                                                         Program Syntax
                                                                                        Globals \subseteq VarName
                                                                                  g \in
procedure {:yields} SetColGrayIfWhite({:cnst "tid"} tid:int)
                                                                                 tl \in
                                                                                       ThreadLocals \subseteq VarName
ensures {:atomic} [if (Color == WHITE())
                                                                                  l \in Locals \subseteq VarName
                               Color := GRAY();]
                                                                                        Var = Globals \cup ThreadLocals \cup Locals
{
                                                                                  v \in
                                                                                        Value
  call cNoLock:= GetColorNoLock();
                                                                                 \sigma \in
                                                                                        Store = Var \rightarrow Value
  call YieldColorOnlyGetsDarker();
                                                                                 G \in StoreGlobals = Globals \rightarrow Value
  if (cNoLock == WHITE()) {
                                                                                TL \in StoreThreadLocals = ThreadLocals \rightarrow Value
         call L_SetColorToGrayIfWhite(tid);
                                                                                 L \in StoreLocals = Locals \rightarrow Value
  }
                                                                          e, \phi, \psi, \rho \ \in \ \mathit{StateExpr} = 2^{\mathit{Store}}
}
                                                                               \alpha,\beta \ \in \ \mathit{TransExpr} = 2^{(\mathit{Store},\mathit{Store})}
                                                                                 le \ \in \ LocalStateExpr = 2^{StoreLocals}
procedure {:yields} YieldColorOnlyGetsDarker()
                                                                                 P \in ProcName
  ensures Color >= old(Color);
                                                                                 A \in ActionName
procedure {:yields} L_SetColGrayIfWhite({:cnst "tid"} tid:int)^{bs} \in ProcName \rightarrow Stmt
                                                                                       Mover = \{B, R, L, N\}
ensures {:atomic} [if (Color == WHITE())
                                                                                 as \in ActionName \rightarrow (StateExpr, TransExpr, Mover)
                               Color := GRAY();]
                                                                                ps \in ProcName \rightarrow (StateExpr, 2^{ThreadLocals}, StateExpr)
                                                                                 rs \in ProcName \rightarrow ActionName
  call AcquireLock(tid);
                                                                                 \lambda \in \mathit{LinearVars} = 2^{\mathit{Globals} \cup \mathit{ThreadLocals}}
  call cLock := GetColorLocked(tid);
                                                                                 ls \in (ActionName \cup ProcName) \rightarrow (LinearVars, LinearVars)
  if (cLock == WHITE()) {
                                                                                  a \in InsideABlock := a^+ \mid a^-
     call SetColorLocked(tid, GRAY());
                                                                                  r \in InsideRefinement ::= r^+ | r^-
  call ReleaseLock(tid);
                                                                                         s \in Stmt ::= skip \mid assert \ le \mid yield \ e, \lambda \mid
                                                                                                         call\ A \mid call\ P \mid async\ P \mid
                                                                                                         ablock \{e, \lambda\} s \mid s; s \mid
                       Figure 6: Program 6
                                                                                                         if le then s else s
                                                                                                         while \{e, \alpha\} le do s
                                                                                 SS \in StmtStack ::= s \mid (L, SS) \mid SS; s
   A simple programming language
                                                                                     T \in Thread ::= (TL, (L, SS))
```

5. Verification

5.1 Type checking

5.2 Safety

Non-interference. Let Yields be the set of yield predicates, preconditions, and postconditions in the program. Let Ablocks be the set of atomic blocks in the program except those inside the bodies of procedures in dom(rs). Let $FV \subseteq VarName \setminus Var$ be a set of fresh variables and Λ be a one-one substitution function from $ThreadLocals \cup Locals$ to FV. Let $\Lambda(\phi)$ represent the result of applying Λ to the expression ϕ . For each predicate $(\phi, \lambda_y) \in Yields$ and for each atomic block ablock $\{e, \lambda_a\}$ $s \in Ablocks$, we prove the following judgment:

$$\{\Lambda(\phi) \land e \land disjoint(\{\Lambda(x) \mid x \in \lambda_u\} \cup \{x \mid x \in \lambda_a\})\} s \{\Lambda(\phi)\}$$

For each predicate $\phi \in Yields$ and for each $P \in dom(rs)$ such that $as(rs(P)) = (\rho, \alpha, m)$, we prove the following to be unsatisfiable:

$$\Lambda(\phi) \circ \rho \circ \alpha \circ \neg \Lambda(\phi)$$

5.3 Refinement

5.4 Yield sufficiency

Commutativity. Let $FV_1, FV_2 \subseteq VarName \setminus Var$ be two sets of disjoint fresh variables. Let Λ_1 and Λ_2 be one-one substitution functions from $ThreadLocals \cup Locals$ to FV_1 and FV_2 respectively. For all $A_1, A_2 \in ActionName$ such that $as(A_1) = (\rho_1, \alpha_1, m_1)$ and $as(A_2) = (\rho_2, \alpha_2, m_2)$, if $m_1 \in \{B, R\}$ or $m_2 \in \{B, L\}$ then prove the following valid:

```
if \ le \ then \ s \ else \ s \mid \\ while \ \{e,\alpha\} \ le \ do \ s \\ SS \in StmtStack \ ::= \ s \mid (L,SS) \mid SS; s \\ T \in Thread \ ::= \ (TL,(L,SS)) \\ SC \in StmtCtxt \ ::= \ []_{Stmt} \mid SC; s \\ SSC \in StmtStackCtxt \ ::= \ ([]_{Locals},SC) \mid (L,SSC) \mid SSC; s \\ TC \in ThreadCtxt \ ::= \ ([]_{ThreadLocals},([]_{Locals},SC)) \mid \\ ([]_{ThreadLocals},(L,SSC)) \\ YT \in YieldingThread \ ::= \ TC[TL][L][yield \ e,\lambda] \\ PC \in ProgCtxt \ ::= \ \overrightarrow{YT} \cdot TC \cdot \overrightarrow{YT} \\ \end{cases}
```

Figure 7: Syntax

 $Prog \in Program = (bs, as, ps, rs, ls, G, \overrightarrow{T})$

$$\frac{(G \cup TL \cup L, s) \longrightarrow (G' \cup TL' \cup L', s')}{(G, PC[TL][L][s]) \longrightarrow (G', PC[TL'][L'][s'])} \qquad \text{(Program-Step)}$$

$$\frac{(G \cup TL \cup L, s) \text{ fails}}{(G, PC[TL][L][s]) \text{ fails}} \qquad \text{(Program-Fail)}$$

$$\frac{ps(P) = (\phi, M, \psi)}{(G, PC[TL][L][async P]) \longrightarrow (G, PC[TL][L][skip] \cdot T')} \qquad \text{(Async)}$$

$$\frac{(G, \overrightarrow{PC}[TL][L][async P]) \longrightarrow (G, \overrightarrow{PC}[TL][L][skip] \cdot T')}{(G, \overrightarrow{YT} \cdot (TL, (L, skip)) \cdot \overrightarrow{YT}') \longrightarrow (G, \overrightarrow{YT} \cdot \overrightarrow{YT}')} \qquad \text{(Thread-End)}$$

$$\frac{ps(P) = (\phi, M, \psi)}{(G, PC[TL][L][call P]) \longrightarrow (G, PC[TL][L][(L, SS)])} \qquad \text{(Call)}$$

 $(\Lambda_1(\rho_1) \wedge \Lambda_2(\rho_2)) \circ (\Lambda_1(\alpha_1) \wedge Same(FV_2)) \circ (\Lambda_1(\alpha_2) \wedge Same(FV_1))$ $\Rightarrow (\Lambda_1(\alpha_2) \wedge Same(FV_1)) \circ (\Lambda_1(\alpha_1) \wedge Same(FV_2))$ Figure 8: Operational semantics for program

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$$\begin{array}{l} \sigma \vdash le \rightarrow true \\ \hline (\sigma, assert \ le) \longrightarrow (\sigma, skip) \\ \hline \sigma \vdash le \rightarrow false \\ \hline (\sigma, assert \ le) \ fails \end{array} \ (\text{Assert-False}) \end{array}$$

$$\frac{as(A) = (\rho, \alpha, m) \quad (\sigma, \sigma') \vdash \alpha}{(\sigma, call \ A) \longrightarrow (\sigma', skip)} \tag{Atomic}$$

$$(\sigma, yield \ e, \lambda) \longrightarrow (\sigma, skip)$$
(YIELD)

$$\frac{}{(\sigma, ablock \{e, \lambda\} s) \longrightarrow (\sigma, s)}$$
 (AtomicBlock)

$$\frac{}{(\sigma, skip; s) \longrightarrow (\sigma, s)}$$
(SEQ)

$$\frac{\sigma \vdash le \to true}{(\sigma, if \ le \ then \ s_1 \ else \ s_2 \) \longrightarrow (\sigma, s_1)} \tag{If-True}$$

$$\frac{\sigma \vdash le \rightarrow false}{(\sigma, \textit{if le then } s_1 \textit{ else } s_2 \,) \longrightarrow (\sigma, s_2)} \tag{IF-False}$$

$$\frac{\sigma \vdash le \rightarrow false}{(\sigma, while \{e, \alpha\} \ le \ do \ s) \longrightarrow (\sigma, skip)}$$
 (While-False)

$$\frac{\sigma \vdash le \rightarrow true}{(\sigma, while \{e, \alpha\} le \ do \ s) \longrightarrow (\sigma, s; while \{e, \alpha\} le \ do \ s)} \text{ (While-True)}$$

Figure 9: Operational semantics for statement

For all $A_1, A_2 \in ActionName$ such that $as(A_1) = (\rho_1, \alpha_1, m_1)$ and $as(A_2) = (\rho_2, \alpha_2, m_2)$, if $m_1 \in \{B, R\}$ then prove the following unsatisfiable:

$$\Lambda_1(\rho_1) \circ (\Lambda_2(\rho_2) \circ \Lambda_2(\alpha_2)) \circ \neg \Lambda_1(\rho_1)$$

For all $A_1, A_2 \in ActionName$ such that $as(A_1) = (\rho_1, \alpha_1, m_1)$ and $as(A_2) = (\rho_2, \alpha_2, m_2)$, if $m_1 \in \{B, L\}$ then prove the following unsatisfiable:

$$\neg \Lambda_1(\rho_1) \circ (\Lambda_2(\rho_2) \circ \Lambda_2(\alpha_2)) \circ \Lambda_1(\rho_1)$$

5.5 Program refinement

5.6 Soundness

Theorem 1. Let Prog and Prog' be two programs and $AS \subseteq ActionName$ a set of action names such that the following conditions are satisfied:

- $1. \vdash Prog \ and \vdash Prog' \ and \ AS \vdash Prog \leadsto Prog'.$
- 2. All finite executions of Prog' are safe.
- 3. All infinite executions of Prog' are responsive.
- 4. The program Prog is commutativity-safe.
- 5. The program Prog is interference-free.
- 6. $\vdash_p Prog \ and \vdash_r Prog \ and \vdash_y Prog$.

Then all finite executions of Prog are safe.

5.7 Responsiveness

$$\frac{e \wedge le \Rightarrow f \geq 0}{\{e \rangle while \ \{e, \alpha, f\} \ le \ do \ s \ \{e \wedge \neg le\}} \frac{\{e \wedge le \Rightarrow f \geq 0 \quad s \leq (old(e \wedge le) \Rightarrow old(f) > f)}{\{e \rangle while \ \{e, \alpha, f\} \ le \ do \ s \ \{e \wedge \neg le\}}$$
 (While)

6. Implementation

7. A concurrent garbage collector

We have chosen to demonstrate the proposed verification methodology and tool on a realistic modern concurrent garbage collector.

$$\frac{\lambda; a \vdash skip : \lambda}{\lambda; a \vdash skip : \lambda} \qquad \text{(ASSERT)}$$

$$\frac{\lambda_y \subseteq \lambda}{\lambda; a^- \vdash yield \ e, \lambda_y : \lambda} \qquad \text{(YIELD)} \qquad \frac{ls(A) = (\lambda, \lambda')}{\lambda; a^+ \vdash call \ A : \lambda'} \qquad \text{(ATOMIC)}$$

$$\frac{ls(P) = (\lambda, \lambda')}{\lambda; \mathbf{a}^- \vdash call \ P : \lambda'} \ (\mathsf{PROC}) \ \frac{\lambda \cup \lambda_P \cup \lambda_P' \subseteq \mathit{ThreadLocals}}{\lambda_G, \lambda, \lambda_P; \mathbf{a}^- \vdash \mathit{async} \ P : \lambda_G, \lambda} \ (\mathsf{PROC}) \ \frac{\lambda_G \subseteq \mathit{Globals}}{\lambda_G, \lambda, \lambda_P; \mathbf{a}^- \vdash \mathit{async} \ P : \lambda_G, \lambda} \ (\mathsf{PROC}) \ \frac{\lambda_G \subseteq \mathit{Globals}}{\lambda_G, \lambda_G, \lambda_P; \mathbf{a}^- \vdash \mathit{async} \ P : \lambda_G, \lambda} \ (\mathsf{PROC}) \ \frac{\lambda_G \subseteq \mathit{Globals}}{\lambda_G, \lambda_G, \lambda_P; \mathbf{a}^- \vdash \mathit{async} \ P : \lambda_G, \lambda} \ (\mathsf{PROC}) \ \frac{\lambda_G \subseteq \mathit{Globals}}{\lambda_G, \lambda_G, \lambda_G, \lambda_G, \lambda_G} \ \frac{\lambda_G \subseteq \mathit{Globals}}{\lambda_G, \lambda_G, \lambda_G, \lambda_G, \lambda_G} \ \frac{\lambda_G \subseteq \mathit{Globals}}{\lambda_G, \lambda_G, \lambda_G, \lambda_G} \ \frac{\lambda_G \subseteq \mathit{Globals}}{\lambda_G, \lambda_G, \lambda_G} \ \frac{\lambda_G \subseteq \mathit{Globals}}{\lambda_G} \ \frac{\lambda_G \subseteq$$

$$\frac{\lambda; \mathbf{a}^{+} \vdash s : \lambda' \qquad \lambda_{a} \subseteq \lambda}{\lambda; \mathbf{a}^{-} \vdash ablock \{e, \lambda_{a}\} s : \lambda'}$$
(Ablock)

$$\frac{\lambda; a \vdash SS : \lambda'}{\lambda; a \vdash (L, SS) : \lambda'}$$
 (StackFrame)

$$\frac{\lambda; a \vdash SS : \lambda' \qquad \lambda'; a \vdash s : \lambda''}{\lambda; a \vdash SS; s : \lambda''}$$
 (SEQ)

$$\frac{\lambda; a \vdash s_1 : \lambda' \qquad \lambda; a \vdash s_2 : \lambda'}{\lambda; a \vdash if \ le \ then \ s_1 \ else \ s_2 \ : \lambda'} \tag{ITE}$$

$$\frac{\lambda; a \vdash s : \lambda}{\lambda; a \vdash while \{e, \alpha\} \ le \ do \ s : \lambda} \tag{While}$$

$$\frac{ls(P) = (\lambda, \lambda') \qquad \lambda; \mathbf{a}^{-} \vdash bs(P) : \lambda'}{\vdash P} \tag{Procedure}$$

$$\begin{split} ls(A) &= (\lambda, \lambda') & \lambda \cap Globals = \lambda' \cap Globals \\ as(A) &= (\rho, \alpha, m) & m \in \{B, L\} \Longrightarrow \forall \sigma \in \rho. \exists \sigma'. (\sigma, \sigma') \in \alpha \\ & A \in range(rs) \Longrightarrow Vars(\rho) \cap Locals = \emptyset \\ & OldLocals = old(Locals) \\ & A \in range(rs) \Longrightarrow \alpha = ((\exists OldLocals, Locals. \alpha) \wedge Same(Locals)) \\ & \forall (\sigma, \sigma') \in \alpha. disjoint(\{\sigma(x) \mid x \in \lambda\}) \wedge disjoint(\{\sigma'(x) \mid x \in \lambda'\}) \\ & \underbrace{ \forall (\sigma, \sigma') \in \alpha. \bigcup \{\sigma'(x) \mid x \in \lambda'\} \subseteq \bigcup \{\sigma(x) \mid x \in \lambda\} }_{\vdash A} \end{split} \tag{ACTION}$$

$$\frac{T = (\mathit{TL}, (\mathit{L}, \mathit{SS})) \quad \lambda; \mathbf{a}^- \vdash \mathit{SS} : \lambda'}{\lambda \vdash T} \tag{Thread}$$

$$\forall P \in ProcName. \vdash P \quad \forall A \in ActionName. \vdash A \\ \lambda_G \subseteq Globals \quad \forall 1 \leq i \leq n.(\lambda_G, \lambda_i \vdash T_i) \\ \forall 1 \leq i \leq n.(\lambda_i \subseteq ThreadLocals) \quad \forall 1 \leq i \leq n.(T_i = (TL_i, \ldots)) \\ \underbrace{\frac{disjoint(\{G(x) \mid x \in \lambda_G\} \cup \{TL_i(x) \mid 1 \leq i \leq n, x \in \lambda_i\})}{\vdash (bs, as, ps, rs, ls, G, T_1 \ldots T_n)}}_{} (PROGRAM)$$

Figure 10: Type checking rules

To this end, we designed a garbage collector that extends the concurrent collector of Dijkstra et. al. [4]. The goal in this design is to get a collector that is on one hand easy to verify and on the other hand highly performant in practice. Most modern concurrent collectors are snapshot-oriented [1, 5–7], and as such may require complex claims on snapshot times and reachability. Other popular concurrent collectors and in particular the *mostly concurrent* garbage collector [2, 3, 9] consist of different complex phases and complex interaction between objects that reside on specific *cards*. We preferred a collector whose invariants are simple and hold continuously as much as possible throughout the execution. This means that a program execution can move from one thread to another and then to the garbage collector while all relevant invariants continue to hold at all times.

Dijkstra's collector seems to be a good candidate, but it cannot be considered a modern performant collector. On the positive side, its write-barrier maintains simple invariants continuously. However, this collector becomes incorrect in the presence of more than one program thread (mutator) and it requires activating the write-

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```
(ASSERT)
                                                                                                                                                                                                                  (SKIP)
                                                                                                                                                                  \overline{P \vdash_r skip : \epsilon}
                                                                                                                                                                                                                                         P \vdash_r assert \ le : \epsilon
                                                                                                                                                                  \frac{P^{'} \in dom(rs) \quad as(rs(P^{'})) = (\rho^{'}, \alpha^{'}, m)}{P \vdash_{r} yield \ e, \lambda : \epsilon} \underbrace{\begin{array}{c} P^{'} \in dom(rs) \quad as(rs(P^{'})) = (\rho^{'}, \alpha^{'}, m) \\ \hline P \vdash_{r} vield \ P^{'} : \epsilon \end{array}}_{P \vdash_{r} vield \ P^{'} : \epsilon} (Call-Loop)
\frac{}{r;\{\}\vdash_{p}\{\phi\}skip\{\phi\}}\quad \text{(Skip)}\quad \frac{}{\mathbf{r}^{-};\{\}\vdash_{p}\{\phi\}assert\;le\{\phi\}}
                                                                                                                              (ASSERT1)
                                                                                                                                                                  \frac{P' \in dom(rs) \quad as(rs(P)) = (\rho, \alpha, m) \quad as(rs(P')) = (\rho', \alpha', m)}{OldLocals = old(Locals) \quad \rho' \circ \alpha' \Rightarrow \exists OldLocals, Locals. \, \alpha}{P \vdash_r call \, P' : N} \quad \text{(Call-Action)}
                                                                                                                              (ASSERT2)
r^+; {} \vdash_p {\phi \land le} assert le{\phi}
\frac{1}{r;\{\}\vdash_{p}\{e\}yield\;e,\lambda\{e\}}\;(\texttt{Yield})
                                                                                                                                                                                                       \frac{as(rs(P')) = (\rho', \alpha', m) \qquad \rho' \circ \alpha' \Rightarrow Havoc(\{\})}{P \vdash_r async P' : \epsilon} \text{ (Async)}
                                                                                                                                                                   P' \in dom(rs)
 as(A) = (\rho, \alpha, m)  M \subseteq ThreadLocals \quad \alpha \Rightarrow Havoc(Globals \cup M \cup Locals)   \phi \Rightarrow \rho \quad Unsat(\phi \circ \alpha \circ \neg \psi) 
                                                                                                                                                                  s \leq old(e) \Rightarrow Havoc(L)
                                                                                                                                                                                                                                                                                     (ABLOCK-LOOP)
                                                                                                                                                                  P \vdash_r ablock \{e, \lambda\} s : \epsilon
                                                                                                                              (ATOMIC)
                                 r; M \vdash_p \{\phi\} call \ A\{\psi\}
                                                                                                                                                                  as(rs(P)) = (\rho, \alpha, m) \qquad OldLocals = old(Locals) \\ \underline{s \leq old(e) \Rightarrow \exists OldLocals, Locals, \alpha} \qquad (\text{Ablock-Action})
ps(P) = (\phi, M, \psi) P \not\in dom(rs)
                                                                                                                                  (PROC1)
              r; M \vdash_{p} \{\phi\} call P\{\psi\}
                                                                                                                                                                                             P \vdash_r ablock \{e, \lambda\} s : N
ps(P) = (\phi, M, \psi) \quad P \in dom(rs)r; M \vdash_{p} \{\phi\} call \ rs(P)\{\psi\}
                                                                                                                                                                   P \vdash_r s_1 : re_1 \qquad P \vdash_r s_2 : re_2
                                                                                                                                                                                                                                                                                                         (SEQ)
                                                                                                                                  (PROC2)
            r; M \vdash_p \{\phi\} call P\{\psi\}
                                                                                                                                                                           P \vdash_r s_1; s_2 : re_1 \cdot re_2
\frac{ps(P) = (\phi, M, \psi)}{r; \{\} \vdash_{p} \{\rho \land \phi\} \, async \, P\{\rho\}} \, \, (\text{Async})
                                                                                                                                                                         P \vdash_r s_1 : re_1 \qquad P \vdash_r s_2 : re_2
                                                                                                                                                                                                                                                                                                          (ITE)
                                                                                                                                                                  P \vdash_r if \ le \ then \ s_1 \ else \ s_2 : re_1 + re_2
                                                                                                                                                                                       P \vdash_r s : re
                                                                                                                                                                                                                                                                                                    (WHILE)
              r; M \vdash_p \{\phi_1 \wedge e\}s\{\phi_2\}
                                                                                                                                                                  \overline{P \vdash_r while \{e, \alpha\} le do s : re^*}
                                                                                                                               (ABLOCK)
\frac{r; M \vdash_{p} \{\phi_{1} \land e\} ablock \{e, \lambda\} s \{\phi_{2}\}}{r; M_{1} \vdash_{p} \{\phi_{1}\} SS \{\phi_{2}\} \qquad r; M_{2} \vdash_{p} \{\phi_{2}\} s \{\phi_{3}\}}{r; M_{1} \cup M_{2} \vdash_{p} \{\phi_{1}\} SS; s \{\phi_{3}\}}
                                                                                                                                                                  \frac{\forall P \in dom(rs). \ bs(P) \vdash_r \{N\}}{\vdash_r (bs, as, ps, rs, ls, G, T_1 \dots T_n)} \ (\texttt{PROGRAM})
                                                                                                                                       (SEQ)
r; M_1 \vdash_p \{e \land \phi_1\} s_1 \{\phi_2\} \qquad r; M_2 \vdash_p \{\neg e \land \phi_1\} s_2 \{\phi_2\}
                                                                                                                                        (ITE)
           r; M_1 \cup M_2 \vdash_p \{\phi_1\} if \ le \ then \ s_1 \ else \ s_2 \{\phi_2\}
                                                                                                                                                                                                            Figure 12: Refinement rules
                    r; M \vdash_{p} \{e \land le\} s\{e\}
                                                                                                                                 (WHILE)
r; M \vdash_p \{e\} while \{e, \alpha\} le do s \{e \land \neg le\}
\frac{r; M \vdash_{p} \phi \Rightarrow \phi' \qquad r; M \vdash_{p} \{\phi'\}SS\{\psi'\} \qquad \psi' \Rightarrow \psi}{r; M \vdash_{p} \{\phi\}SS\{\psi\}}
                                                                                                                              (WEAKEN)
r; M \vdash_p \{\phi\}SS\{\psi\} \qquad Vars(\rho) \cap M = \{\}
                                                                                                                                 (FRAME)
               r; M \vdash_p \{\rho \land \phi\} SS\{\rho \land \psi\}
                                                                                                                                                                                                                    (SKIP)
                                                                                                                                                                                                                                                                                                   (ASSERT)
                                                                                                                                                                                                                                       \overline{assert\ le \preceq Havoc(\{\})}
                                                                                                                                                                  \overline{skip \leq Havoc(\{\})}
\frac{ps(P) = (\phi, M, \psi) \qquad r = \mathbf{r}^+ \Longleftrightarrow P \in dom(rs)}{r; M' \vdash_p \{\phi\} bs(P)\{\psi\} \qquad M' \subseteq M} \\ \vdash_p P
                                                                                                                                                                                                                                             as(A)=(\rho,\alpha,m)
                                                                                                                        (PROCEDURE)
                                                                                                                                                                                                                     (YIELD)
                                                                                                                                                                                                                                                                                                  (ATOMIC)
                                                                                                                                                                  yield\ e, \lambda \prec false
                                                                                                                                                                                                                                                     call A \prec \alpha
\begin{array}{l} r; M \vdash_{p} \{\phi'\}SS\{\psi\} & (\mathit{Vars}(\phi) \cup \mathit{Vars}(\psi)) \cap \mathit{Locals} = \{\} \\ \forall G, \mathit{TL}. \ \phi(G, \mathit{TL}) \Rightarrow \phi'(G, \mathit{TL}, L) & \\ \end{array} \tag{Stackframe}
                                                                                                                                                                                                               (CALL)
                                                                                                                                                                                                                                                                                                    (ASYNC)
                                                                                                                                                                  \overline{call\ P \preceq false}
                                                                                                                                                                                                                                      \overline{async\ P \preceq Havoc(\{\})}
                                     r; M \vdash_{p} {\phi}(L, SS){\psi}
                                                                                                                                                                                                                      (Ablock) \frac{s_1 \preceq \alpha_1}{} \quad s_2 \preceq \alpha_2
                                                                                                                                                                                s \preceq \alpha
T = (TL, (L, SS)) G \cdot TL \cdot L \vdash_p \phi \rightarrow true
                                                                                                                                                                                                                                                                                                         (SEQ)
                                                                                                                                                                  \overline{ablock \{e, \lambda\} s \preceq \alpha}
                \mathbf{r}^-; M \vdash_p \{\phi\}(L, SS)\{true\}
                                                                                                                               (THREAD)
                                                                                                                                                                                           s_1 \leq \alpha_1 \qquad s_2 \leq \alpha_2
\frac{\forall P \in ProcName. \vdash_{p} P \quad \forall 1 \leq i \leq n. \ G \vdash_{p} T_{i}}{\vdash_{p} (bs, as, ps, rs, ls, G, T_{1} \dots T_{n})} \ (PROGRAM)
                                                                                                                                                                                                                                                                                                          (ITE)
                                                                                                                                                                  if le then s_1 else s_2 \leq (le \circ \alpha_1) \vee (\neg le \circ \alpha_2)
                                                                                                                                                                  s \leq \beta \neg le \circ Havoc(\{\}) \Rightarrow \alpha \beta \circ \alpha \Rightarrow \alpha
                                                                                                                                                                                                                                                                                                    (WHILE)
                                                                                                                                                                               while \{e, \alpha\} le do s \leq e \circ \alpha \circ \neg le
                  Figure 11: Sequential rules for partial correctness
                                                                                                                                                                  \frac{s \preceq \alpha \qquad \alpha \Rightarrow \alpha'}{s \preceq \alpha'} \ (\text{Weaken})
```

Figure 13: Abstracting statements by actions

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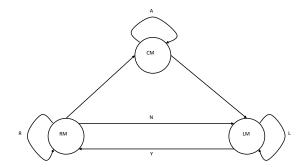


Figure 14: Specification for yield sufficiency

$$\frac{\vdash_{y} SS: (x,y) \qquad \vdash_{y} s: (y,z)}{\vdash_{y} SS; s: (x,z)} \tag{SEQ}$$

 $x \in \{RM,\,CM\}$

 $\vdash_y ablock \{e, \lambda\} s : (x, CM)$

$$\frac{\vdash_{y} s_{1}: (x,y) \qquad \vdash_{y} s_{2}: (x,y)}{\vdash_{y} if \ le \ then \ s_{1} \ else \ s_{2}: (x,y)} \tag{ITE}$$

$$\frac{\vdash_{y} s:(x,x)}{\vdash_{y} \textit{ while } \{e,\alpha\} \textit{ le do } s:(x,x)} \tag{While}$$

$$\frac{\vdash_{y} bs(P):(x,y)}{\vdash_{y} P} \quad \text{(Procedure)} \ \, \frac{\vdash_{y} SS:(x,y)}{\vdash_{y} (L,SS):(x,y)} \quad \text{(StackFrame)}$$

$$\frac{T = (\mathit{TL}, (L, \mathit{SS})) \quad \vdash_{y} (L, \mathit{SS}) : (x, y)}{\vdash_{y} T} \tag{Thread}$$

$$\frac{\forall P \in \mathit{ProcName} \setminus \mathit{dom}(rs). \ \vdash_y P \quad \forall 1 \leq i \leq n. \ \vdash_y T_i}{\vdash_y (\mathit{bs}, \mathit{as}, \mathit{ps}, rs, \mathit{ls}, G, T_1 \dots T_n)} \ (\mathsf{Program})$$

Figure 15: Yield sufficiency rules

Figure 16: Program refinement

barrier on writes to root pointers, which means write barrier overhead on the runtime stack accesses and register accesses. The first issue means that the collector is unacceptable for use with modern multicore platforms. The second issue implies bad performance. Concurrent garbage collectors today are expected to work correctly without adding a write-barrier overhead to local accesses.

We therefore extended and modified Dijkstra's collector to make it work with parallel programs and also not require applying a write-barrier on root modifications. As far as we know, this garbage collector's algorithm has not been proposes previously in the literature.

8. Related work

References

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(ABLOCK)

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