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Course No : EEE-4416

Course Title : Matlab Simulation Lab

Experiment : MATLAB GUI Project

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# Digital Logic & Digital Modulation Simulator (MATLAB App)

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## Introduction

This project is a MATLAB App Designer-based application that integrates Digital Logic Design and Digital Modulation concepts into a single interactive platform. The app provides a user-friendly graphical interface for simulating logic gates, constructing truth tables, and visualizing digital modulation schemes.

## Features

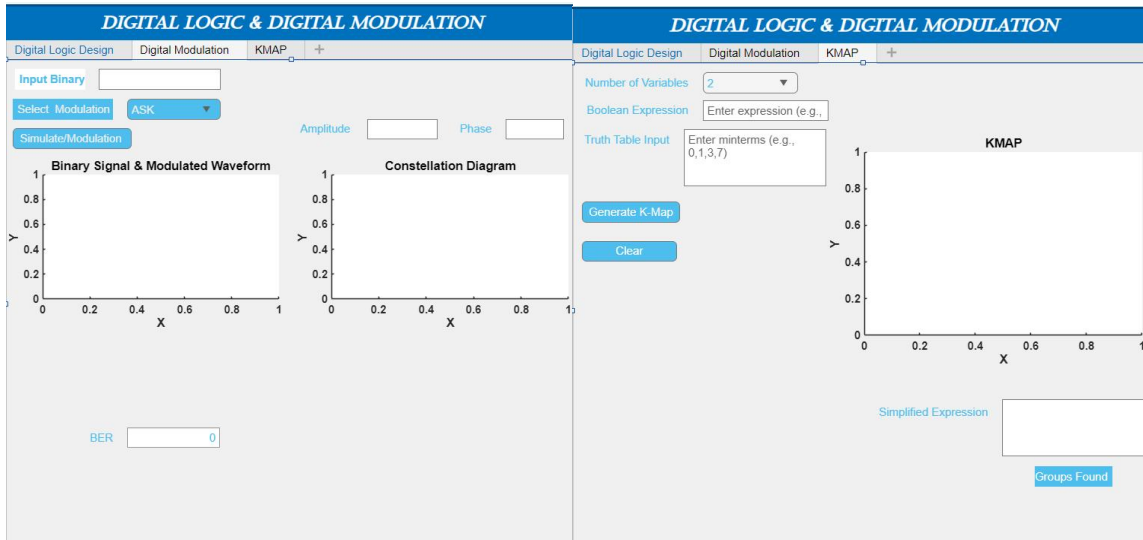
- **Digital Logic Design Tab**: Includes buttons for logic gates (AND, OR, NOT, NAND, NOR, XOR, XNOR).
- Generates truth tables based on selected logic gates.
- Displays corresponding waveforms for input and output signals.
- **Digital Modulation Tab**: Allows selection of modulation schemes (ASK, PSK, FSK, etc.). [\(Not completed yet. It's a prototype\)](#)
- User inputs binary streams for modulation.
- Displays output waveform, constellation diagram, and eye diagram.
- Includes fields for amplitude, phase, BER, and modulation parameters.

## Objective

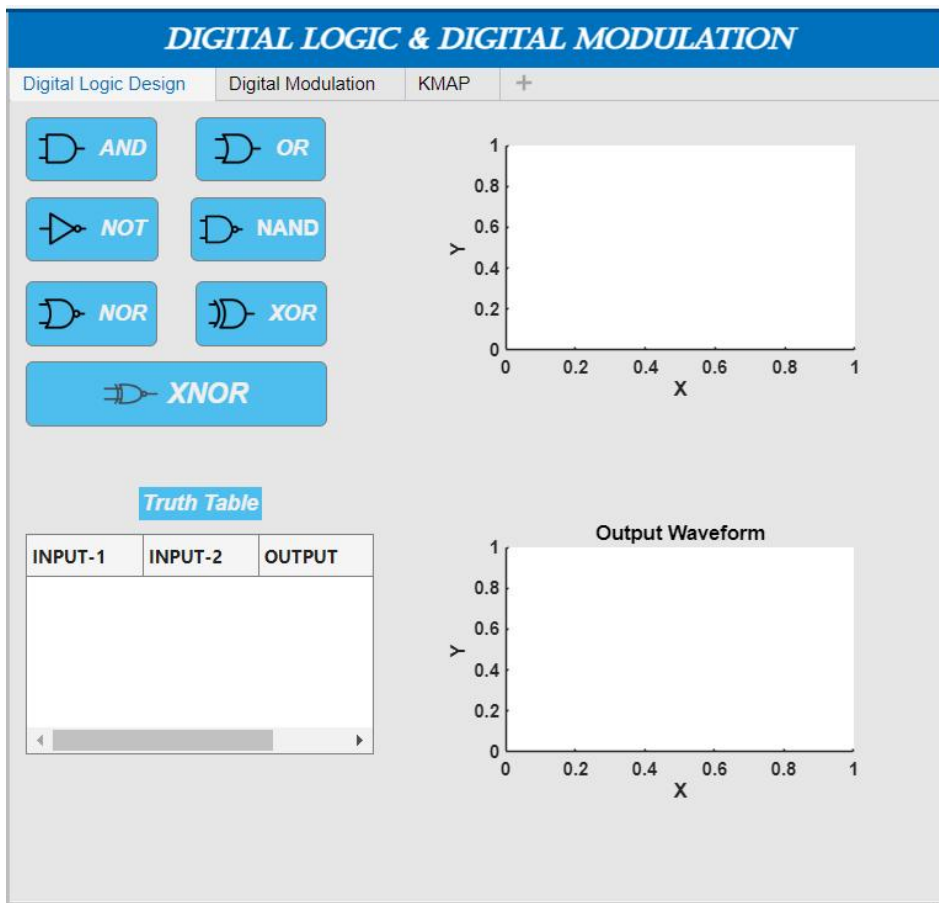
The main objective of this project is to provide an educational and simulation tool that helps students and researchers understand the fundamentals of digital logic, digital modulation and Kmap of logic design. By combining truth table generation, logic gate simulation, Kmap, and modulation visualization, the app serves as an interactive laboratory environment.

## APP's Design View

## Prototype



## Working

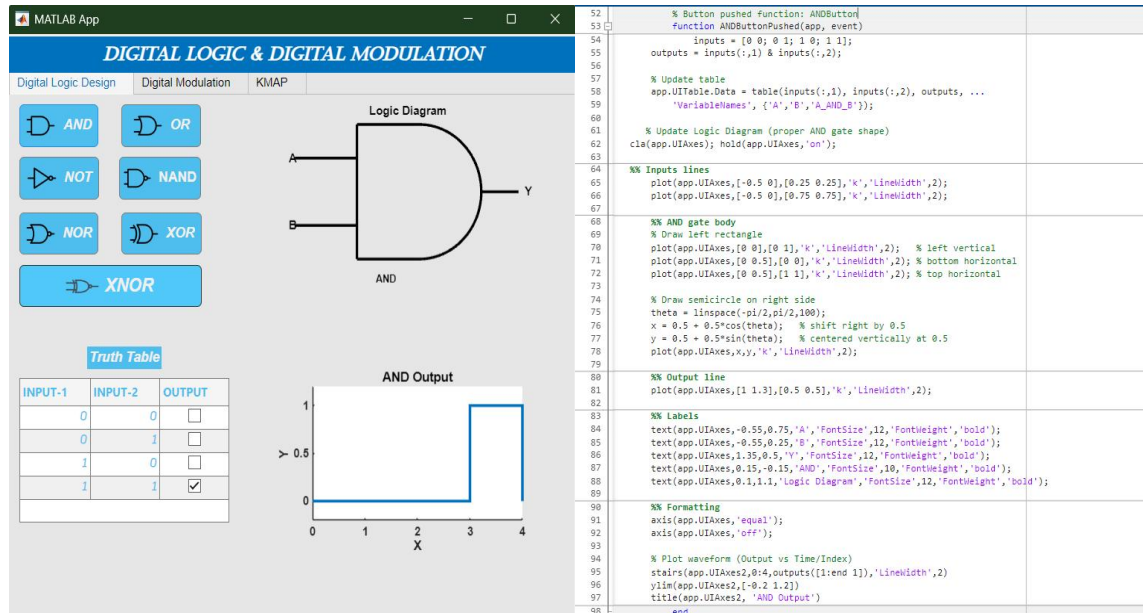


## Different Segments and Outputs

And Gate:

Design view Output

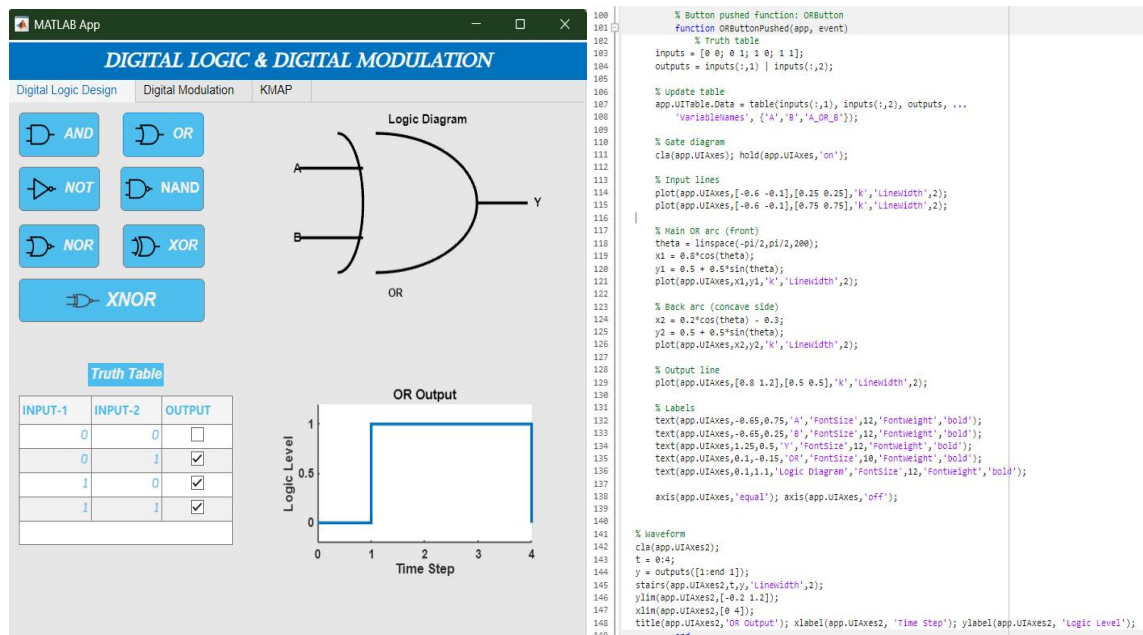
Code view



OR Gate:

Design view Output

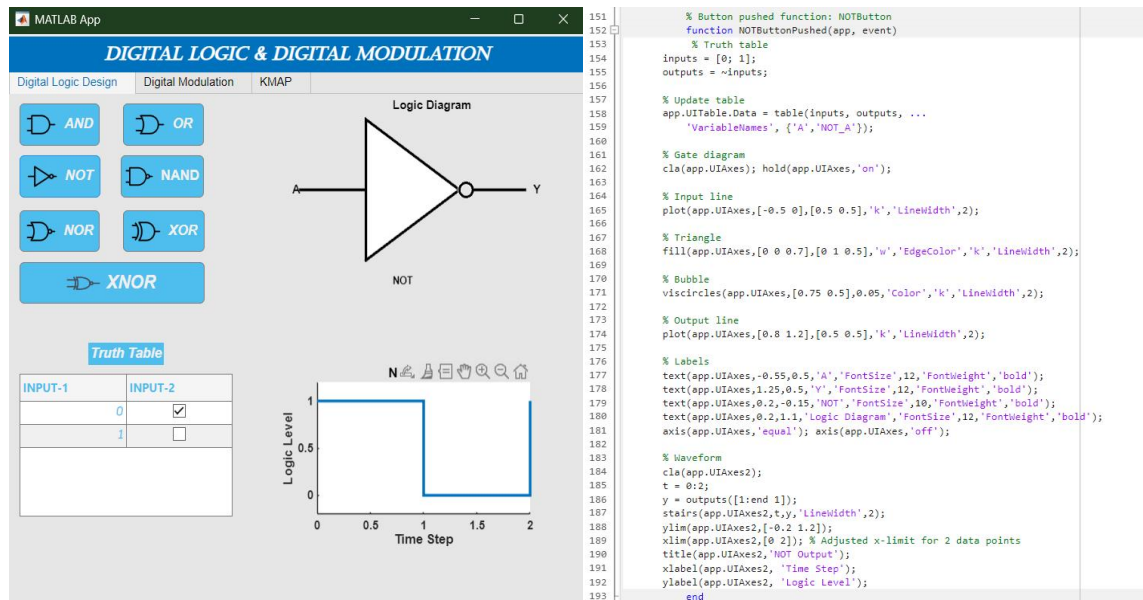
Code view



NOT Gate:

Design view Output

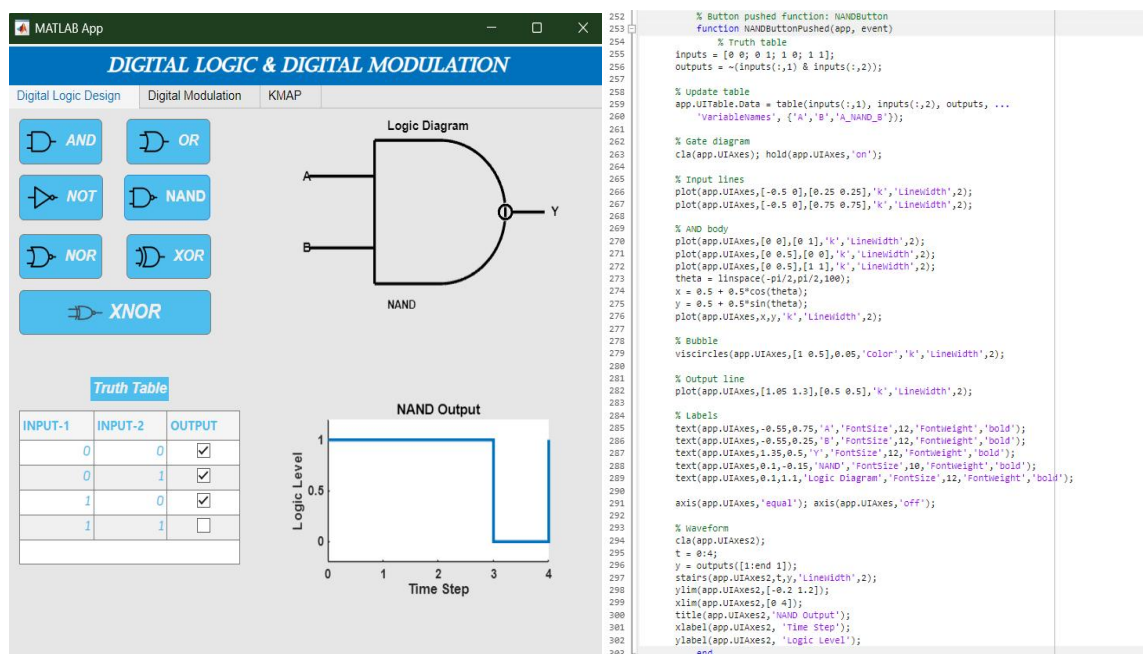
Code view



NAND Gate:

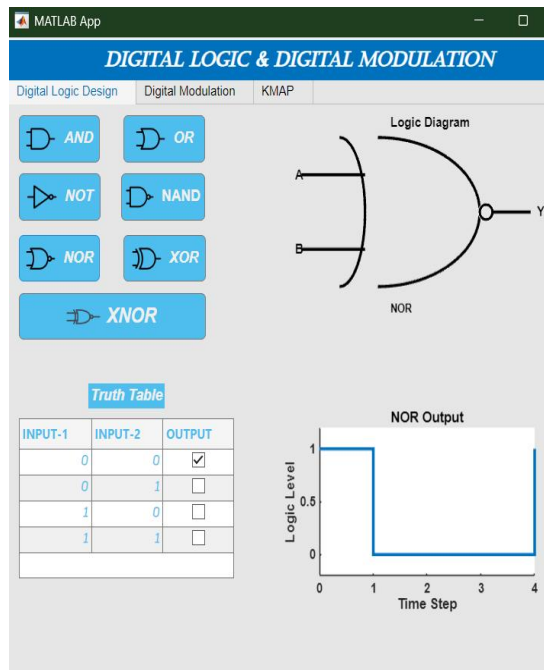
Design view Output

Code view



## NOR Gate:

### Design view Output

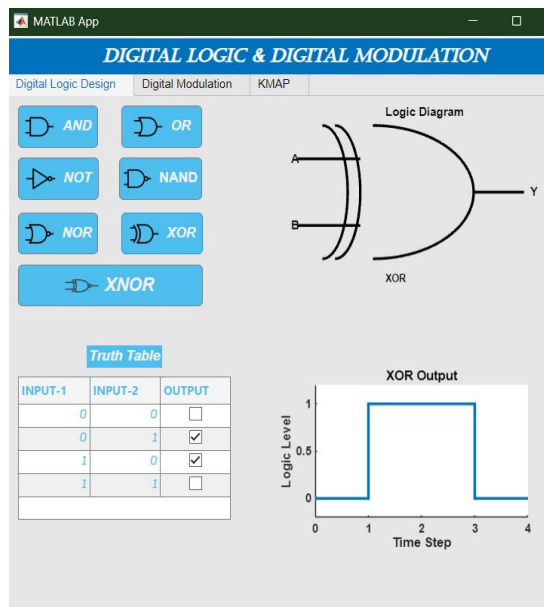


### Code view

```
385 % Button pushed function: NORButton
386 function NORButtonPushed(app, event)
387 % Truth table
388 inputs = [0 0; 0 1; 1 0; 1 1];
389 outputs = ~(inputs(:,1) | inputs(:,2));
390
391 % Update table
392 app.UITable.Data = table(inputs(:,1), inputs(:,2), outputs, ...
393     'VariableNames', {'A','B','A_NOR_B'});
394
395 % Gate diagram
396 cla(app.UIAxes); hold(app.UIAxes,'on');
397
398 % Input lines
399 plot(app.UIAxes,[-0.6 -0.1],[0.25 0.25],'k','LineWidth',2);
400 plot(app.UIAxes,[-0.6 -0.1],[0.75 0.75],'k','LineWidth',2);
401
402 % OR body
403 theta = linspace(-pi/2,pi/2,200);
404 x1 = 0.8*cos(theta);
405 y1 = 0.5 + 0.5*sin(theta);
406 plot(app.UIAxes,x1,y1,'k','LineWidth',2);
407
408 x2 = 0.2*cos(theta) - 0.3;
409 y2 = 0.5 + 0.5*sin(theta);
410 plot(app.UIAxes,x2,y2,'k','LineWidth',2);
411
412 % Bubble
413 viscircles(app.UIAxes,[0.85 0.5],0.05,'k','LineWidth',2);
414
415 % Output line
416 plot(app.UIAxes,[0.9 1.2],[0.5 0.5],'k','LineWidth',2);
417
418 % Labels
419 text(app.UIAxes,-0.65,0.75,'A','FontSize',12,'FontWeight','bold');
420 text(app.UIAxes,-0.65,0.25,'B','FontSize',12,'FontWeight','bold');
421 text(app.UIAxes,1.25,0.5,'Y','FontSize',12,'FontWeight','bold');
422 text(app.UIAxes,0.1,-0.15,'NOR','FontSize',10,'FontWeight','bold');
423 text(app.UIAxes,0.1,1.1,'Logic Diagram','FontSize',12,'FontWeight','bold');
424
425 axis(app.UIAxes,'equal'); axis(app.UIAxes,'off');
426
427 % Waveform
428 cla(app.UIAxes2);
429 t = 0:4;
430 y = outputs([1:end 1]);
431 stairs(app.UIAxes2,t,y,'LineWidth',2);
432 ylim(app.UIAxes2,[-0.2 1.2]);
433 xlim(app.UIAxes2,[0 4]);
434 title(app.UIAxes2,'NOR Output');
435 xlabel(app.UIAxes2,'Time Step');
436 ylabel(app.UIAxes2,'Logic Level');
437 end
```

## XOR Gate:

### Design view Output



### Code view

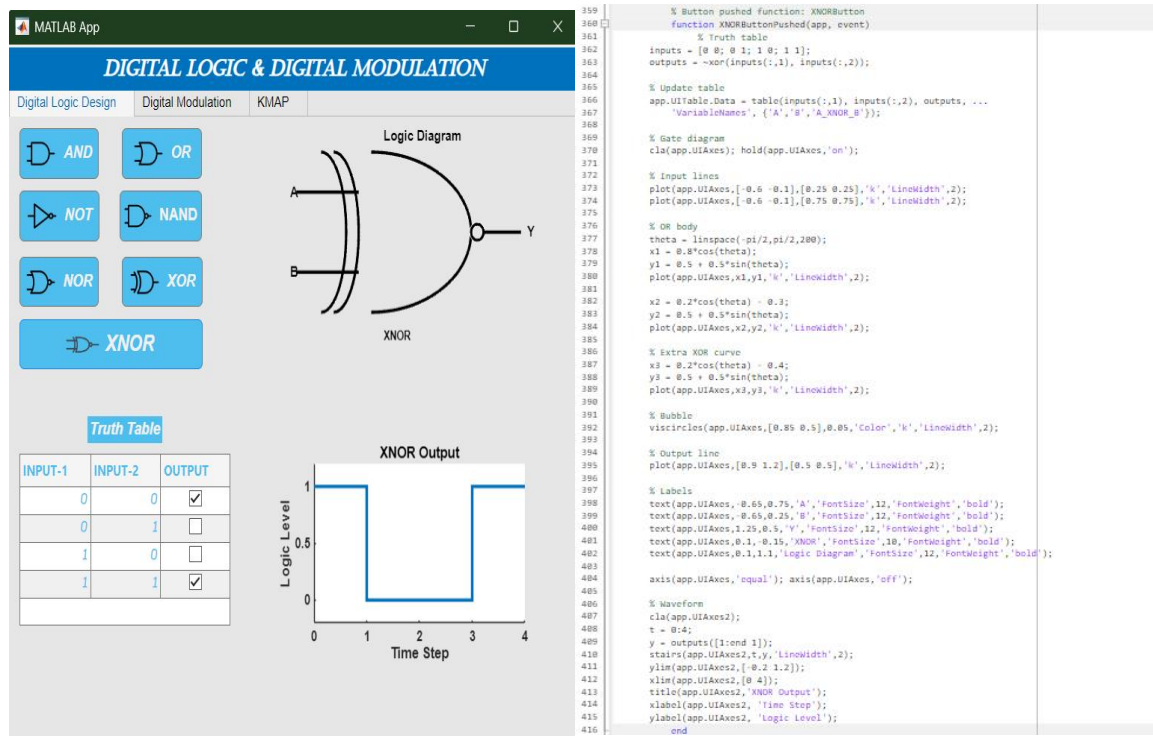
```
195 % Button pushed function: XORButton
196 function XORButtonPushed(app, event)
197 % Truth table
198 inputs = [0 0; 0 1; 1 0; 1 1];
199 outputs = xor(inputs(:,1), inputs(:,2));
200
201 % Update table
202 app.UITable.Data = table(inputs(:,1), inputs(:,2), outputs, ...
203     'VariableNames', {'A','B','A_XOR_B'});
204
205 % Gate diagram
206 cla(app.UIAxes); hold(app.UIAxes,'on');
207
208 % Input lines
209 plot(app.UIAxes,[-0.6 -0.1],[0.25 0.25],'k','LineWidth',2);
210 plot(app.UIAxes,[-0.6 -0.1],[0.75 0.75],'k','LineWidth',2);
211
212 % Main OR arc (front)
213 theta = linspace(-pi/2,pi/2,200);
214 x1 = 0.8*cos(theta);
215 y1 = 0.5 + 0.5*sin(theta);
216 plot(app.UIAxes,x1,y1,'k','LineWidth',2);
217
218 % Back arc (concave side)
219 x2 = 0.2*cos(theta) - 0.3;
220 y2 = 0.5 + 0.5*sin(theta);
221 plot(app.UIAxes,x2,y2,'k','LineWidth',2);
222
223 % Extra XOR arc (parallel to back curve)
224 x3 = 0.2*cos(theta) - 0.4;
225 y3 = 0.5 + 0.5*sin(theta);
226 plot(app.UIAxes,x3,y3,'k','LineWidth',2);
227
228 % Output line
229 plot(app.UIAxes,[0.9 1.2],[0.5 0.5],'k','LineWidth',2);
230
231 % Labels
232 text(app.UIAxes,-0.65,0.75,'A','FontSize',12,'FontWeight','bold');
233 text(app.UIAxes,-0.65,0.25,'B','FontSize',12,'FontWeight','bold');
234 text(app.UIAxes,1.25,0.5,'Y','FontSize',12,'FontWeight','bold');
235 text(app.UIAxes,0.1,-0.15,'XOR','FontSize',10,'FontWeight','bold');
236 text(app.UIAxes,0.1,1.1,'Logic Diagram','FontSize',12,'FontWeight','bold');
237
238 axis(app.UIAxes,'equal'); axis(app.UIAxes,'off');
239
240 % Waveform
241 cla(app.UIAxes2);
242 t = 0:4;
243 y = outputs([1:end 1]);
244 stairs(app.UIAxes2,t,y,'LineWidth',2);
245 ylim(app.UIAxes2,[-0.2 1.2]);
246 xlim(app.UIAxes2,[0 4]);
247 title(app.UIAxes2,'XOR Output');
248 xlabel(app.UIAxes2,'Time Step');
249 ylabel(app.UIAxes2,'Logic Level');
250 end
```



## XNOR Gate:

### Design view Output

### Code view



## Conclusion

The Digital Logic & Digital Modulation Simulator is a versatile learning and teaching tool. It enhances practical understanding of core concepts in Digital Electronics and Communication Systems through interactive simulations.