



Lab 2 - Implementing Decision Instructions in RISC V Assembly Language

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Task 1: Code:

Listing 3:

```
ASM Task1a.s x
ASM Task1a.s
1  # beq x5, x6, label -> opcode, then binary codes for registers, bits for the distance (contain a statement)
2  .text
3  .globl main
4  main:
5      li x22, 10      # i = 0
6      li x23, 10      # j = 10
7      li x20, 5        # g = 5
8      li x21, 3        # h = 3
9      li x19, 0        # f = 0
10 Loop:
11     bne x22, x23, Else # if (i==j): f= g+h | Else f= g-h
12     add x19, x20, x21
13     beq x0, x0, Exit
14
15 Else:
16     sub x19, x20, x21
17 Exit:
18     j end
19
20 end:
21     j end
22
23 # exit and end are outside the loop bz they are the steps that define
24 #what the count is to cause the jump.
25 #main has the f else condition only
26
27 # ctrl+shift + P: assemble and run
28 # in assembler take the middle hex value, convert to binary, use the table in RISC-V greencard reference to distribute the bits.
29 # cross check values for the imm[12|10:5] and imm[4:1|11] with the binary value to get the value for the distance.
```

**Results:**

ASM	Task1a.s	assembly	X
1	0x00000000	0x00A00B13	addi x22 x0 10
2	0x00000004	0x00A00B93	addi x23 x0 10
3	0x00000008	0x00500A13	addi x20 x0 5
4	0x0000000C	0x00300A93	addi x21 x0 3
5	0x00000010	0x00000993	addi x19 x0 0
6	0x00000014	0x017B1663	bne x22 x23 12
7	0x00000018	0x015A09B3	add x19 x20 x21
8	0x0000001C	0x00000463	beq x0 x0 8
9	0x00000020	0x415A09B3	sub x19 x20 x21
10	0x00000024	0x0040006F	jal x0 4
11	0x00000028	0x0000006F	jal x0 0

The hex value for **bne** we obtained was: 0x017B1663

Converting it to binary we obtained:

Field	imm[12]	imm[10:5]	rs2	rs1	funct3	imm[4:1]	imm[11]	opcode
Value	0	000000	11000	01001	001	0110	0	1100011

Splitting it based on the RISC-V table we obtained the value for our offset:

- 000000001100 = 12 in Decimal.
- Step count = 3

The hex value for **beq** we obtained was: 0x000000463

converting it to binary we obtained:

Field	imm[12]	imm[10:5]	rs2	rs1	funct3	imm[4:1]	imm[11]	opcode
Value	0	000000	00000	00000	000	0100	0	1100011

Splitting it based on the RISC-V table we obtained the value for our offset:

- 000000001000 = 8 in decimal
- Step count = 2



Task 1:
Code:
Listing 4:

```
Task1b.s
1  .data
2  my_array: .space 20
3  .text
4  .globl main
5  main:
6
7      # 1. Load the base address of the array into x25
8      la x25, my_array
9
10     li t0, 10      # Load 10 into temporary register t0
11     sw t0, 0(x25)   # Store t0 at array[0] (offset 0)
12
13     li t0, 10      # Load 10 into t0
14     sw t0, 4(x25)   # Store t0 at array[1] (offset 4)
15
16     li t0, 10      # Load 10 into t0
17     sw t0, 8(x25)   # Store t0 at array[2] (offset 8)
18
19     li t0, 25      # Load 25 into t0
20     sw t0, 12(x25)  # Store t0 at array[3] (offset 12)
21
22     li t0, 30      # Load 30 into t0
23     sw t0, 16(x25)  # Store t0 at array[4] (offset 16)
24
25     li x22, 0      # x22 (i) = 0 (Start index)
26     li x24, 10     # x24 (k) = 10 (The value to skip)
27
28 Loop:
29     slli x10, x22, 2
30     add x10, x10, x25
31     lw x9, 0(x10)
32     bne x9, x24, Exit
33     addi x22, x22, 1
34     beq x0, x0, Loop
35 Exit:
36     j end
37 end:
38     j end
```

**Results:**

```
✓ VARIABLES
  ✓ PC
    PC = 0x00000054
  > PRIV
  > CSR
  ✓ Integer
    x00 (zero) = 0x00000000
    x01 (ra)   = 0x00000058
    x02 (sp)   = 0x7FFFFFF0
    x03 (gp)   = 0x10000000
    x04 (tp)   = 0x00000000
    x05 (t0)   = 0x0000001E
    x06 (t1)   = 0x00000000
    x07 (t2)   = 0x00000000
    x08 (s0)   = 0x00000000
    x09 (s1)   = 0x00000019
    x10 (a0)   = 0x1000000C
```

ASM Task1a.s	assembly	ASM Task1b.s
1	0x00000000	0x10000C97 auipc x25 65536
2	0x00000004	0x000C8C93 addi x25 x25 0
3	0x00000008	0x00A00293 addi x5 x0 10
4	0x0000000C	0x005CA023 sw x5 0(x25)
5	0x00000010	0x00A00293 addi x5 x0 10
6	0x00000014	0x005CA223 sw x5 4(x25)
7	0x00000018	0x00A00293 addi x5 x0 10
8	0x0000001C	0x005CA423 sw x5 8(x25)
9	0x00000020	0x01900293 addi x5 x0 25
10	0x00000024	0x005CA623 sw x5 12(x25)
11	0x00000028	0x01E00293 addi x5 x0 30
12	0x0000002C	0x005CA823 sw x5 16(x25)
13	0x00000030	0x00000B13 addi x22 x0 0
14	0x00000034	0x00A00C13 addi x24 x0 10
15	0x00000038	0x002B1513 slli x10 x22 2
16	0x0000003C	0x01950533 add x10 x10 x25
17	0x00000040	0x00052483 lw x9 0(x10)
18	0x00000044	0x01849663 bne x9 x24 12
19	0x00000048	0x001B0B13 addi x22 x22 1
20	0x0000004C	0xFE0006E3 beq x0 x0 -20
21	0x00000050	0x0040006F jal x0 4
22	0x00000054	0x0000006F jal x0 0



The hex value for **bne** operation obtained is: 0x01849663

Converting it to binary we obtained: 00000001100001001001011001100011

Field	imm[12]	imm[10:5]	rs2	rs1	funct3	imm[4:1]	imm[11]	opcode
Value	0	000000	11000	01001	001	0110	0	1100011

Splitting it based on the RISC-V table we obtained the value for our offset:

- 000000001100 = 12 in Decimal.
- Step count = 3

The hex value for **beq** operation obtained is: 0xFE0006E3

Converting it to binary we obtained: 111111100000000000000011011100011

Field	imm[12]	imm[10:5]	rs2	rs1	funct3	imm[4:1]	imm[11]	opcode
Value	1	111111	00000	00000	000	0110	1	1100011

Splitting it based on the RISC-V table we obtained the value for our offset: 111111110110

- Shift Left 1 (adding an implicit 0 – Imm[0]) = 1111111101100
- Taking 2's complement = 0000000010100
- This is equivalent to -20 in Decimal.
- Step count = $-20/4 = -5$ (5 instructions Up)



Task 2:

Code:

```
1  .text
2  .globl main
3  main:
4      #valyes for a b c and x
5      li x21, 0 #a
6      li x22, 20      #b = 20
7      li x23, 10      #c = 10
8      li x20, 4      #x = 1 or 2 or 3 or 4
9
10     li x6, 1 #if x ==1
11     li x7, 2 #if x==2
12     li x28, 3 #if x==3
13     li x29, 4 #if x==4
14     li x5, 0 #default case
15     li x30, 2 # temp var for division and multi
16
17     beq x20, x6, Case1
18     beq x20, x7, Case2
19     beq x20, x28, Case3
20     beq x20, x29, Case4
21     beq x0, x0, Default
22
23 Case1:
24     add x21, x22, x23
25     j end
26
27 Case2:
28     sub x21, x22, x23
29     j end
30
31 Case3:
32     mul x21, x22, x30
33     j end
34
35 Case4:
36     div x21, x22, x30
37     j end
38
39 Default:
40     li x21, 0
41     j end
42
43 end:
44     j end
```



Results:

<div><div>RIABLES</div><div>'C PC = 0x00000064</div><div>PRIV hex</div><div>SR</div><div>Integer</div><div>x00 (zero) = 0x00000000 x01 (ra) = 0x00000068 x02 (sp) = 0x7FFFFFF0 x03 (gp) = 0x10000000 x04 (tp) = 0x00000000 x05 (t0) = 0x00000000 x06 (t1) = 0x00000001 x07 (t2) = 0x00000002 x08 (s0) = 0x00000000 x09 (s1) = 0x00000000 x10 (a0) = 0x00000000 x11 (a1) = 0x00000000 x12 (a2) = 0x00000000 x13 (a3) = 0x00000000 x14 (a4) = 0x00000000 x15 (a5) = 0x00000000 x16 (a6) = 0x00000000 x17 (a7) = 0x00000000 x18 (s2) = 0x00000000 x19 (s3) = 0x00000000 x20 (s4) = 0x00000001 x21 (s5) = 0x0000001E x22 (s6) = 0x00000014 x23 (s7) = 0x0000000A x24 (s8) = 0x00000000 x25 (s9) = 0x00000000 x26 (s10) = 0x00000000 x27 (s11) = 0x00000000 x28 (t3) = 0x00000003 x29 (t4) = 0x00000004 x30 (t5) = 0x00000002 x31 (t6) = 0x00000000</div></div>	<div><div>RIABLES</div><div>C PC = 0x00000064</div><div>RIV</div><div>SR</div><div>Integer</div><div>x00 (zero) = 0x00000000 x01 (ra) = 0x00000068 x02 (sp) = 0x7FFFFFF0 x03 (gp) = 0x10000000 x04 (tp) = 0x00000000 x05 (t0) = 0x00000000 x06 (t1) = 0x00000001 x07 (t2) = 0x00000002 x08 (s0) = 0x00000000 x09 (s1) = 0x00000000 x10 (a0) = 0x00000000 x11 (a1) = 0x00000000 x12 (a2) = 0x00000000 x13 (a3) = 0x00000000 x14 (a4) = 0x00000000 x15 (a5) = 0x00000000 x16 (a6) = 0x00000000 x17 (a7) = 0x00000000 x18 (s2) = 0x00000000 x19 (s3) = 0x00000000 x20 (s4) = 0x00000002 x21 (s5) = 0x0000000A x22 (s6) = 0x00000014 x23 (s7) = 0x0000000A x24 (s8) = 0x00000000 x25 (s9) = 0x00000000 x26 (s10) = 0x00000000 x27 (s11) = 0x00000000 x28 (t3) = 0x00000003 x29 (t4) = 0x00000004 x30 (t5) = 0x00000002 x31 (t6) = 0x00000000</div></div>	<div><div>ARIABLES</div><div>PC PC = 0x00000064</div><div>PRIV</div><div>CSR</div><div>Integer</div><div>x00 (zero) = 0x00000000 x01 (ra) = 0x00000068 x02 (sp) = 0x7FFFFFF0 x03 (gp) = 0x10000000 x04 (tp) = 0x00000000 x05 (t0) = 0x00000000 x06 (t1) = 0x00000001 x07 (t2) = 0x00000002 x08 (s0) = 0x00000000 x09 (s1) = 0x00000000 x10 (a0) = 0x00000000 x11 (a1) = 0x00000000 x12 (a2) = 0x00000000 x13 (a3) = 0x00000000 x14 (a4) = 0x00000000 x15 (a5) = 0x00000000 x16 (a6) = 0x00000000 x17 (a7) = 0x00000000 x18 (s2) = 0x00000000 x19 (s3) = 0x00000000 x20 (s4) = 0x00000003 x21 (s5) = 0x00000028 x22 (s6) = 0x00000014 x23 (s7) = 0x0000000A x24 (s8) = 0x00000000 x25 (s9) = 0x00000000 x26 (s10) = 0x00000000 x27 (s11) = 0x00000000 x28 (t3) = 0x00000003 x29 (t4) = 0x00000004 x30 (t5) = 0x00000002 x31 (t6) = 0x00000000</div></div>	<div><div>JN... No Con... ⚙</div><div>ARIABLES</div><div>PC PC = 0x00000064</div><div>PRIV</div><div>CSR</div><div>Integer</div><div>x00 (zero) = 0x00000000 x01 (ra) = 0x00000068 x02 (sp) = 0x7FFFFFF0 x03 (gp) = 0x10000000 x04 (tp) = 0x00000000 x05 (t0) = 0x00000000 x06 (t1) = 0x00000001 x07 (t2) = 0x00000002 x08 (s0) = 0x00000000 x09 (s1) = 0x00000000 x10 (a0) = 0x00000000 x11 (a1) = 0x00000000 x12 (a2) = 0x00000000 x13 (a3) = 0x00000000 x14 (a4) = 0x00000000 x15 (a5) = 0x00000000 x16 (a6) = 0x00000000 x17 (a7) = 0x00000000 x18 (s2) = 0x00000000 x19 (s3) = 0x00000000 x20 (s4) = 0x00000004 x21 (s5) = 0x0000000A x22 (s6) = 0x00000014 x23 (s7) = 0x0000000A x24 (s8) = 0x00000000 x25 (s9) = 0x00000000 x26 (s10) = 0x00000000 x27 (s11) = 0x00000000 x28 (t3) = 0x00000003 x29 (t4) = 0x00000004 x30 (t5) = 0x00000002 x31 (t6) = 0x00000000</div></div>	<div><div>ARIABLES</div><div>PC PC = 0x00000064</div><div>PRIV</div><div>CSR</div><div>Integer</div><div>x00 (zero) = 0x00000000 x01 (ra) = 0x00000068 x02 (sp) = 0x7FFFFFF0 x03 (gp) = 0x10000000 x04 (tp) = 0x00000000 x05 (t0) = 0x00000000 x06 (t1) = 0x00000001 x07 (t2) = 0x00000002 x08 (s0) = 0x00000000 x09 (s1) = 0x00000000 x10 (a0) = 0x00000000 x11 (a1) = 0x00000000 x12 (a2) = 0x00000000 x13 (a3) = 0x00000000 x14 (a4) = 0x00000000 x15 (a5) = 0x00000000 x16 (a6) = 0x00000000 x17 (a7) = 0x00000000 x18 (s2) = 0x00000000 x19 (s3) = 0x00000000 x20 (s4) = 0x00000005 x21 (s5) = 0x00000000 x22 (s6) = 0x00000014 x23 (s7) = 0x0000000A x24 (s8) = 0x00000000 x25 (s9) = 0x00000000 x26 (s10) = 0x00000000 x27 (s11) = 0x00000000 x28 (t3) = 0x00000003 x29 (t4) = 0x00000004 x30 (t5) = 0x00000002 x31 (t6) = 0x00000000</div></div>
--	--	--	--	--

Case 1

Case 2

Case 3

Case 4

Default



Task 3:

Code:

```
1  .text
2  .globl main
3  main:
4      li x22, 0 #i=0
5      li x23, 0 #sum=0
6      li x5, 0x200 #array address
7      li x6, 10 #size of loop =10
8
9  loop1:
10     bge x22, x6, loop1_done    # if i ≥ 10, exit init loop
11     slli x7, x22, 2            # x7 = i * 4 (byte offset)
12     add x7, x7, x5            # x7 = address of a[i]
13     sw x22, 0(x7)             # a[i] = i (store i into array)
14     addi x22, x22, 1          # i++
15     j loop1
16
17 loop1_done:
18     li x22, 0 #reset i=0 for second loop
19
20 loop2:
21     bge x22, x6, exit #if i≥10 exit loop
22     slli x7, x22, 2 #jump by 2^2 =4
23     add x7, x7, x5 #address of array[i] in x7
24     lw x8, 0(x7) #load array[i] in x8
25     add x23, x23, x8 #sum = sum + array[i]
26     addi x22, x22, 1 #i++
27     j loop2
28
29 exit:
30     j end
31
32 end:
33     j end
```


**Results:**

Integer

x00 (zero) = 0x00000000

x01 (ra) = 0x00000050

x02 (sp) = 0x7FFFFFF0

x03 (gp) = 0x10000000

x04 (tp) = 0x00000000

x05 (t0) = 0x00000200

x06 (t1) = 0x0000000A

x07 (t2) = 0x00000224

x08 (s0) = 0x00000009

x09 (s1) = 0x00000000

x10 (a0) = 0x00000000

x11 (a1) = 0x00000000

x12 (a2) = 0x00000000

x13 (a3) = 0x00000000

x14 (a4) = 0x00000000

x15 (a5) = 0x00000000

x16 (a6) = 0x00000000

x17 (a7) = 0x00000000

x18 (s2) = 0x00000000

x19 (s3) = 0x00000000

x20 (s4) = 0x00000000

x21 (s5) = 0x00000000

x22 (s6) = 0x0000000A

x23 (s7) = 0x0000002D

x24 (s8) = 0x00000000

x25 (s9) = 0x00000000

x26 (s10) = 0x00000000

x27 (s11) = 0x00000000

x28 (t3) = 0x00000000

x29 (t4) = 0x00000000

x30 (t5) = 0x00000000

x31 (t6) = 0x00000000

Memory

Address	+0	+1	+2	+3
0x00000228	0	0	0	0
0x00000224	9	0	0	0
0x00000220	8	0	0	0
0x0000021C	7	0	0	0
0x00000218	6	0	0	0
0x00000214	5	0	0	0
0x00000210	4	0	0	0
0x0000020C	3	0	0	0
0x00000208	2	0	0	0
0x00000204	1	0	0	0
0x00000200	0	0	0	0
0x000001FC	0	0	0	0
0x000001F8	0	0	0	0

Address:

Up

Down

Jump to:

-- choose --

Display Format:

Decimal

Bytes per Row:

4

**Task 4:****Code:**

```
assembly Task4.s X
LabsCA-Git-Shaheer_Areeba > Lab02 > ASM Task4.s
1  .text
2  .globl main
3  main:
4      li x5, 3 #a -> test val
5      li x6, 4 #b -> assumed base vals for testing
6      li x7, 0 #i
7      li x10, 0x200 #array base address (random)
8
9  loopouter:
10     bge x7, x5, exit #end program
11     li x29, 0 # j will reset everytime i loop 1 iteration end
12
13  loopinner:
14     bge x29, x6, endinner #if j>=b end inner loop
15
16     slli x28, x29, 4 #x28 = j * 16 (byte offset for D[4*j])
17     add x28, x28, x10 #x28 = address of D[4*j]
18
19     add x30, x7, x29 #x30 = i + j
20     sw x30, 0(x28) #D[4*j] = i + j
21
22     addi x29, x29, 1 #j++
23     j loopinner
24
25  endinner:
26     addi x7, x7, 1 #i++
27     j loopouter
28
29  exit:
30     j end
31
32  end:
33     j end
34
```

**Results:**

Integer

x00 (zero) = 0x00000000

x01 (ra) = 0x00000044

x02 (sp) = 0x7FFFFFF0

x03 (gp) = 0x10000000

x04 (tp) = 0x00000000

x05 (t0) = 0x00000003

x06 (t1) = 0x00000004

x07 (t2) = 0x00000003

x08 (s0) = 0x00000000

x09 (s1) = 0x00000000

x10 (a0) = 0x00000200

x11 (a1) = 0x00000000

x12 (a2) = 0x00000000

x13 (a3) = 0x00000000

x14 (a4) = 0x00000000

x15 (a5) = 0x00000000

x16 (a6) = 0x00000000

x17 (a7) = 0x00000000

x18 (s2) = 0x00000000

x19 (s3) = 0x00000000

x20 (s4) = 0x00000000

x21 (s5) = 0x00000000

x22 (s6) = 0x00000000

x23 (s7) = 0x00000000

x24 (s8) = 0x00000000

x25 (s9) = 0x00000000

x26 (s10) = 0x00000000

x27 (s11) = 0x00000000

x28 (t3) = 0x00000230

x29 (t4) = 0x00000004

x30 (t5) = 0x00000005

x31 (t6) = 0x00000000

Memory

Address

+0

+1

+2

+3

0x00000230

05

00

00

00

0x0000022C

00

00

00

00

0x00000228

00

00

00

00

0x00000224

00

00

00

00

0x00000220

04

00

00

00

0x0000021C

00

00

00

00

0x00000218

00

00

00

00

0x00000214

00

00

00

00

0x00000210

03

00

00

00

0x0000020C

00

00

00

00

0x00000208

00

00

00

00

0x00000204

00

00

00

00

0x00000200

02

00

00

00

Address:

Up

Down

Jump to:

-- choose --

Display Format:

Hex

Bytes per Row:

4



Assessment Rubric

Lab 2 - Implementing Decision Instructions in RISC V Assembly Language

Name	Student ID:	Section:
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Points Distribution:

	Task No.	LR 2 (Code)	LR 5 (Results)
In-Lab	Task 1	-	/5
	Task 2	/10	/10
	Task 3	/15	/10
	Task 4	/20	/10
Total Points: 100		/45	/35
CLO Mapped		CLO 2	

Affective Domain Rubric		Points	CLO Mapped
AR7	Report Submission & Git Upload	/10 & /10	CLO 2

CLO	Total Points	Points Obtained
2	100	
Total	100	