

- Q-1** How does an 8052 distinguish references to SFR's from references to memory locations between the address range 80H to FFH.
- Q-2** What problem can occur if one wants to read the count in a running 16 bit timer in 8051? Illustrate with an example. Write code in 8051 assembly language which will avoid this problem.
- Q-3** In what circumstances can the data stored in the output latch differ from the data read from its pins? Which kind of 8051 instructions use the latch data for a port instead of using values read from the pins. Give examples.
- Q-4** Why are instructions which set or clear a bit in the bit addressable region regarded as read-modify-write instructions?
- Q-5** We have an 8051 based system using a crystal of 12 MHz. Give complete hardware and software details of what needs to be done to generate a square wave of frequency 1KHz on a pin of Port 0 (say Port 0.0), using timer T0.

Paper Ends
