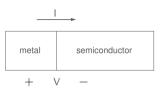
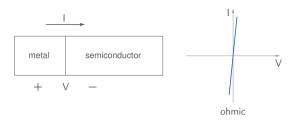


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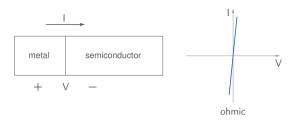
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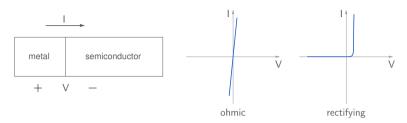
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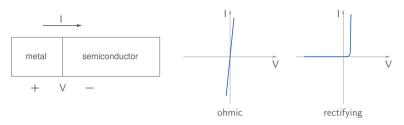
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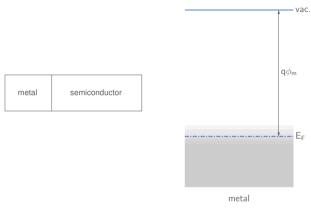
What decides whether a given M-S junction is ohmic or rectifying?

metal semiconductor

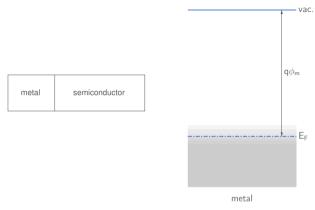
metal semiconductor

The band diagram of a metal-semiconductor junction is determined by

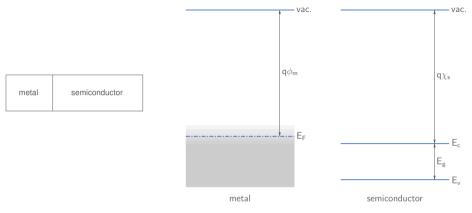
- metal work function  $\phi_m$  (difference between the "vacuum level" and the Fermi level)



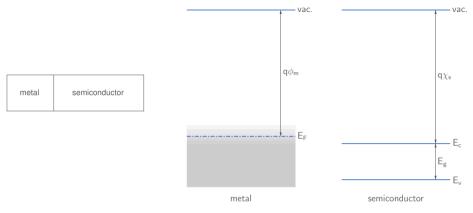
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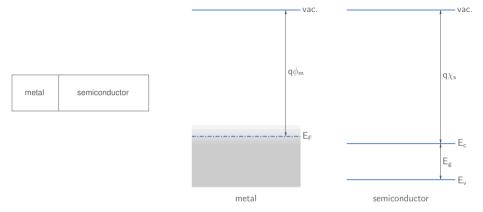
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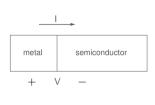
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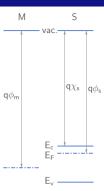


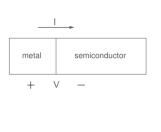
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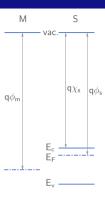


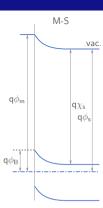
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- additional electron states within the band gap at the interface
   (We will ignore this effect, i.e., we will assume the M-S interface to be perfect.)

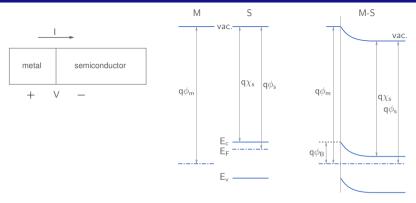




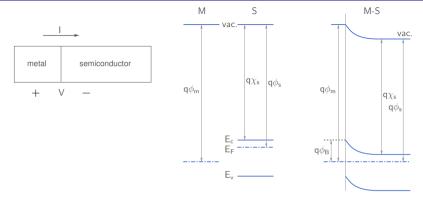




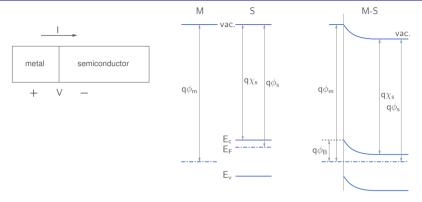




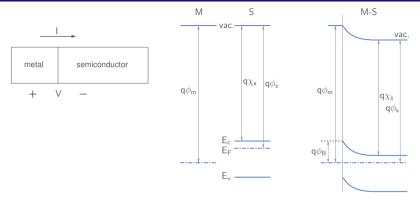
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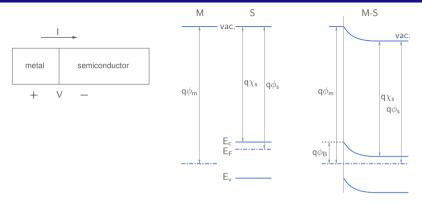
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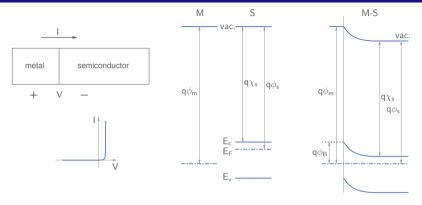
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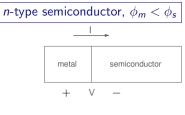
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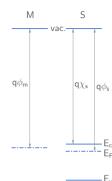


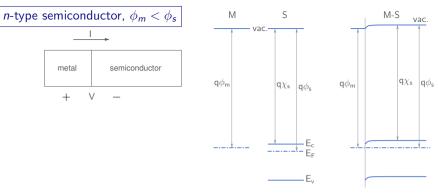
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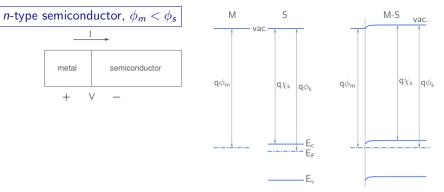


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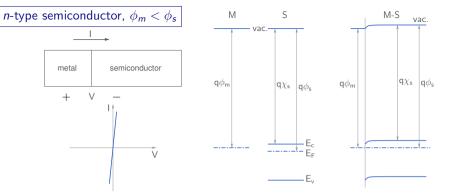




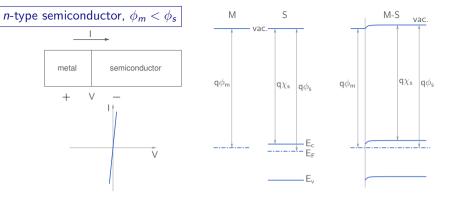




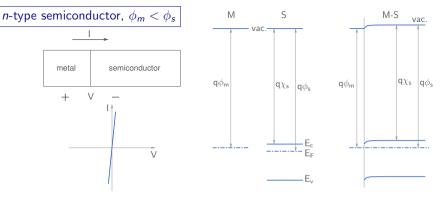
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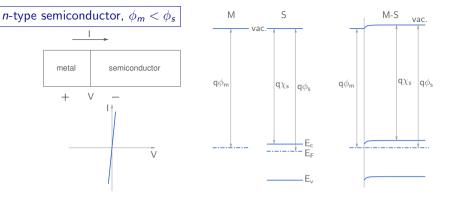
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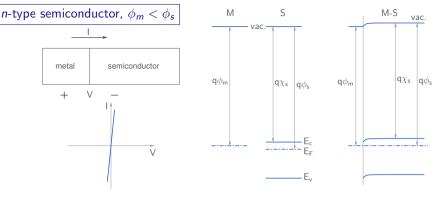
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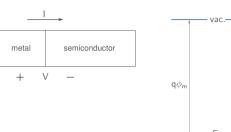


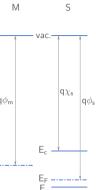
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Because of these complications, the barrier heights get modified. However, the qualitative picture remains valid as long as the actual experimentally measured barrier heights are used.

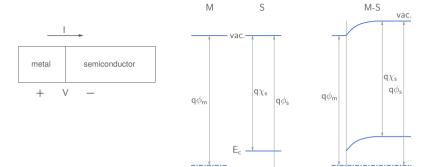
M. B. Patil. IIT Bombay

# $\emph{p}\text{-type}$ semiconductor, $\phi_{\emph{m}}<\phi_{\emph{s}}$

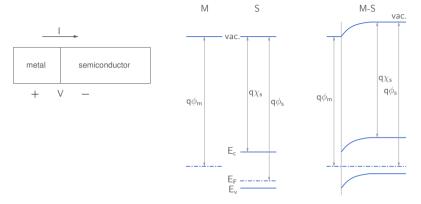




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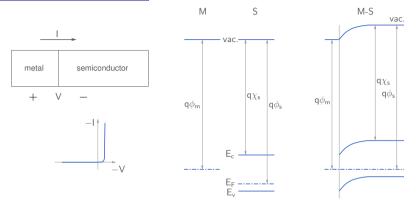


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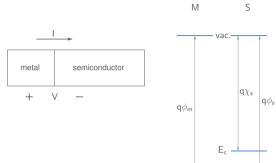


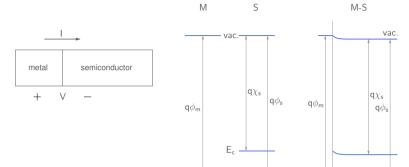
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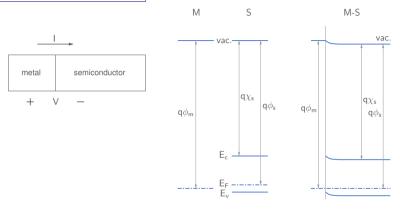


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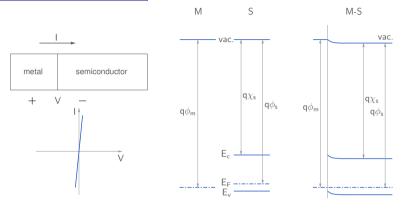


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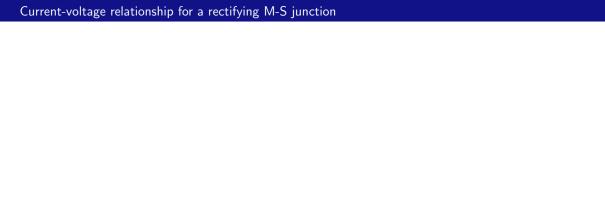


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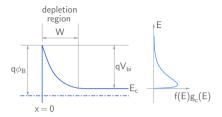


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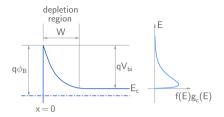
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- In an M-S junction, minority carriers play no role in current conduction, and it is the injection of the majority carriers from semiconductor to metal which determines the current.

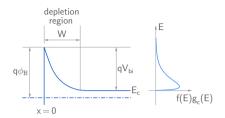


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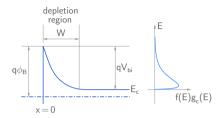
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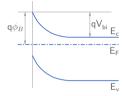
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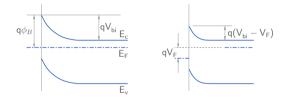
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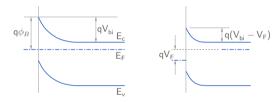


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- \* In equilibrium, there is an equal and opposite current density,  $J_{M \to S} = -J_{S \to M} = -A^* T^2 e^{-\phi_B/V_T}$ , resulting in a net current density J = 0.

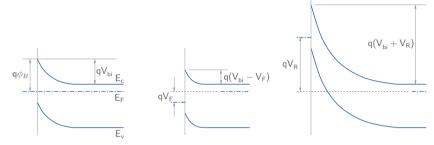






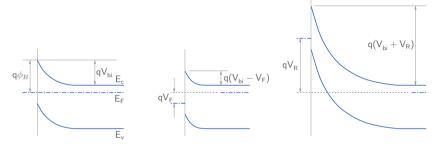
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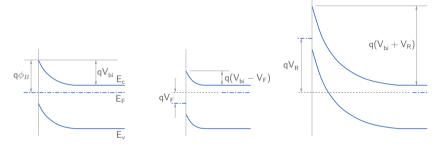
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- \* For a M-S junction, current conduction is dominated by thermionic emission of majority carriers. (In addition, there can be a tunnelling component of the current called "thermionic field emission." 1)

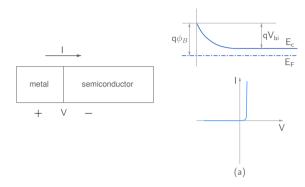
<sup>&</sup>lt;sup>1</sup>M. Shur, *Physics of Semiconductor Devices*. New Delhi: Prentice-Hall India, 1990.

- \* For both pn and rectifying M-S junctions, we have  $J=J_s\left(e^{V_a/V_T}-1\right)$ .
- \* For a pn junction,  $J_s = q \left( \frac{D_n n_{p0}}{L_n} + \frac{D_p p_{n0}}{L_p} \right)$  involves minority carrier densities and lifetimes.
- \* For a M-S junction, current conduction is dominated by thermionic emission of majority carriers. (In addition, there can be a tunnelling component of the current called "thermionic field emission." 1)
- \* As we will see, turn-off of a pn diode involves removal of the excess minority carrier charge, a slow process governed by the lifetime of the minority carriers. In Schottky diodes, there is no such requirement, and therefore they can be turned off much faster.

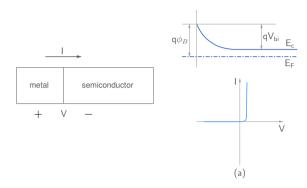
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- Remark: The process of thermionic emission also takes place in a p-n junction, but it can be ignored.

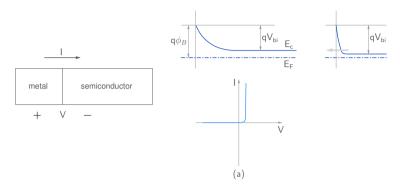
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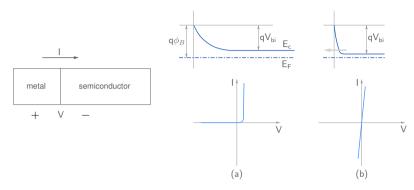
st The contact in (a) is rectifying because of the potential barrier.



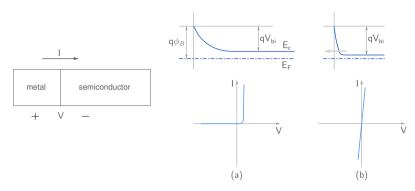
- \* The contact in (a) is rectifying because of the potential barrier.
- If the doping density is increased, the barrier width (depletion region width) decreases, and tunneling of electrons becomes possible even with a small applied voltage (of either polarity)



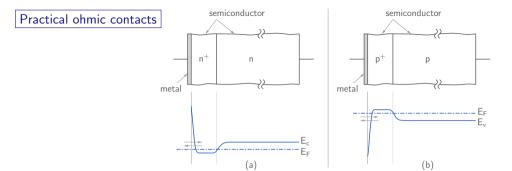
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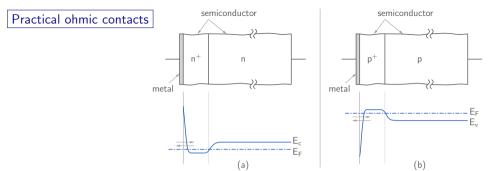


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- \* The contact in (a) is rectifying because of the potential barrier.
- If the doping density is increased, the barrier width (depletion region width) decreases, and tunneling of electrons becomes possible even with a small applied voltage (of either polarity) → an ohmic contact.





\* In many practical situations, ohmic contacts are required to be made to an *n*-type or *p*-type semiconductor region with a moderate doping density, and a metal which will form an ohmic contact is either not available or is not technologically convenient.

# Practical ohmic contacts semiconductor n+ n metal metal E<sub>c</sub> E<sub>F</sub>

(a)

\* In many practical situations, ohmic contacts are required to be made to an *n*-type or *p*-type semiconductor region with a moderate doping density, and a metal which will form an ohmic contact is either not available or is not technologically convenient.

(b)

\* In such cases, a heavily doped region (of the same type) is first created, forming an  $n^+n$  or  $p^+p$  junction.

# Practical ohmic contacts semiconductor n+ n metal metal E<sub>E</sub> E<sub>V</sub>

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- \* In such cases, a heavily doped region (of the same type) is first created, forming an  $n^+n$  or  $p^+p$  junction.
- \* These  $n^+n$  or  $p^+p$  junctions are essentially ohmic since a large number of majority carriers (of the same type) are available for conduction on both sides of the junction.

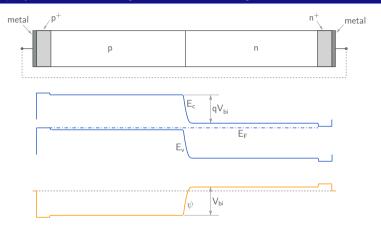
# Practical ohmic contacts semiconductor n+ n metal metal E<sub>c</sub> E<sub>c</sub> E<sub>v</sub>

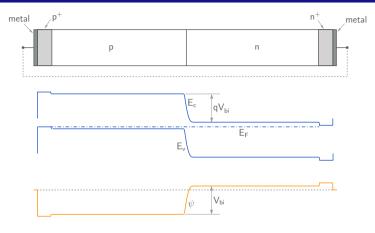
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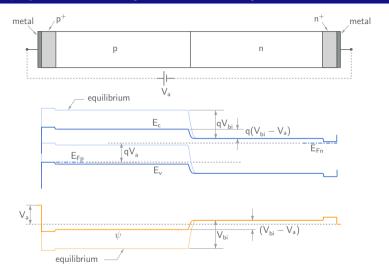
(b)

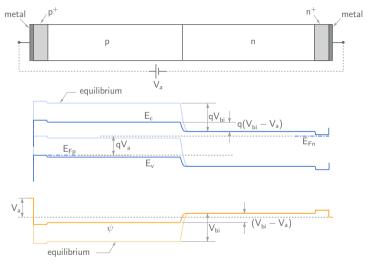
- \* In such cases, a heavily doped region (of the same type) is first created, forming an  $n^+n$  or  $p^+p$  junction.
- \* These  $n^+n$  or  $p^+p$  junctions are essentially ohmic since a large number of majority carriers (of the same type) are available for conduction on both sides of the junction.
- \* Next, metal is deposited to make a metal- $p^+$  or metal- $p^+$  junction, which is ohmic irrespective of the barrier  $\phi_B$  because of tunnelling. In this manner, the objective of making a low-resistance metallic contact is achieved. (In practice, metallic contacts also need to be "alloyed" by subjecting them to temperatures of  $\sim 450\,^{\circ}\mathrm{C}$  for a few minutes.)



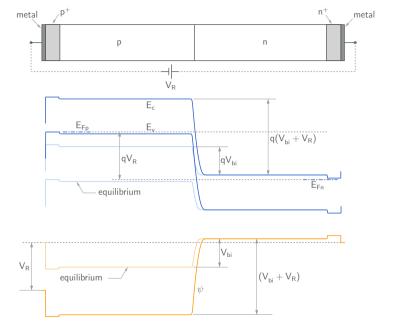


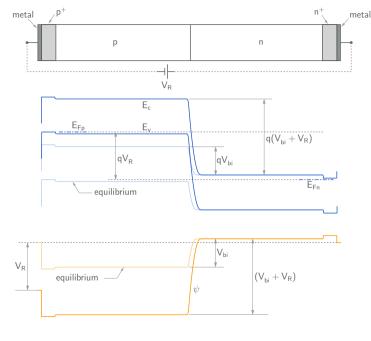
\* Equilibrium: The net voltage drop is zero; the voltage drop ( $V_{\rm bi}$ ) across the depletion region is equal and opposite to the sum of the other voltage drops.





\* Forward bias: The voltage drops across the M-S junctions, the  $n^+$ -n junction, and the  $p^+$ -p junction remain the same as in equilibrium; the applied forward voltage appears across the depletion region  $(V_{\rm bi} \rightarrow V_{\rm bi} - V_a)$ .





\* Reverse bias: The voltage drops across the M-S junctions, the  $n^+$ -n junction, and the  $p^+$ -p junction remain the same as in equilibrium; the applied reverse voltage appears across the depletion region ( $V_{\rm bi} \rightarrow V_{\rm bi} + V_R$ ).