Logic Optimization Heuristic Based

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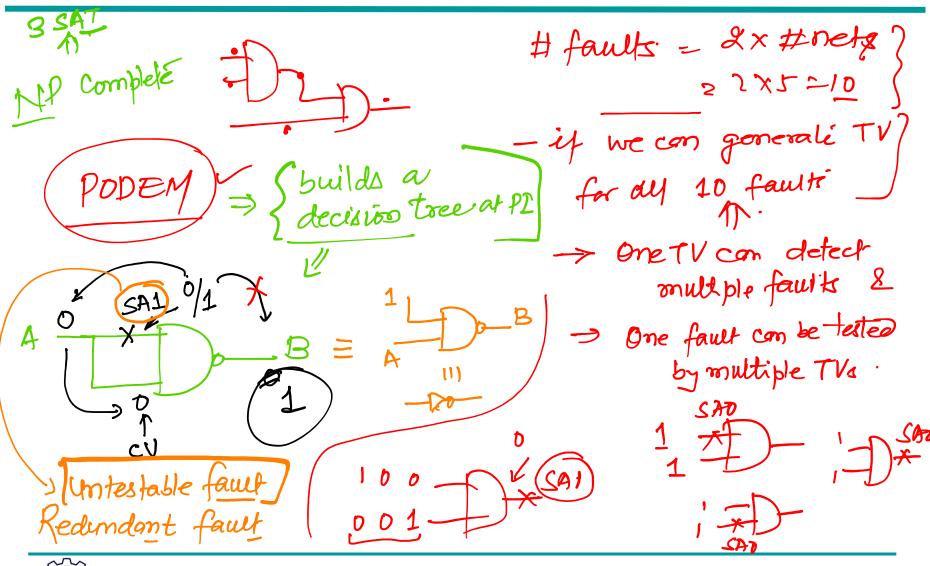


EE-677: Foundations of VLSI CAD



Lecture 25 on 07 Oct 2021

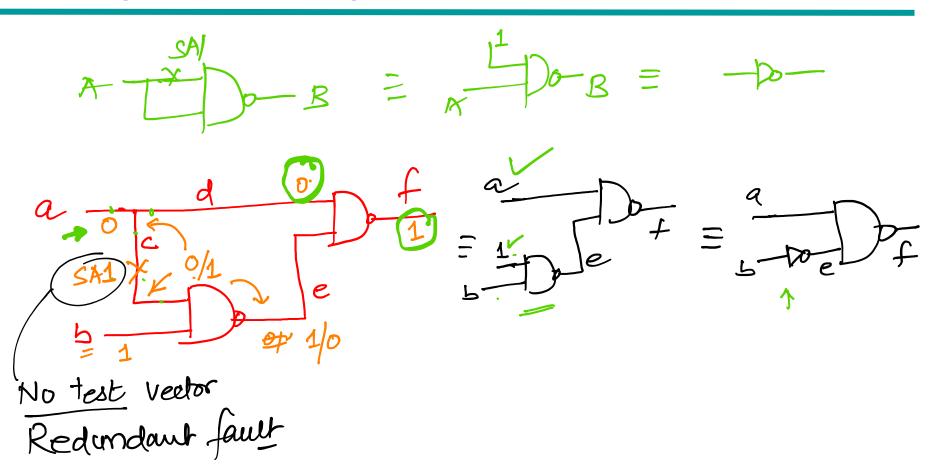
CADSL





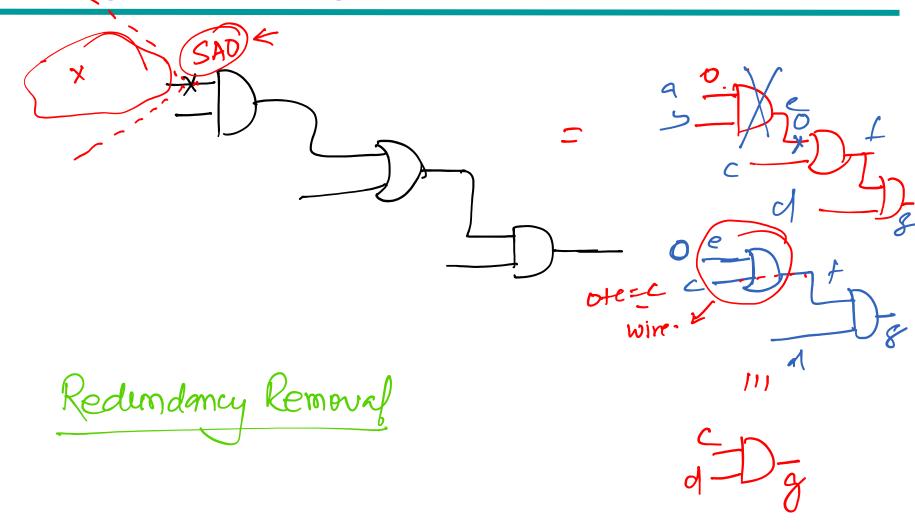
Either we generate a TV for a famely it declare this fault as redundant Boolean Dell Identification of redundant fault. 50 -> Red.] FC = 950 ×100 : 95% # Total fault # detectable Faul Efficiency # red. faults













CADSL

Heuristic based 2 Level Logic Minimization Exact Method > QM algo (EPI) - Petricks method - Petricks method - JLP



Some more background

- Function f (x₁, x₂,, x_i,, x_n)
- Cofactor of f with respect to variable x_i - $f_{xi} = f(x_1, x_2, ..., 1, ..., x_n)$
- Cofactor of f with respect to variable x_i'

$$- f_{xi'} = f (x_1, x_2, ..., 0, ..., x_n)$$

Boole's expansion theorem:

- f (
$$x_1$$
, x_2 , ..., x_i , ..., x_n) = x_i f x_i + $x_{i'}$ f $x_{i'}$

- Also credited to Claude Shannon

Example

- Function: f = ab + bc + ac
- Cofactors:

$$-f_a = b + c$$

$$-f_{a'} = bc$$

• Expansion:

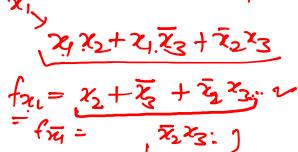
$$- f = a f_a + a' f_{a'} = a(b + c) + a' bc$$



Unateness →

- UNATE CONTRACTOR
- Function f (x₁, x₂,, x_i,, x_n)
- Positive unate in x_i when:

$$-f_{xi} \geq f_{xi'}$$



Negative unate in x_i when:

$$-f_{xi} \leq f_{xi'}$$

 A function is positive/negative unate when positive/negative unate in all its variables

Operators

- Function f (x₁, x₂,, x_i,, x_n)
- Boolean difference of f w.r.t. variable x_i:
 - $\partial f/\partial x_i \equiv f_{xi} \oplus f_{xi'}$
- Consensus of f w.r.t. variable x_i:

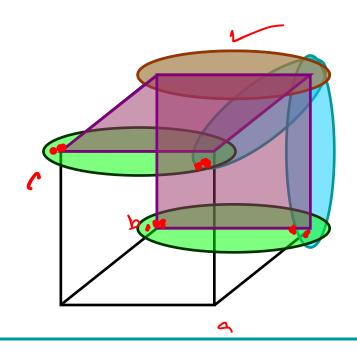
$$-C_{xi} \equiv f_{xi} \cdot f_{xi'}$$

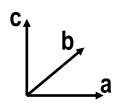
• *Smoothing* of f w.r.t. variable x_i:

$$-S_{xi} \equiv \underbrace{f_{xi}}_{xi} + \underbrace{f_{xi'}}_{xi'}$$

Example f = ab + bc + ac

- The Boolean difference $\partial f/\partial a = f_a \oplus f_{a'} = b'c + bc'$
- The consensus $C_a = f_a \cdot f_{a'} = bc$
- The smoothing $S_a \equiv f_a + f_{a'} = b + c$





BEBQUT



Thank You



