# High Level Synthesis Scheduling

### Virendra Singh

Computer Architecture and Dependable Systems Lab



http://www.ee.iitb.ac.in/~viren/

E-mail: viren@ee.iitb.ac.in



EE-677: Foundations of VLSI CAD



**CADSL** 

## **Architectural Synthesis**

#### **Architectural Level Abstraction**

- Datapath
- Controller

#### Architectural Synthesis

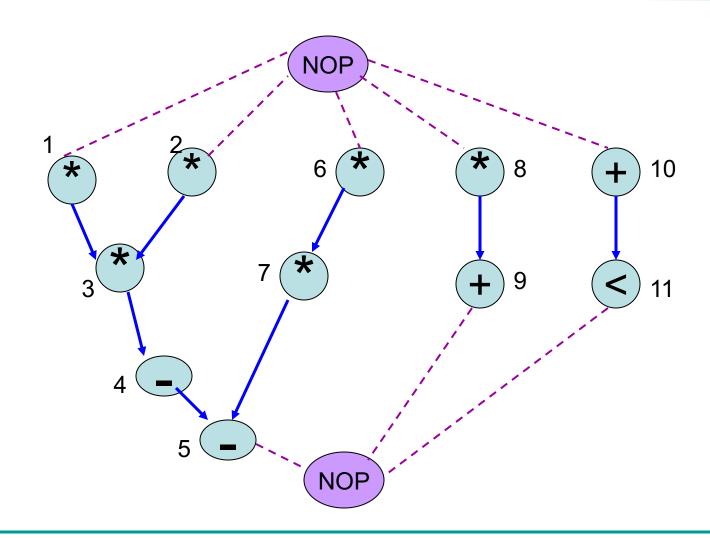
Constructing the <u>macroscopic structure</u> of a digital circuit starting from behavioural models that can be captured from Data flow or Sequencing Graph





## Sequencing Graph









## Temporal Domain: Scheduling

Delay **D** = {
$$d_i$$
; i = 0,1, 2, ..... n}

Start time T =
$$\{t_i; i= 0, 1, ...., n\}$$

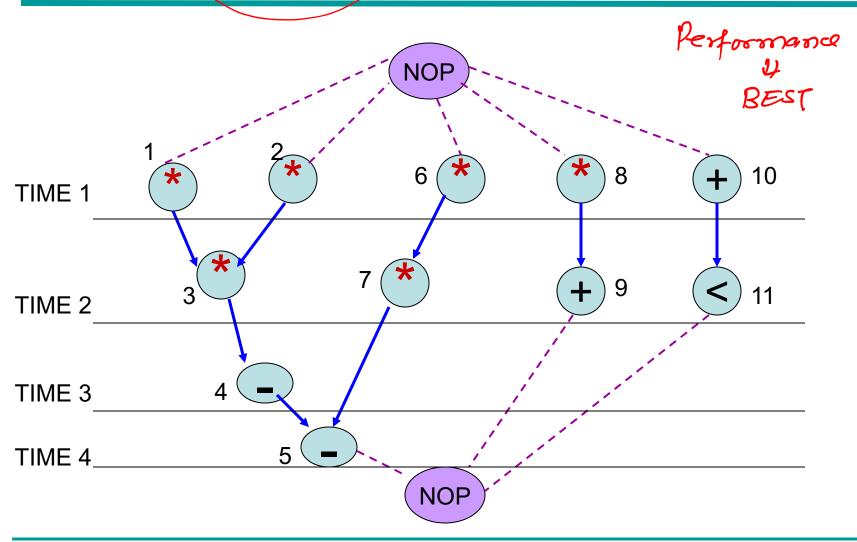
Scheduling: Task of determining the start timing, subject to preceding constraints specified by sequencing graph

Latency 
$$\lambda = t_n - t_0$$





## ASAP Scheduling







#### slack | flebility **ALAP Scheduling** latency NOP A=5 time 2 TIME 1 6 time 3 TIME 2 10 timely TIME 3 9 time 5 TIME 4 NOP



EE-677@IITB

**CADSL** 

Pruear => sourt(x)

 $\lim_{x \to \infty} \frac{1}{2} \int_{-\infty}^{\infty} \frac$ 

Large problem

M- note variables

Mr - constraints

## Scheduling with Resource Constraint

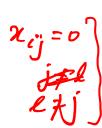
#### **ILP Formulation**



Binary decision variable  $X = \{x_{ii}\}$ 

1. Start time of each operation is unique

$$\Sigma_{l} x_{il} = 1$$



2. Sequencing relations represented by G<sub>s</sub>(V,E) must be satisfied

$$\Sigma_{l} | I.x_{il} \ge \Sigma_{l} | I.x_{jl} + d_{j}$$

3. Resource bound must be met at every schedule step

$$\Sigma_k \Sigma_m x_{im} \le \hat{a_k}$$



## Minimum Resource Scheduling under Latency Constraint

20,14

Binary decision variable 
$$X = \{x_{il}\}$$

1. Start time of each operation is unique

$$\Sigma_{\rm I} x_{\rm il} = 1$$

2. Sequencing relations represented by G<sub>s</sub>(V,E) must be satisfied

$$\Sigma_{l} x_{il} \ge \Sigma_{l} xjl + d_{i}$$

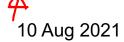
3. Resource bound must be met at every schedule step

$$\Sigma_k \Sigma_m x_{im} - a_k \leq 0$$
 variable.

4. Latency Constraints

$$\Sigma_{l} I. x_{nl} \leq \lambda_{l} + 1$$









## Minimum Resource Scheduling under Latency Constraint

All operation must start only once

$$x_{0,1} = 1$$

$$x_{1,1} = 1$$

$$x_{2,1} = 1$$

$$x_{3,2} = 1$$

$$x_{4,3} = 1$$

$$x_{5,4} = 1$$

$$x_{6,1} + x_{6,2} = 1$$

$$x_{7,2} + x_{7,3} = 1$$

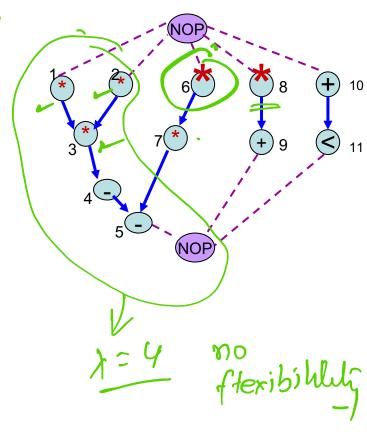
$$x_{8,1} + x_{8,2} + x_{8,3} = 1$$

$$x_{9,2} + x_{9,3} + x_{9,4} = 1$$

$$x_{10,1} + x_{10,2} + x_{10,3} = 1$$

$$x_{11,2} + x_{11,3} + x_{11,4} = 1$$

$$x_{n,5} = 1$$







### Minimum Resource Scheduling under **Latency Constraint**

Constraints – based on sequencing

(more than one starting time for at. least one operation)

$$2 x_{7,2} + 3 x_{7,3} - x_{6,1} - 2 x_{6,2} - 1 \ge 0$$

$$2 x_{9,2} + 3 x_{9,3} + 4 x_{9,4} - x_{8,1} - 2 x_{8,2} - 3 x_{8,3} - 1 \ge 0$$

$$2 x_{11.2} + 3 x_{11.3} + 4 x_{11.4} - x_{10.1} - 2 x_{10.2} - 3 x_{10.3} - 1 \ge 0$$

$$4 x_{5,4} - 2 x_{7,2} - 3 x_{7,3} - 1 \ge 0$$

$$5 x_{n,5} - 2 x_{9,2} - 3 x_{9,3} - 4 x_{9,4} - 1 \ge 0$$

$$5 x_{n,5} - 2 x_{11,2} - 3 x_{11,3} - 4 x_{11,4} - 1 \ge 0$$







## Minimum Resource Scheduling under **Latency Constraint**

#### **Resource Constraints**



$$X_{1,1} + X_{2,1} + X_{6,1} + X_{8,1} - a_1 \le 0$$



$$X_{3,2} + X_{6,2} + X_{7,2} + X_{8,2} - a_1 \le 2$$

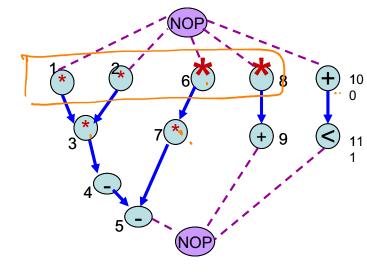
$$x_{7.3} + x_{8.3} - a_1 \le 0$$

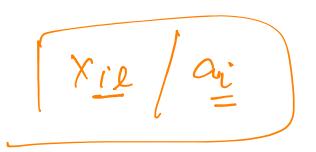
$$x_{10,1} - a_2 \le 0$$

$$x_{9,2} + x_{10,2} + x_{11,2} - a_2 \le 0$$

$$x_{4,3} + x_{9,3} + x_{10,3} + x_{11,3} - a_2 \le 0$$

$$x_{5.4} + x_{9.4} + x_{11.4} - a_2 \le 0$$









## Minimum Resource Scheduling under Latency Constraint

Optimize 
$$c^{T}a$$
  
c = [5, 1]

$$c = [5, 1]$$

Objective function to minimize =  $c^{T}a = 5$ .  $a_1 + 1$ .  $a_2$ 

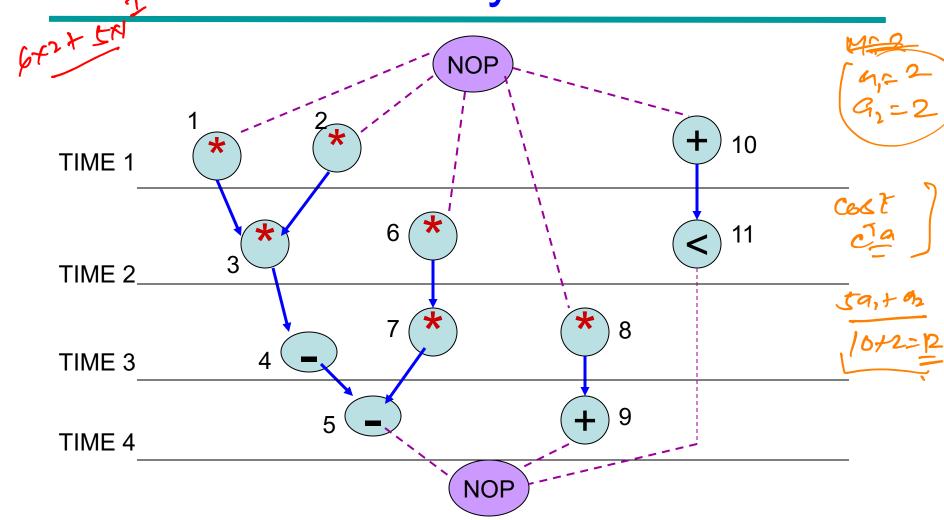
sumif(x) 
$$\wedge$$

Mut 
$$ALU$$
 $5$ 
 $1$ 
 $a_1$ 
 $a_2$ 
 $c^Ta = 5$ .  $a_1 + 1$ .  $a_2$ 
 $a_1$ 
 $a_2$ 
 $a_1$ 
 $a_2$ 
 $a_1$ 
 $a_2$ 
 $a_1$ 
 $a_2$ 
 $a_1$ 



## Optimum Scheduling under Latency Constraint







EE-677@IITB

**CADSL** 

X+B=1 Co-optimszalion

## Approximation Scheduling Algorithms

- Hardware scheduling is modeled by sequencing graph with possibly multiple-cycle operations with different types
- Minimum latency resource constraint scheduling is known to be intractable
- Heuristic Solution
  - ✓ List Scheduling V
  - ✓ Force-directed Scheduling
  - ✓ Trace scheduling





- Minimize latency under resource constraint a
- Candidate operation U<sub>I,k</sub>
  - Operation of type k whose predecessors have already been scheduled early enough so that the corresponding operations are completed at step l
- Unfinished operations T<sub>I,k</sub>
- Priority list





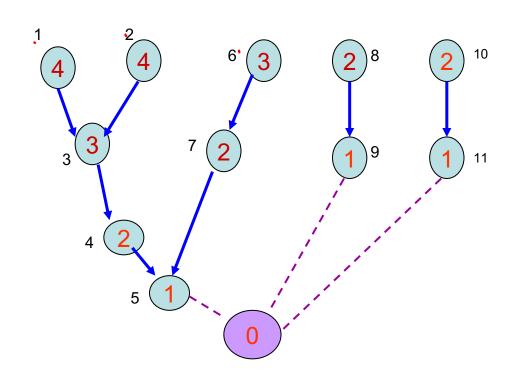


```
LIST_SCHED (G_s(v,E)){
     Repeat {
          for each resource type k = 1,2,...., n<sub>res</sub> {
                     Determine candidate operation U_{l,k};
                     Determine unfinished operations T_{l,k}; — on going
                    Select S_k \equiv U_{l,k} vertices, s.t. |S_k| + |T_{l,k}| \le a_k;
                     Schedule the S_k operation at step / by setting t_i = / for all i: v_k \in S_k;
              l = l+1;
          Until (v<sub>n</sub> is scheduled);
          Return (t); }
```



- Assume  $a_1 = 2$  multipliers and  $a_2 = 2$  ALUs
- Assume operations have unit delay
- **Priority function**







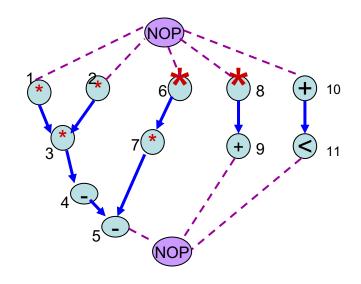


#### First Step

- $\star$  K = 1, U<sub>1,1</sub> = {v<sub>1</sub>, v<sub>2</sub>, v<sub>6</sub>, v<sub>8</sub>}
- Selected Operations are

$$\triangleright$$
 { $v_1$ ,  $v_2$ }

- Label is maximal
- $\star$  K = 2, U<sub>1,2</sub> = {v<sub>10</sub>}
- Selected Operation
  - > {v<sub>10</sub>}



$$S_1 = \{v_1, v_2, v_{10}\}$$





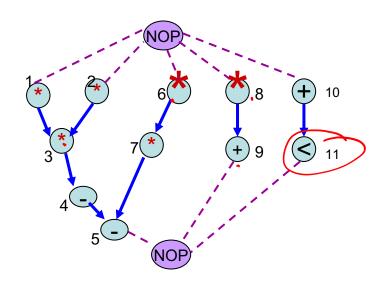
#### Second Step

$$\star$$
 K = 1, U<sub>2,1</sub> = {v<sub>3</sub>, v<sub>6</sub>, v<sub>8</sub>}

- Selected Operations are
  - $\triangleright$  { $v_3, v_6$ }
  - Label is maximal

$$\star$$
 K = 2, U<sub>1,2</sub> = { $v_{11}$ }

- Selected Operation
  - > {v<sub>11</sub>}



$$S_2 = \{v_3, v_6, v_{11}\}$$



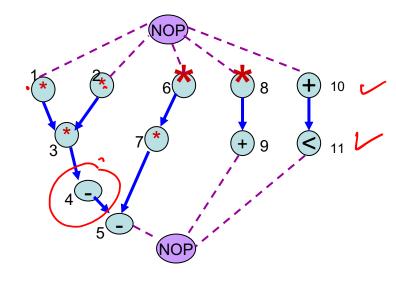


#### Third Step

- $\star$  K = 1, U<sub>3,1</sub> = {v<sub>7</sub>, v<sub>8</sub>}
- Selected Operations are

$$\triangleright$$
 { $v_7, v_8$ }

- $\star$  K = 2, U<sub>1,2</sub> = {v<sub>4</sub>}
- Selected Operation
  - > {v<sub>4</sub>}



$$S_3 = \{v_7, v_8, v_4\}$$





#### Fourth Step

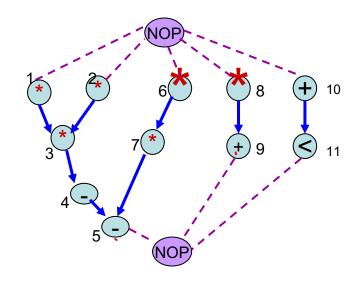
**⋄** K = 1, 
$$U_{3,1} = \Phi$$

$$\star$$
 K = 2, U<sub>1,2</sub> = {v<sub>5</sub>, v<sub>9</sub>}

Selected Operation

$$ightharpoonup \{v_{5}, v_{9}\}$$



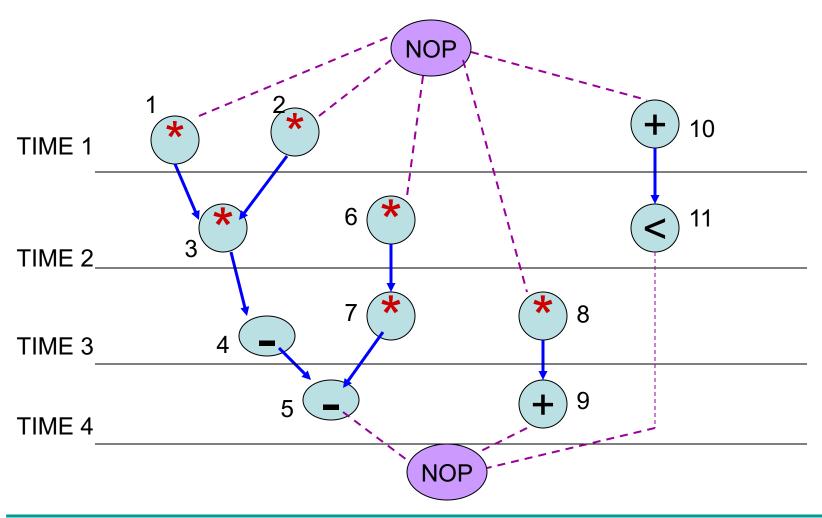


$$S_4 = \{v_5, v_9\}$$





## Optimum Scheduling under Resource Constraint







- Assume  $a_1 = 3$  multipliers and  $a_2 = 1$  ALU
- Multiplier Delay = 2 and ALU delay = 1
- Priority function (weight of longest path to sink node)





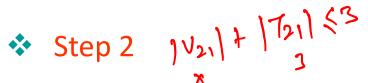
$$U_{1,1} = \{v_1, v_2, v_6, v_8\}$$

$$U_{1,2} = \{v_{10}\}$$

$$T_{1,1} = \Phi$$

$$T_{1,2} = \Phi$$

$$S_1 = \{v_1, v_2, v_6, v_{10}\}$$



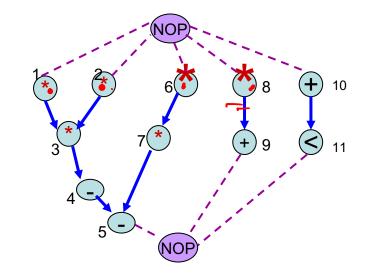
$$U_{2,1} = \{v_8\}$$

$$U_{2,2} = \{v_{11}\}$$

$$T_{2,1} = \{v_1, v_2, v_6\}$$

$$T_{2,2} = \Phi$$

$$S_2 = \{ v_{11} \}$$





$$U_{3,1} = \{v_8, v_3, v_7\}$$

$$U_{3,2} = \Phi$$

$$T_{3,1} = \Phi$$

$$T_{3,2} = \Phi$$

$$S_3 = \{v_8, v_3, v_7\}$$





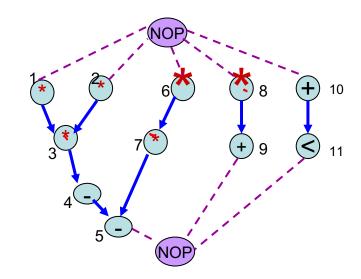
$$U_{4,1} = \Phi$$

$$U_{4,2} = \Phi$$

$$T_{4,1} = \{v_8, v_3, v_7\}$$

$$T_{4,2} = \Phi$$

$$S_4 = \Phi$$



#### Step 5

$$U_{5,1} = \Phi$$

$$U_{5,2} = \{v_4, v_9\}$$
 $T_{5,2} = \Phi$ 

$$T_{5,1} = \Phi$$

$$T_{5,2} = \Phi$$

$$S_5 = \{v_4\}$$



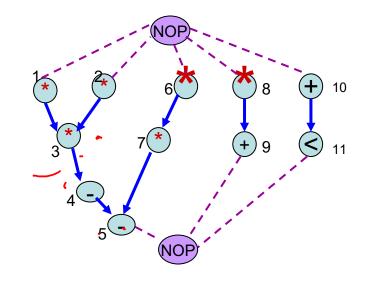
$$U_{6,1} = \Phi$$

$$U_{6,2} = \{v_5, v_9\}$$

$$T_{6,1} = \Phi$$

$$T_{6,2} = \Phi$$

$$S_6 = \{v_5\}$$



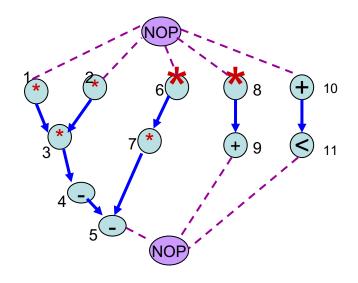
$$U_{7,1} = \Phi$$

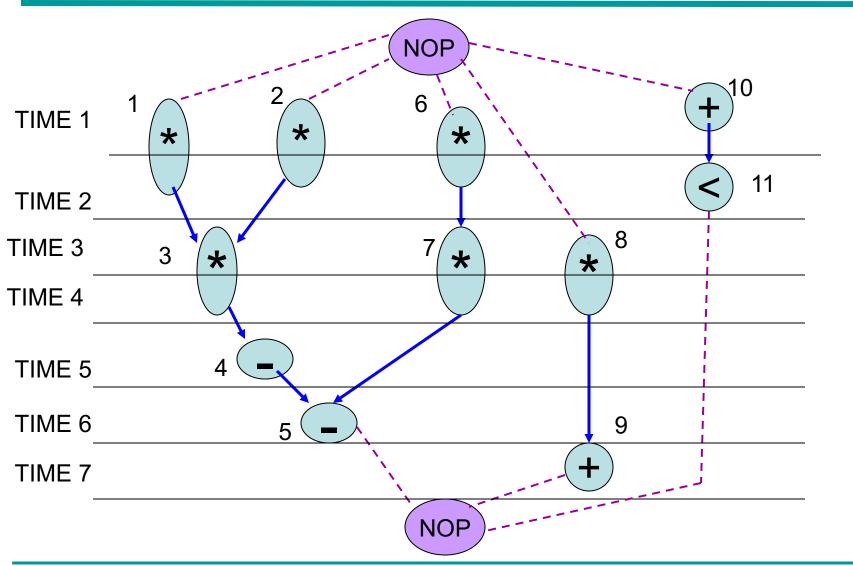
$$T_{7,1} = \Phi$$

$$S_7 = \{v_9\}$$

$$U_{7,2} = \{v_9\}$$
  
 $T_{7,2} = \Phi$ 

$$T_{7,2} = \Phi$$









List Scheduling
Variable

Eliail > Eding: + di

Binding
Automation



## Thank You



