## Microinstruction sequences for some instructions:

Instruction	shifter ALU	C bus	Memory	B bus
Main Loop				
(fetch)	00 110101	000000100	0.01	0.001
	00 110101	000000100	001	0001
NOP		T		1
NOP	00 000000	00000000	000	0000
	00 000000	00000000	000	0000
IADD				
ILOAD				
POP				
101				
		•		l l
GOTO				
WIDE	1	<u> </u>		1
MATOR				
		l		1
IINC				