

# IADD MAR MDR PC MBR CPP LV PC CPP 60 TOS OPC Н Shifter LV

MAR, SP = [SP] - 1; read

# IADD MAR MDR PC MBR CPP LV PC CPP 60 TOS OPC Н Shifter

MAR, SP = [SP] - 1; read

LV

# IADD MAR MDR PC MBR CPP LV PC CPP 60 TOS OPC Н Shifter

MAR, SP = [SP] - 1; read

LV

### IADD MAR MDR PC MBR CPP LV PC CPP 60 TOS OPC Н [SP]-1 Shifter LV

MAR, SP = [SP] - 1; read

### IADD MAR MDR PC MBR CPP LV PC CPP 60 TOS OPC Н [SP]-1 Shifter LV

MAR, SP = [SP] - 1; read

### IADD MAR MDR PC MBR CPP LV PC CPP 60 TOS OPC Н [SP]-1 Shifter LV

MAR, SP = [SP] - 1; read

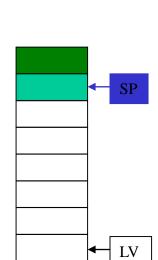
# IADD MAR MDR PC MBR LV CPP 60

TOS

OPC

Н

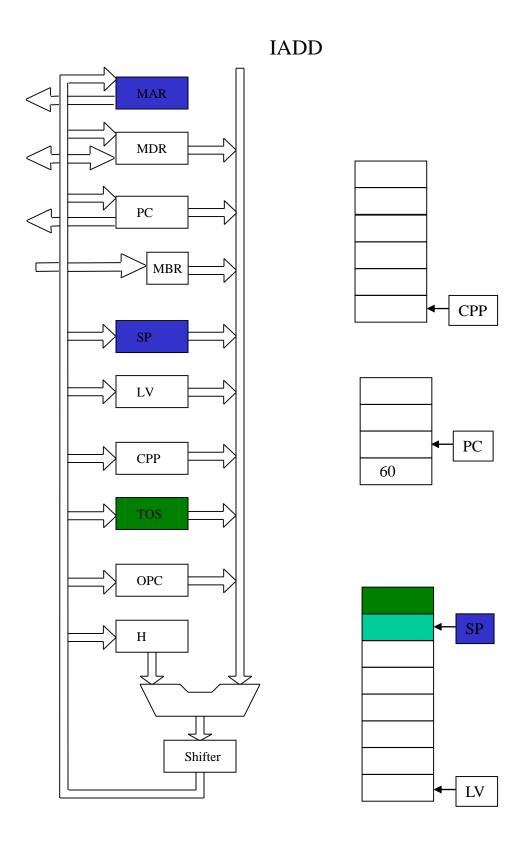
Shifter



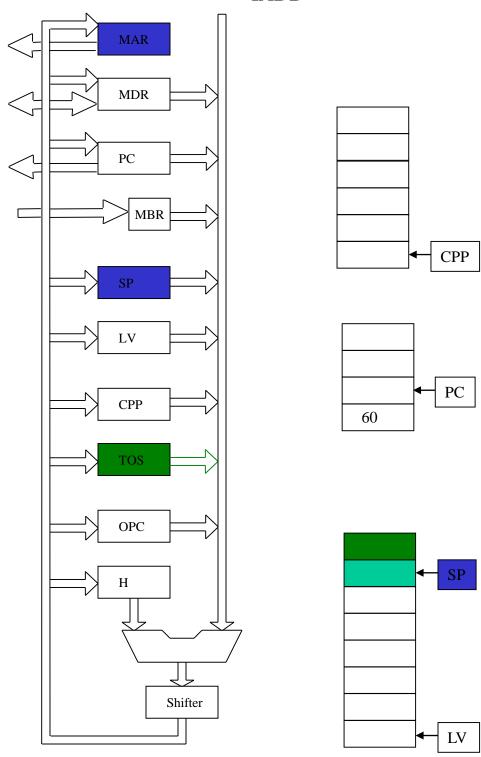
CPP

PC

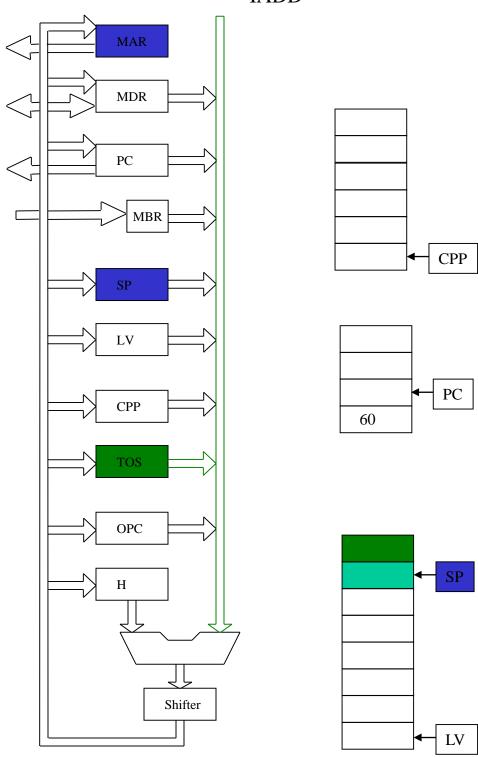
MAR, SP = [SP] - 1; read



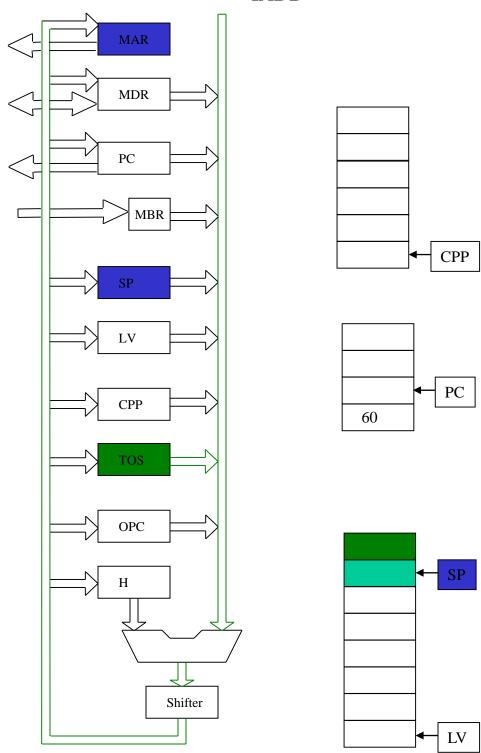
Waiting until end of current cycle

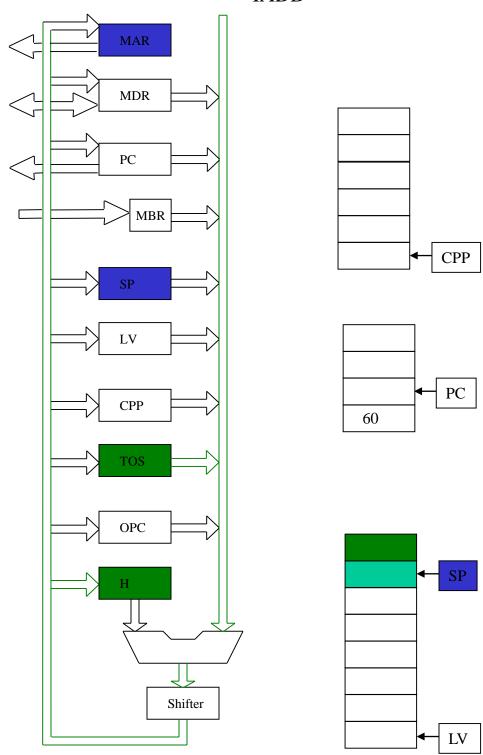


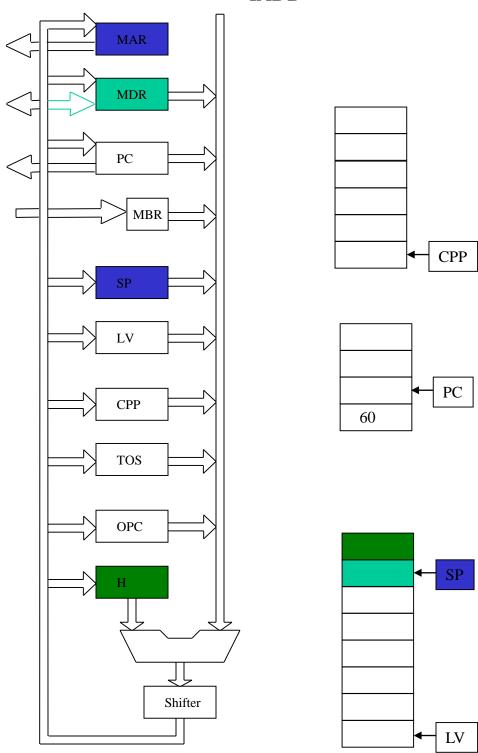
H = [TOS]



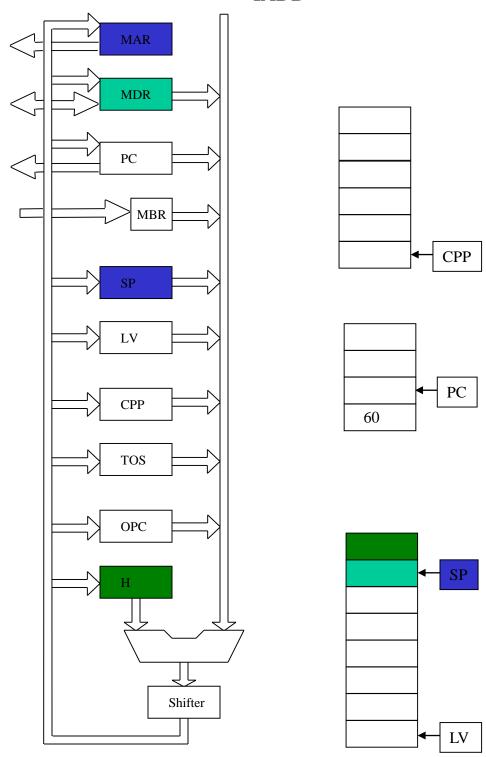
H = [TOS]







H = [TOS]



# IADD MAR MDR PC MBR CPP LV PC CPP 60 TOS OPC Shifter LV

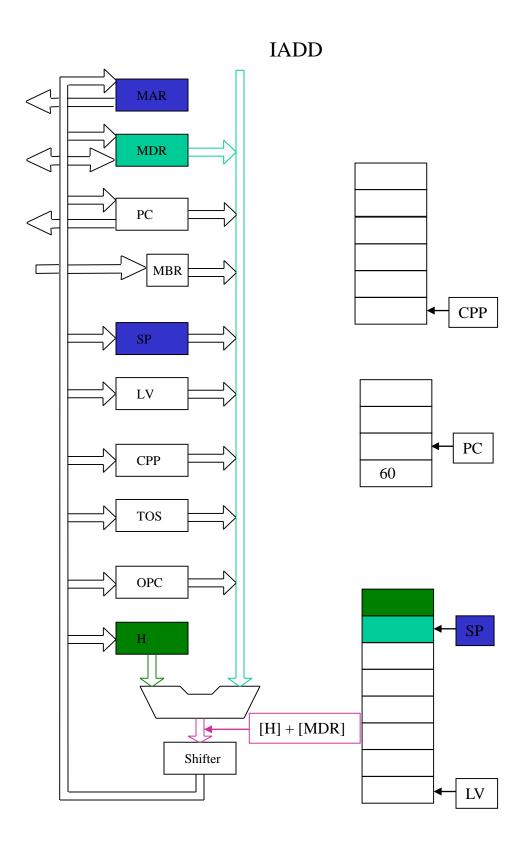
MDR, TOS = [H] + [MDR]; write

# IADD MAR MDR PC MBR CPP LV PC CPP 60 TOS OPC Shifter LV

MDR, TOS = [H] + [MDR]; write

# IADD MAR MDR PC MBR CPP LV PC CPP 60 TOS OPC Shifter LV

MDR, TOS = [H] + [MDR]; write



MDR, TOS = [H] + [MDR]; write

# IADD MAR MDR PC MBR CPP LV PC CPP 60 TOS OPC [H] + [MDR]Shifter LV

MDR, TOS = [H] + [MDR]; write

# IADD MAR MDR PC MBR CPP LV PC CPP 60 TOS OPC [H] + [MDR]Shifter LV

MDR, TOS = [H] + [MDR]; write

# IADD MAR MDR PC MBR CPP LV PC CPP 60 TOS OPC Н Shifter LV

MDR, TOS = [H] + [MDR]; write

# IADD MAR MDR PC MBR CPP LV PC CPP 60 TOS OPC Н Shifter LV

When write completes