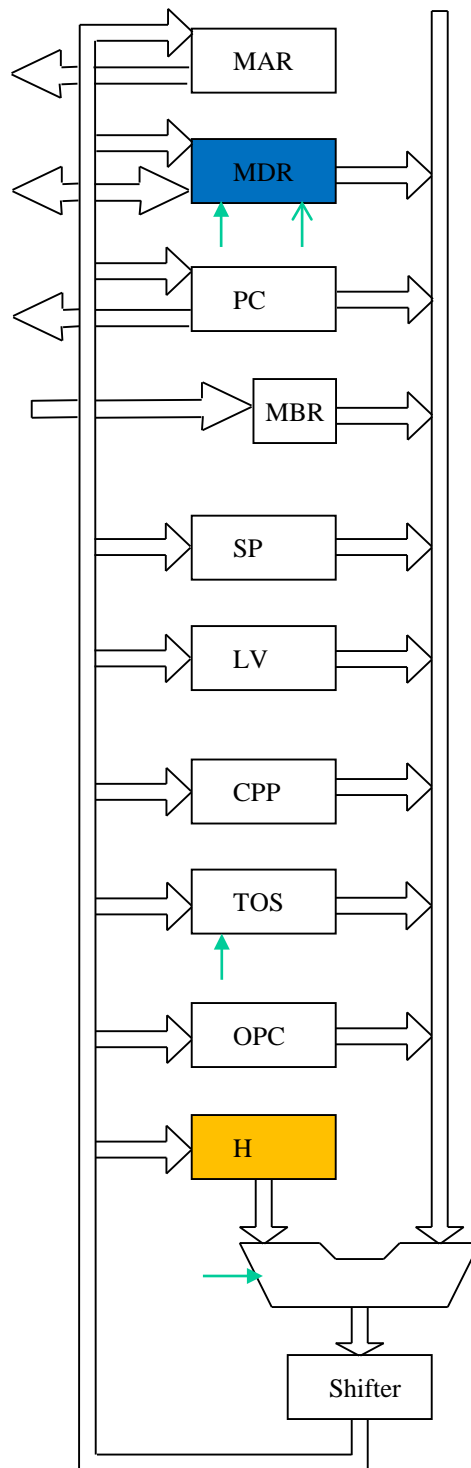
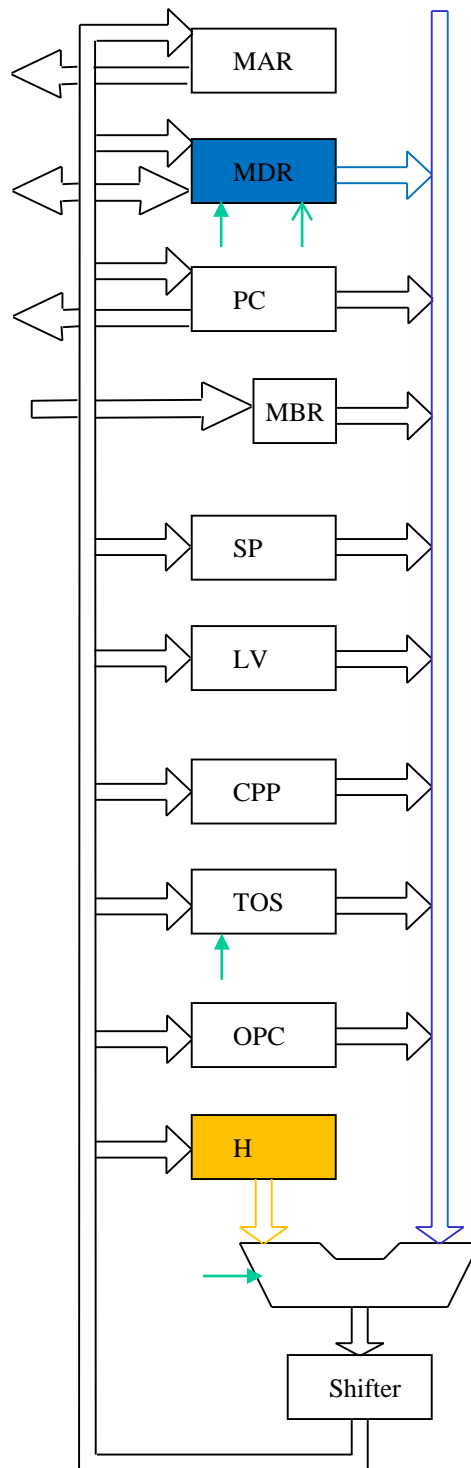
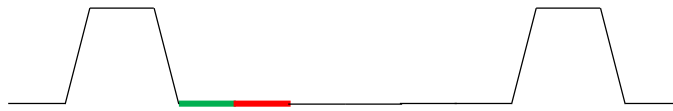


Timing diagram for MDR, TOS = [MDR] + [H]



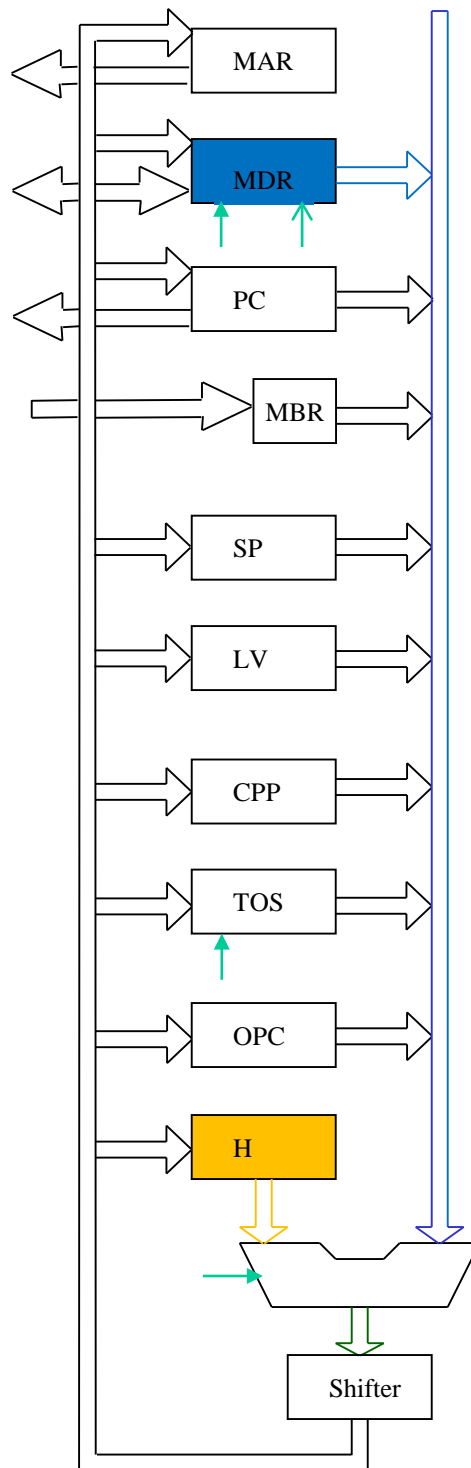
Control signals are set up

$$\mathbf{MDR, TOS = [MDR] + [H]}$$



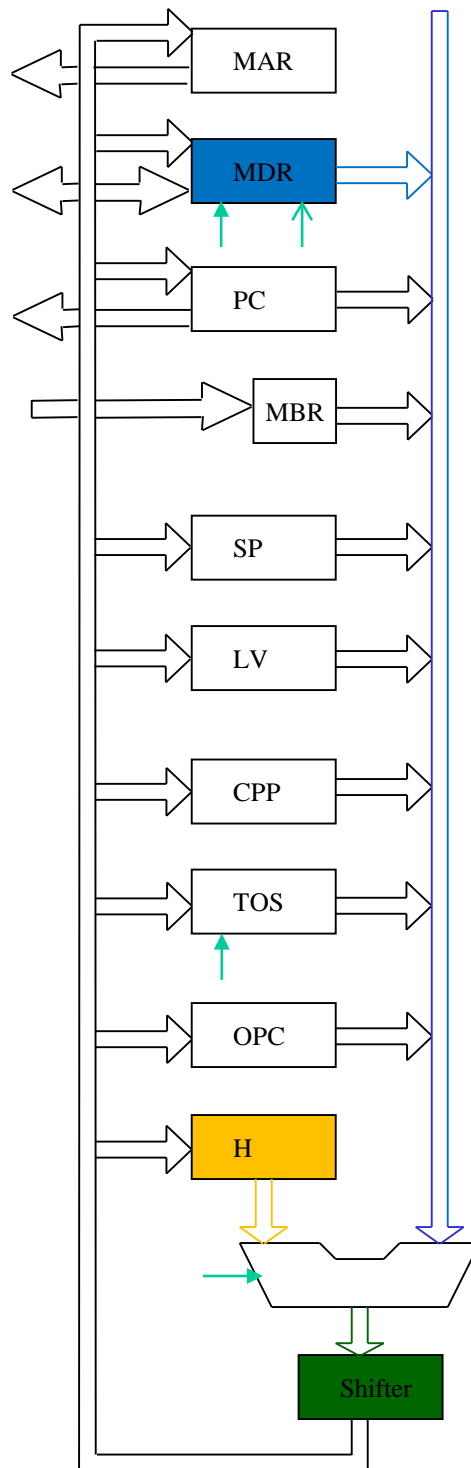
Drive H and B bus

$$\mathbf{MDR, TOS = [MDR] + [H]}$$



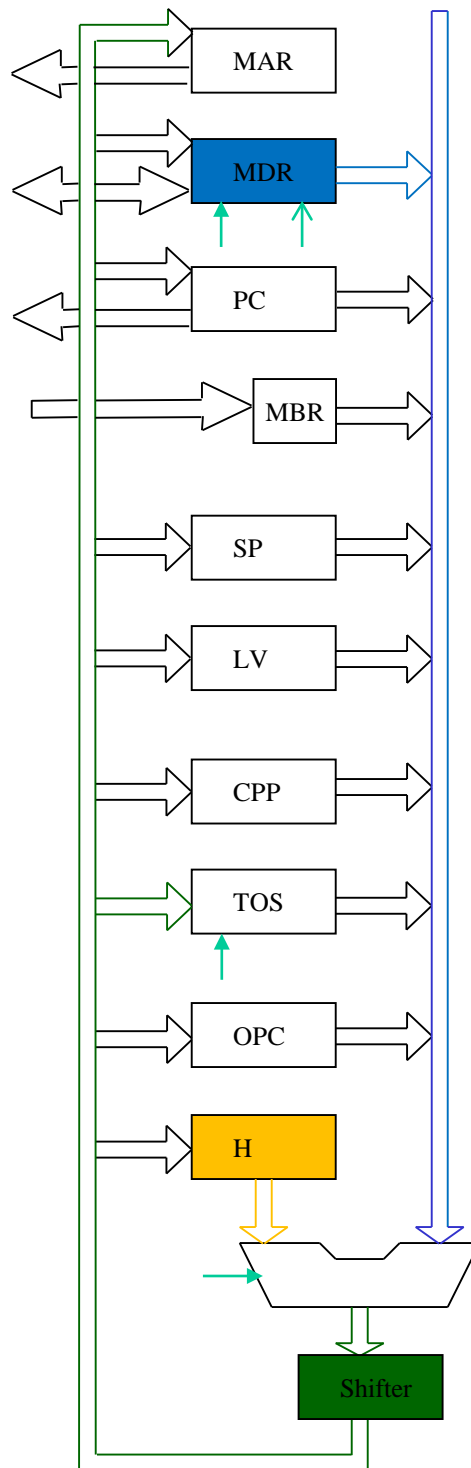
ALU and shifter

$$\mathbf{MDR, TOS = [MDR] + [H]}$$



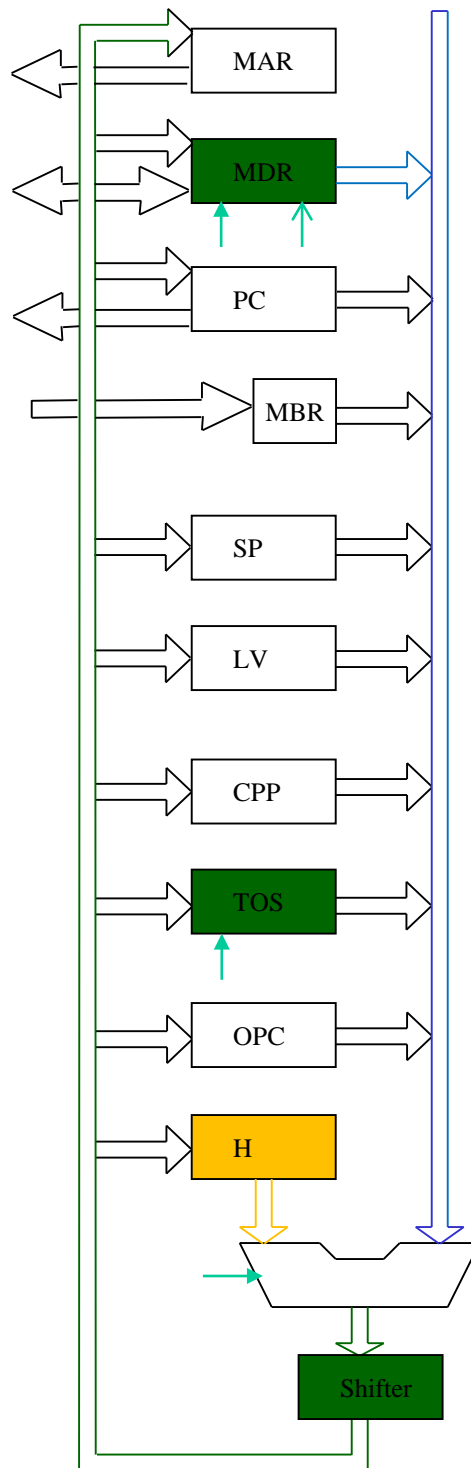
ALU and shifter

$$\mathbf{MDR, TOS = [MDR] + [H]}$$



Propagation from shifter to C bus

$$\mathbf{MDR, TOS = [MDR] + [H]}$$



Registers loaded from C bus

$$\mathbf{MDR, TOS = [MDR] + [H]}$$

