

# Analog IC Design (Xschem, Ngspice, ADT0)

## Lab 03

### Cascode Amplifier

#### Part 1: Device Sizing Using SA

Parameter	Value
$Av = gmro$	50
$g_m/I_D$	10 S/A
Supply ( $V_{DD}$ )	1.8 V
Quiescent (DC) output voltage	$V_{DD}/2 = 0.9 V$
Bias Current	20 $\mu A$

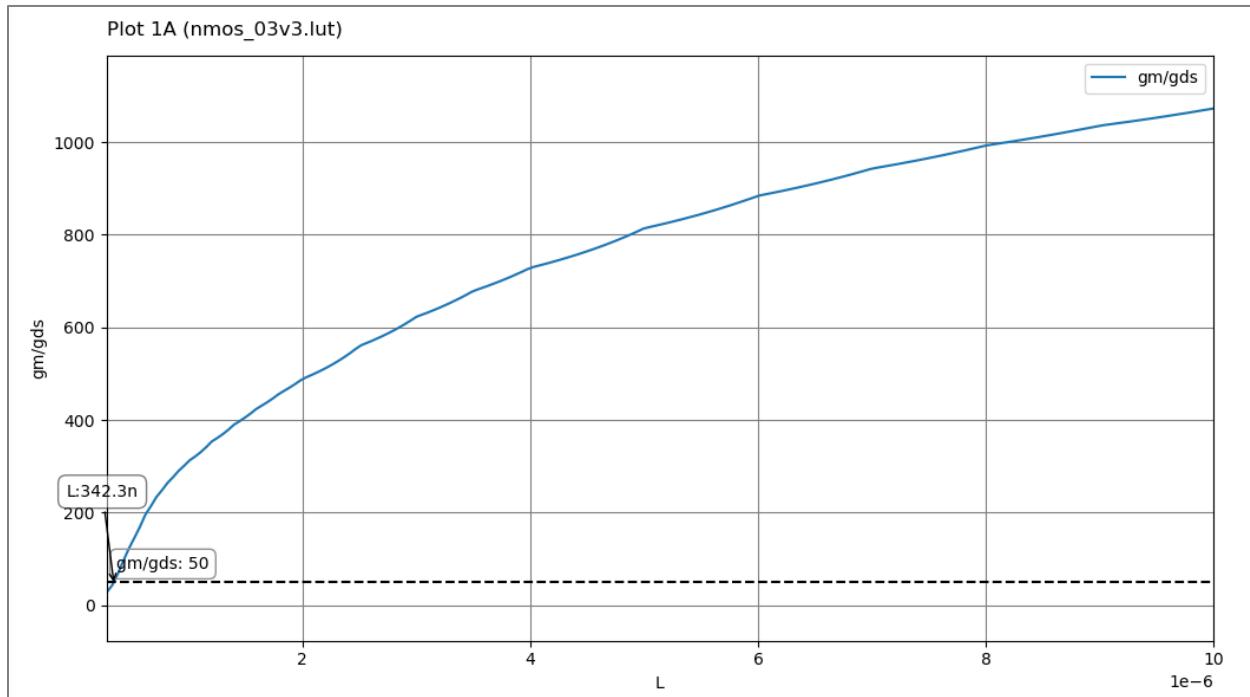


Figure 1:  $gm/gds$  VS  $L$

I chose  $L = 350 \text{ nm}$  instead of  $L=342.3 \text{ nm}$  to add a margin for process variations and to ensure that  $(\frac{g_m}{g_{ds}})$  remains above 50.

ID	20u	?
gm/ID	10	?
L	350n	?
VDS	0.9	?
VSB	0	?
Stack	1	?

**Results:**

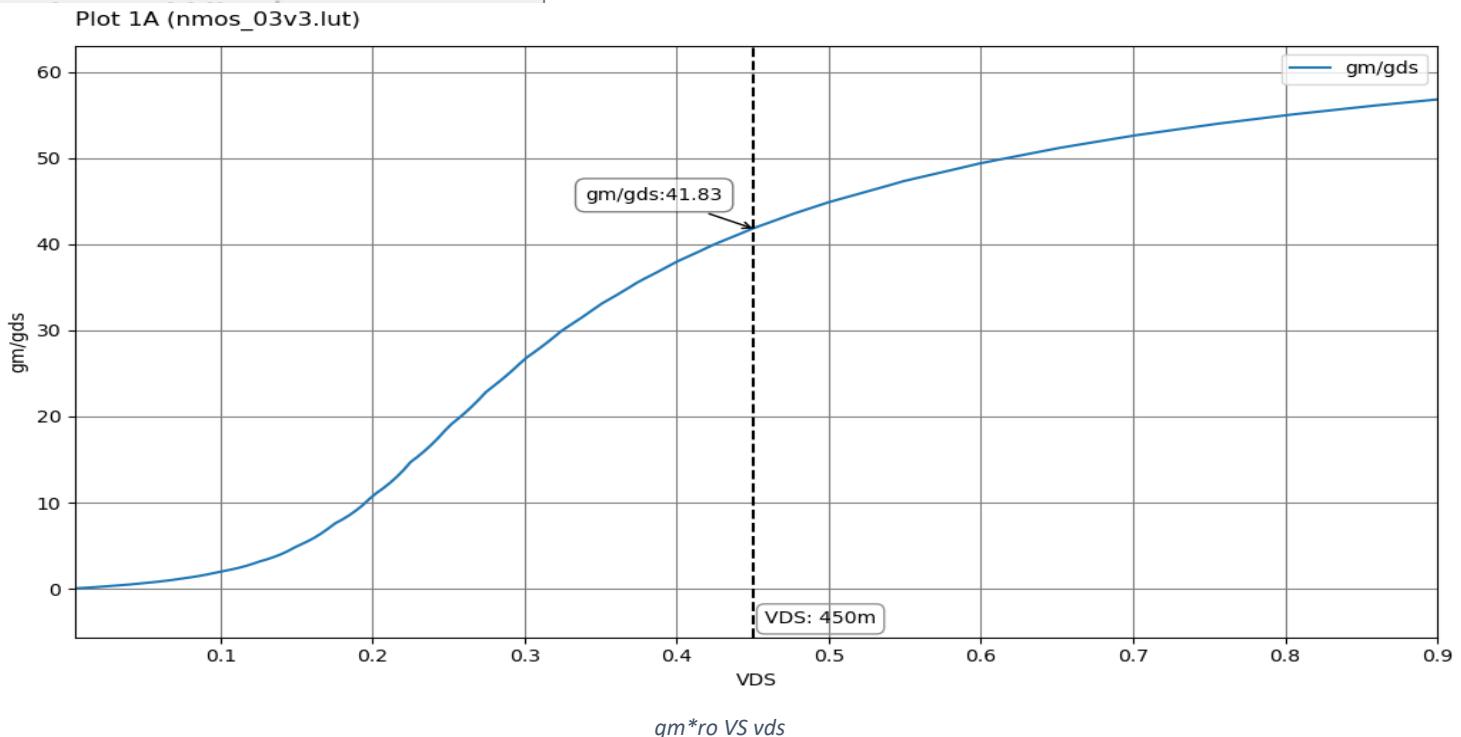
Name	TT-27.0
1 ID	20u
2 IG	N/A
3 L	350n
4 W	3.49u

Y-Expr gm/ID\*fT

Plot

From ADT:

after adjusting values for L & VDS & ID & gm/ID  
we got **W=3.49u**

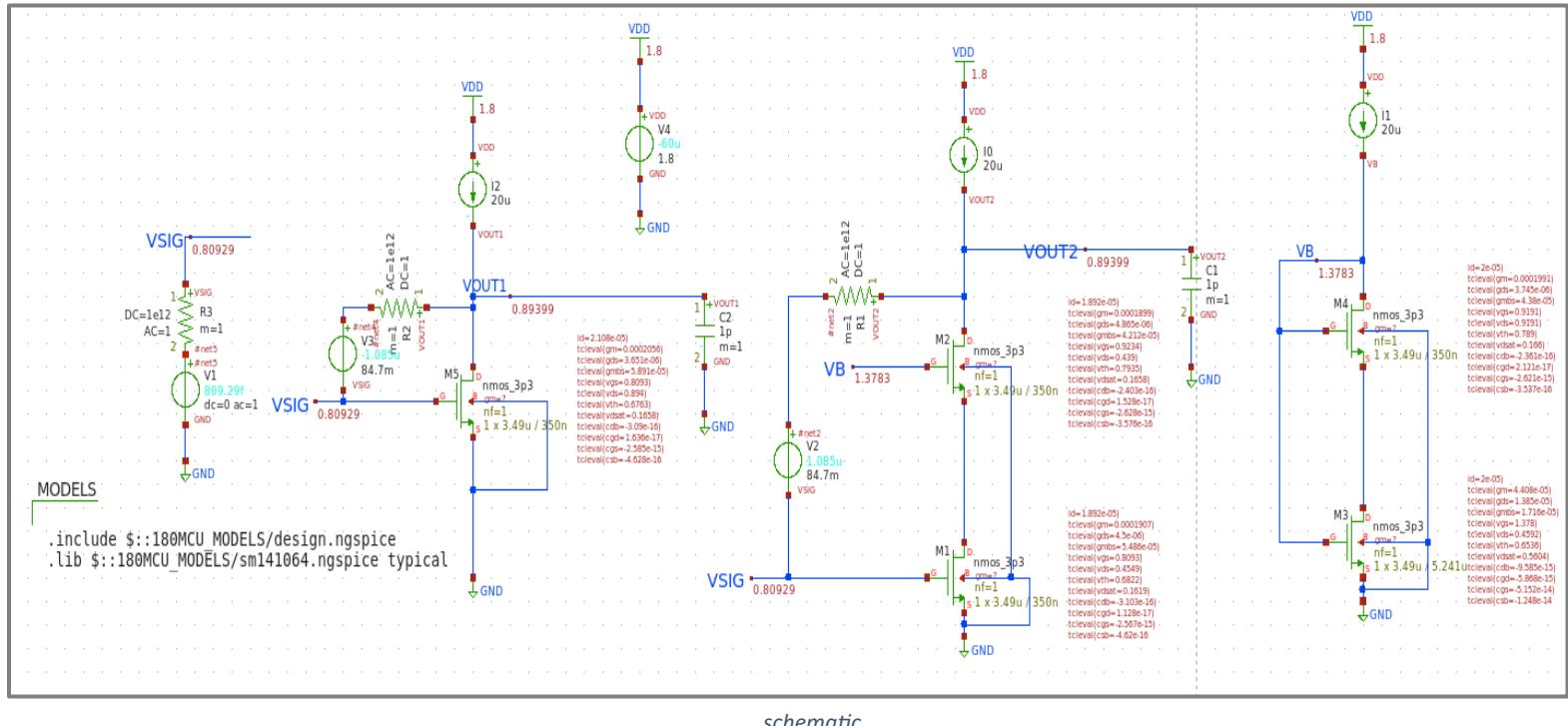


**Comment:**

AS shown  $(\frac{g_m}{g_{ds}})$  depends on VDS , where at  $vds=0.45 \rightarrow (\frac{g_m}{g_{ds}}) = 41.83$

## PART 2: Cascode for Gain :

## 1. OP Analysis :



### *schematic*

ID	<input type="button" value="▼"/>	20u	<input type="button" value="?"/>
W	<input type="button" value="▼"/>	3.49u	<input type="button" value="?"/>
L	<input type="button" value="▼"/>	350n	<input type="button" value="?"/>
VDS	<input type="button" value="▼"/>	0.45	<input type="button" value="?"/>
VSB	<input type="button" value="▼"/>	0	<input type="button" value="?"/>
Stack		<input type="text" value="1"/> <input type="button" value="▼"/>	<input type="button" value="?"/>

Results:

Name		TT-27.0	<input type="button" value="▲"/>
3	L	350n	<input type="button" value="▼"/>
4	W	3.49u	<input type="button" value="▼"/>
5	VGS	815.3m	<input type="button" value="▼"/>

ID	20u	
W	3.49u	
L	350n	
VDS	0.45	
VSB	0.45	
Stack	1	

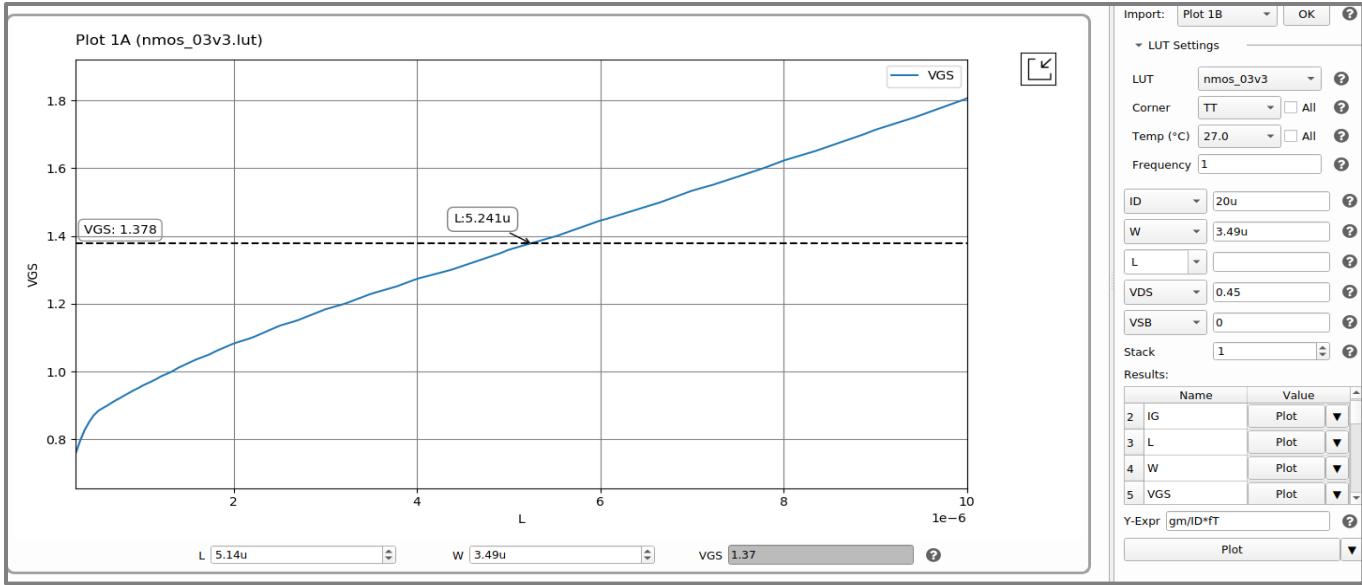
**Results:**

Name	TT-27.0	▼
3 L	350n	
4 W	3.49u	
5 VGS	928m	

To calc value of **dc source** we will clac value of VGS5  
As shown and the source value will=0.9-VGS5=**84.7mV**

To clac value of VB we will clac value of VGS2  
and  $VB = 0.45 + VGS2 = 1.378V$

After that We will use it to calc value of L3



As shown  $L_3=5.241\mu$

10) Check that all transistors operate in saturation. Does any transistor operate in triode? Why?

All transistors operate in saturation except M3, because  $V_{dsat3} > V_{ds3}$

11) Do all transistors have the same  $V_{th}$ ? Why?

Transistors have not the same  $V_{th}$  due to

Body Effect (Substrate Bias)

The threshold voltage depends on the voltage between the source and the substrate (VSB). If the substrate bias varies,  $V_{th}$  will also change

Drain-Induced Barrier Lowering (DIBL)

At higher drain-to-source voltages (VDS), the electric field from the drain can lower the potential barrier in the channel, effectively reducing  $V_{th}$

12) What is the relation ( $\ll, <, =, >, \gg$ ) between  $gm$  and  $gds$ ?

$gm \gg gds$  [M0, M1, M2, M4]

$gm > gds$  [only M3 (TRIODE)]

What is the relation ( $\ll, <, =, >, \gg$ ) between  $gm$  and  $gmb$ ?

$gm > gmb$  [M0, M1, M2, M4]

$gm > gmb$  [only M3 (TRIODE)]

What is the relation ( $\ll, <, =, >, \gg$ ) between  $cgs$  and  $cgd$ ?

$Cgs \gg Cgd$  [M0, M1, M2, M4]

$Cgs > Cgd$  [only M3 (TRIODE)]

What is the relation ( $\ll, <, =, >, \gg$ ) between  $cdb$  and  $csb$ ?

$Csb > Cdb$  [M0, M1, M2, M4]

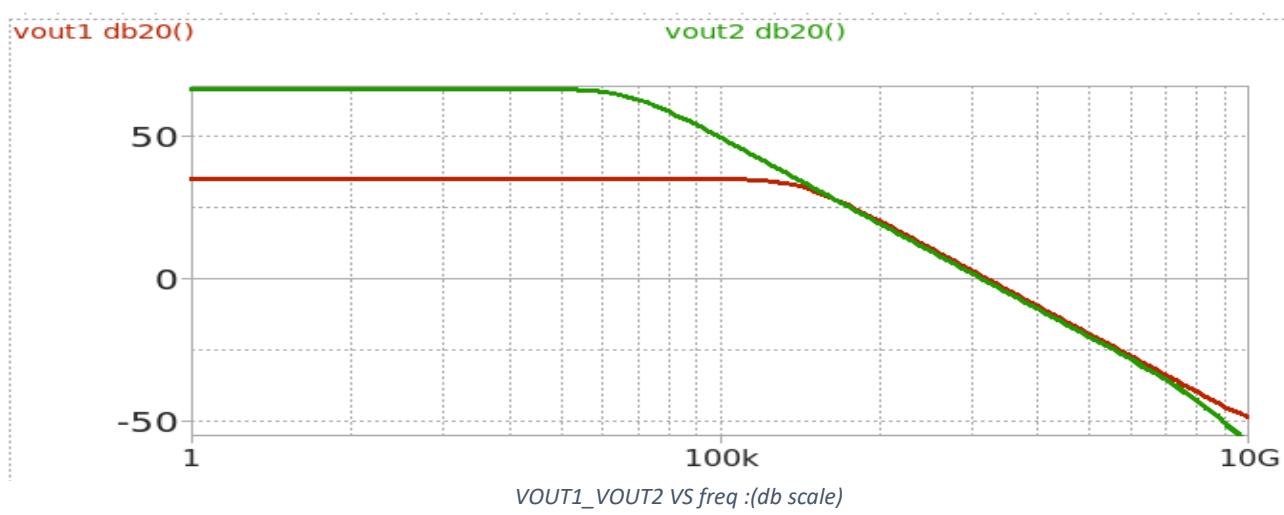
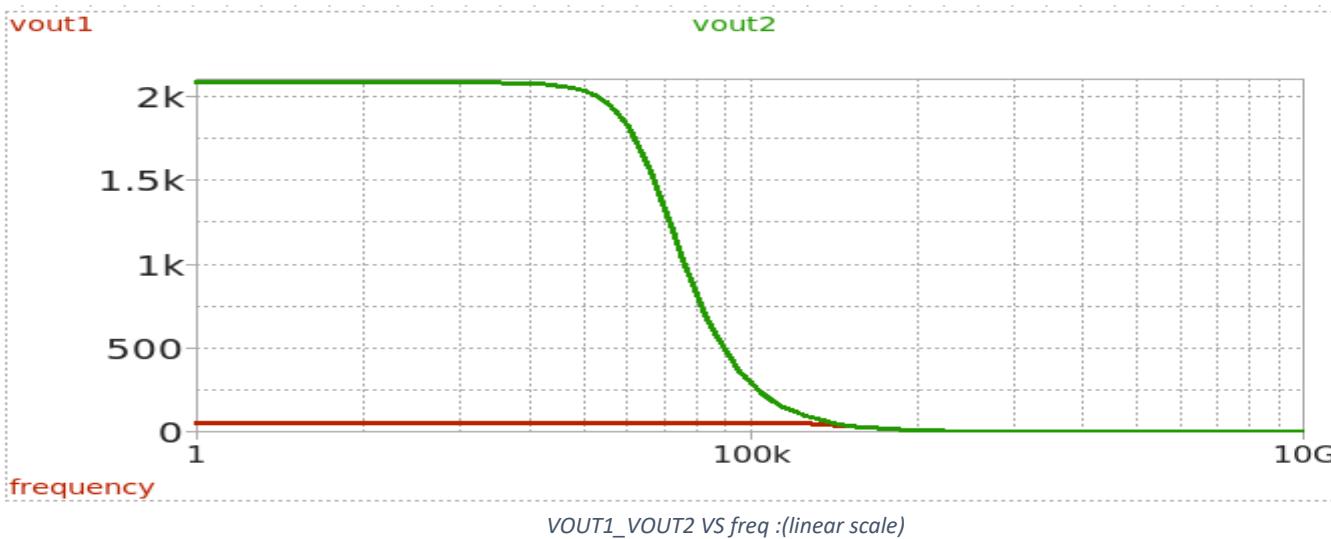
$Csb > Cdb$  [only M3 (TRIODE)]

## 2. AC Analysis

- 2) Use measure expressions to calculate parameters (DC gain, BW, GBW, and UGF) and export them to a text file.

```
1 max_gain_cs = 5.604095e+01
2 bw_cs = 6.017334e+05
3 gbw_cs = 3.372171e+07
4 ugf_cs = 3.401296e+07
5 max_gain_casc = 2.138377e+03
6 bw_casc = 1.340691e+04
7 gbw_casc = 2.866903e+07
8 ugf_casc = 2.894499e+07
```

- 3) Report the Bode plot (magnitude) of CS and cascode appended on the same plot



### Hand analysis:

In a **common source** amplifier , **dc gain** (mag) given by :

$$A_V = gm \times r_o = gm \times \frac{1}{gds} = 56.31$$

But In a **cascode amplifier** , **dc gain** (mag) given by:

$$A_V = gm \times R_{out} = gm_1 \times r_o^2 (gm_2 + gmb_2) r_o^1 \approx 2.066 K$$


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In a **common source** amplifier , **(BW)**given by :

$$\frac{1}{2\pi \times r_{o5} \times C_L} = 593.1 \text{ KHZ}$$

But In a **cascode amplifier** , **(BW)** given by:

$$\frac{1}{2\pi \times R_{out} \times C_L} = 14.21 \text{ KHZ}$$


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**Cs : GBW** nearly = **UGF** =  $BW \times Av = 33.4 \text{ MHZ}$

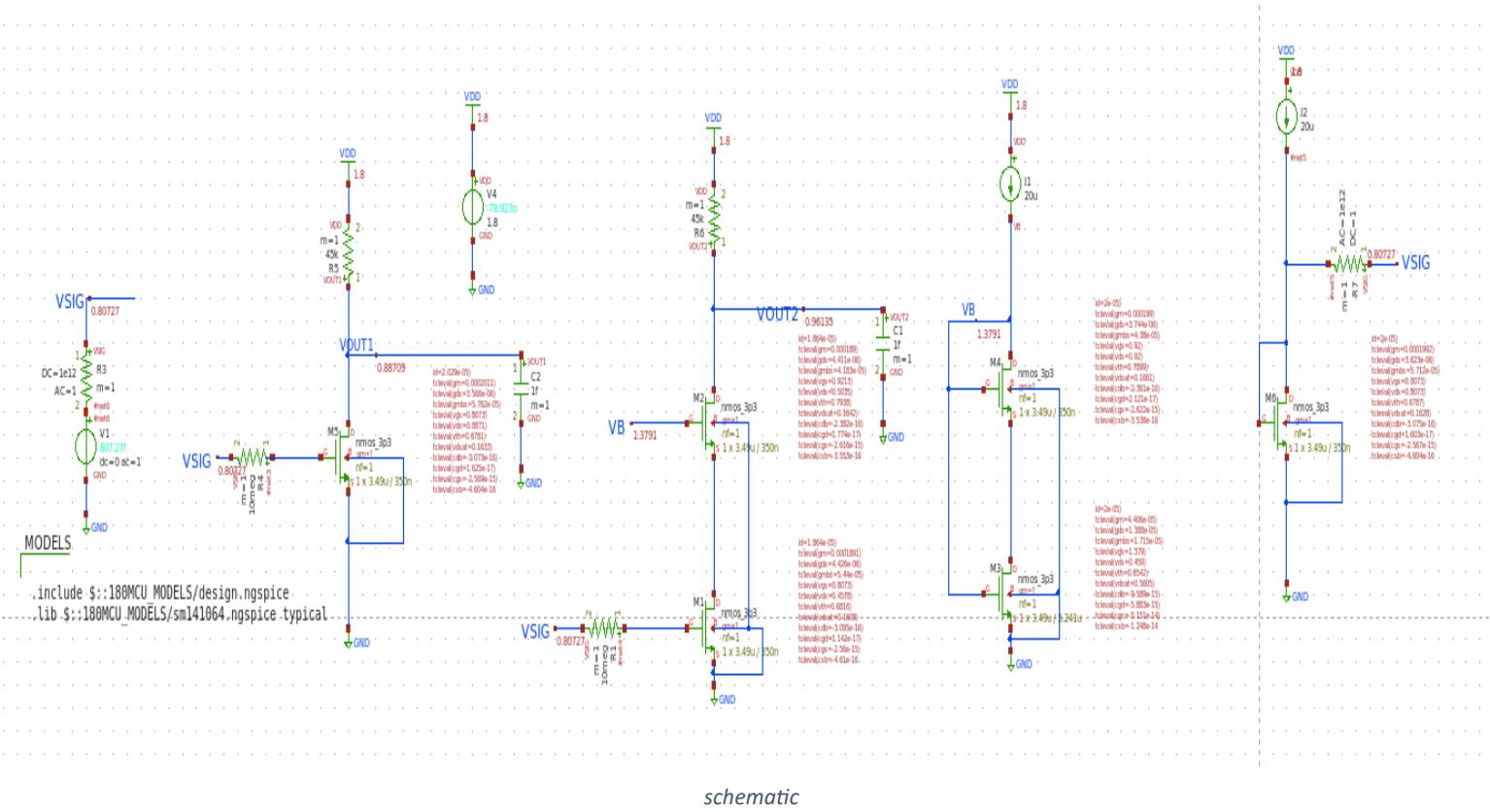
**Cascode : GBW** nearly = **UGF** =  $BW \times Av = 29.4 \text{ MHZ}$

	CS	Cascode
DC gain hand analysis	56.31	2.066 K
DC gain from simulation	56.04	2.138 K
BW hand analysis	593.1 KHZ	14.21 KHZ
BW from simualtion	601.7 KHZ	13.41 KHZ
GBW hand analysis	33.4 MHZ	29.4 MHZ
GBW from simulation	33.7 MHZ	28.7 MHZ
UGF hand analysis	33.4 MHZ	29.4 MHZ
UGF from simualtion	34.01 MHZ	28.94 MHZ

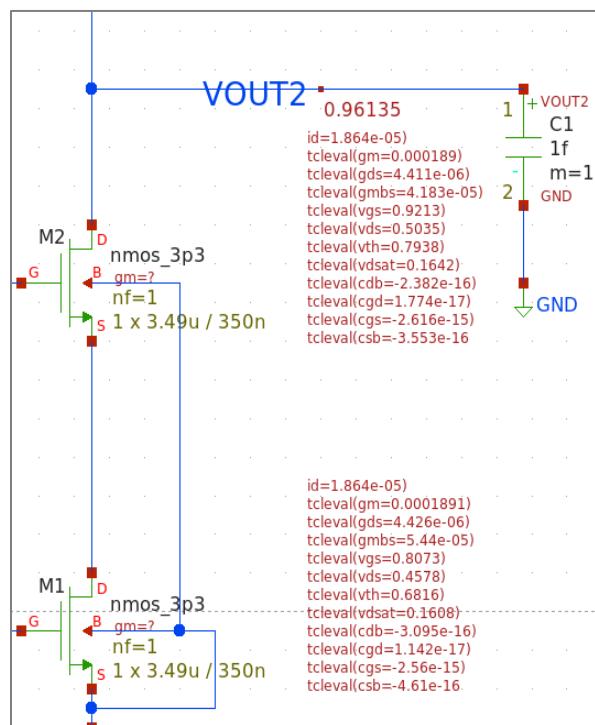
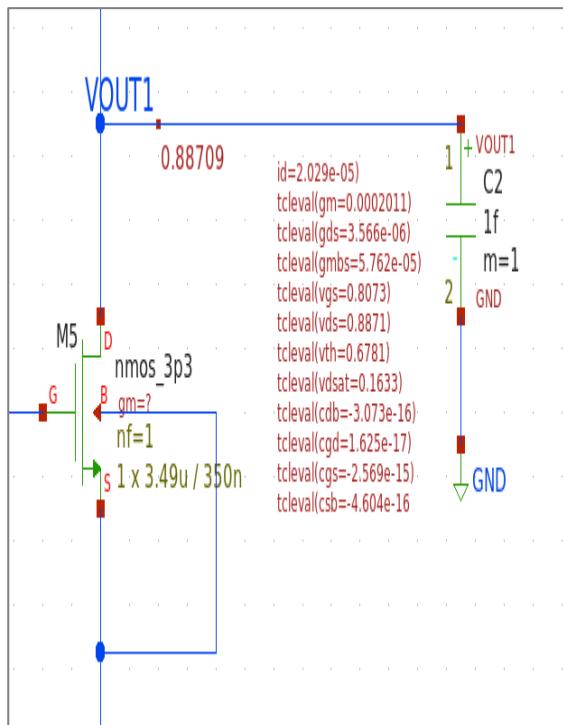
### COMMENT

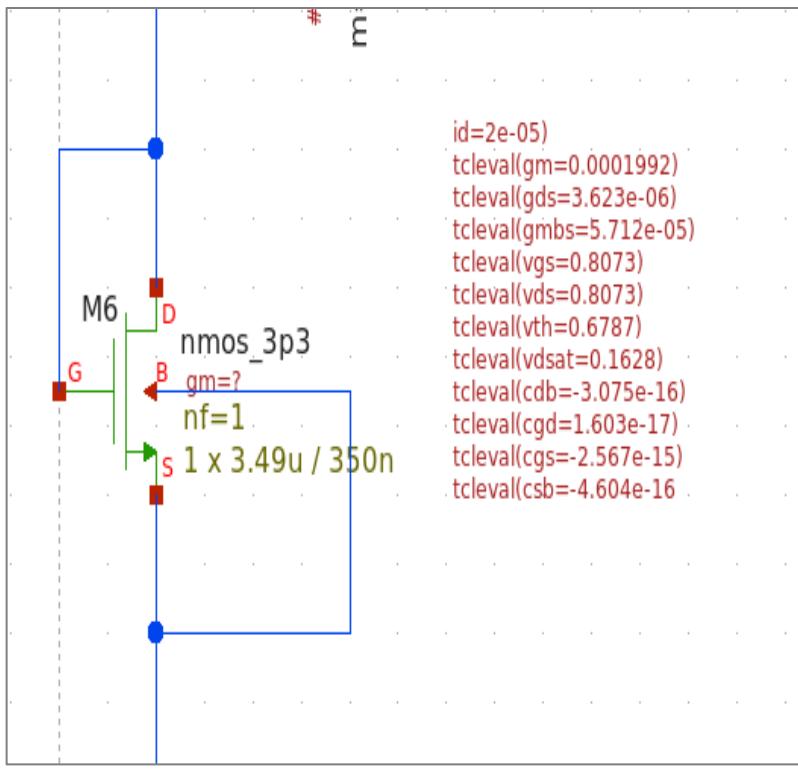
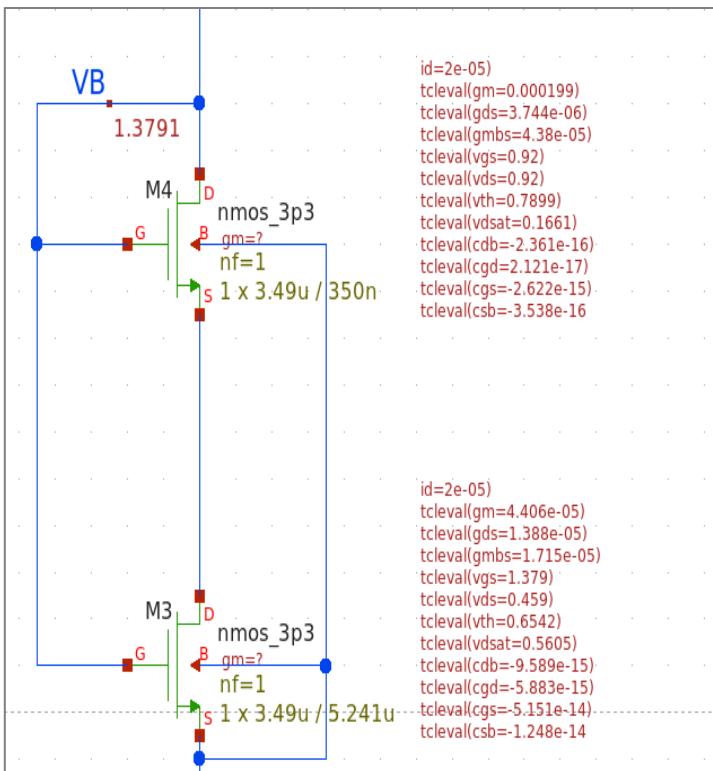
The cascode is preferred when high DC gain is critical, while the CS stage is better for wider bandwidth applications. Their GBW remains similar, demonstrating a fundamental trade-off between gain and speed.

## Part 3: Cascode for BW



schematic





$$R_D \text{ the voltage drop on it is } \approx V_{DD}/2 \longrightarrow R_D = \frac{V_{DD}}{2 \cdot I_D} = 45 \text{ K}\Omega$$

4) Check that all transistors operate in saturation. Does any transistor operate in triode?  
Why?

→ All transistors operate in saturation except M3, because  $V_{dsat3} > V_{ds3}$

caps values:

Info: [Sizing Assistant] The resultant point 'CGS = 3.269f'  
 Info: [Sizing Assistant] The resultant point 'CGD = 614.2a'  
 Info: [Sizing Assistant] The resultant point 'CDB = 2.194f'  
 Info: [Sizing Assistant] The resultant point 'CSB = 3.06f'

Caps values for M5

Info: [Sizing Assistant] The resultant point 'CGS = 3.261f'  
 Info: [Sizing Assistant] The resultant point 'CGD = 682.2a'  
 Info: [Sizing Assistant] The resultant point 'CDB = 2.389f'  
 Info: [Sizing Assistant] The resultant point 'CSB = 3.061f'

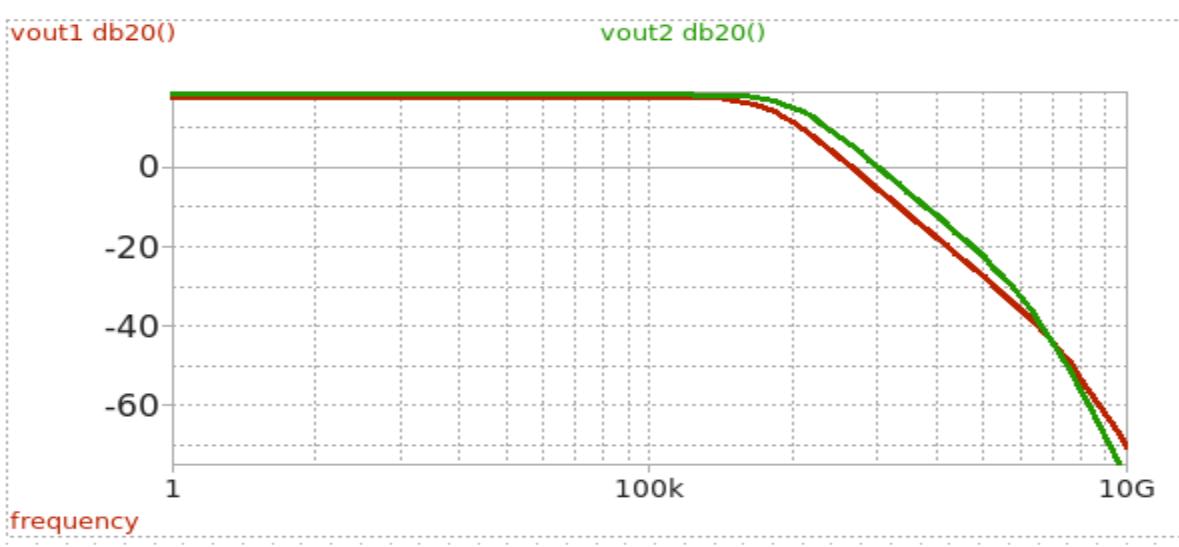
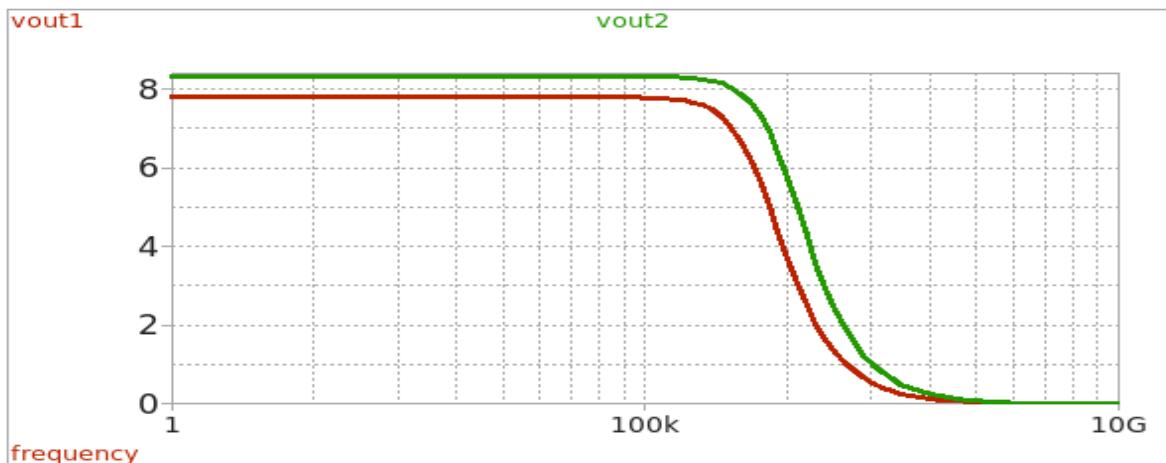
Caps values for M2

## AC Analysis

Use measure expressions to calculate parameters (DC gain, BW, GBW, and UGF) and export them to a text file.

```
1 max_gain_cs = 7.798065e+00
2 bw_cs = 1.700767e+06
3 gbw_cs = 1.326269e+07
4 ugf_cs = 1.321444e+07
5 max_gain_casc = 8.321841e+00
6 bw_casc = 3.016187e+06
7 gbw_casc = 2.510023e+07
8 uaf_casc = 2.489442e+07
```

Report the Bode plot (magnitude) of CS and cascode appended on the same plot



### Hand analysis:

In a **common source** amplifier , **dc gain** (mag) given by :

$$A_V = gm \times r_o // R_d = 7.798$$

But In a **cascode amplifier** , **dc gain** (mag) given by:

$$A_V = gm \times R_{out} = gm_1 \times r_o^2(gm_2 + gmb_2)r_o \approx 8.48$$

In a **common source** amplifier , **(BW)**given by :

$$\frac{1}{2\pi \times R_{in} \times C_{in}} = \frac{1}{2\pi \times R_{sig} \times (C_{gs} + C_{gd}(1+A_V))} = 1835 \text{ KHZ}$$

But In a **cascode amplifier** , **(BW)** given by:

$$\frac{1}{2\pi \times R_{in} \times C_{in}} = \frac{1}{2\pi \times R_{sig} \times (C_{gs} + 2C_{gd}(1+A_V))} = 3441 \text{ KHZ}$$

**Cs : GBW** nearly = UGF = BW × Av= 14.31 MHZ

**Cascode : GBW** nearly = UGF = BW × Av= 29.18 MHZ

	CS	Cascode
DC gain hand analysis	7.798	8.48
DC gain from simulation	7.798	8.322
BW hand analysis	1835 KHZ	3441 KHZ
BW from simualtion	1700 KHZ	3016 KHZ
GBW hand analysis	14.31 MHZ	29.18 MHZ
GBW from simulation	13.26MHZ	25.1 MHZ
UGF hand analysis	14.31 MHZ	29.18 MHZ
UGF from simualtion	13.21MHZ	24.89 MHZ

### COMMENT

The cascode amplifier offers higher bandwidth, slightly higher gain, and nearly double GBW compared to the common source amplifier. This makes it a better choice for high-frequency applications.