

Real-Time and Embedded Systems Design – Lab 6 Report Submission

Team 17

Eman Khaled 18P9713

Omar Hussien 18P1265

Farah Essam 18P3448

Mohamed Mostafa 18P9474

Shehab El Din Adel 18P3863

Video link:

<https://drive.google.com/file/d/1QSkjDwMbQIF801iArG0tzad4wJaD5P51/view?usp=sharing>

```
#include <stdint.h>
#include "tm4c123gh6pm.h"
#include "FreeRTOS.h"
#include "task.h"
#include "timers.h"

#include "queue.h"

# define CLK_FREQ 16000000
#define DELAY_DEBOUNCE CLK_FREQ/1000
#define Get_Bit(Register, Bit) (Register & ( 1 << Bit )) >> Bit

void Delay(int count);

void Init (void);
xTaskHandle uart0task;
void UART0Txchar(char a) ;

static void vSender1Task( void *pvParameters );
static void vSender2Task( void *pvParameters );
static void vReceiverTask( void *pvParameters );

xQueueHandle xQueue;

static char counter = 0;

int main( void )
{
    Init();

    xQueue = xQueueCreate( 5, sizeof( int ) );

    if( xQueue != NULL )
    {
```

```
    xTaskCreate( vSender1Task, (const portCHAR *)"Sender1",
configMINIMAL_STACK_SIZE, NULL, 1, NULL );
    xTaskCreate( vSender2Task, (const portCHAR *)"Sender2",
configMINIMAL_STACK_SIZE, NULL, 1, NULL );
    xTaskCreate( vReceiverTask, (const portCHAR *)"Receiver",
configMINIMAL_STACK_SIZE, NULL, 2, &uart0task );

    vTaskStartScheduler();
}
else
{
}

for( ;; );
}

/*-----*/

static void vSender1Task( void *pvParameters )
{

    for( ;; )
    {

        if((( GPIO_PORTF_DATA_R >>4) &1)==0)
        {
            do
            {
                Delay(20);
            }
            while((( GPIO_PORTF_DATA_R >>4) &1)==1);
            counter++;
            while((( GPIO_PORTF_DATA_R >>4) &1)==0);
            do
            {
                Delay(20);
            }
            while((( GPIO_PORTF_DATA_R >>4) &1)==0);
            taskYIELD();
        }

    }

}
```

```
static void vSender2Task( void *pvParameters )
{
    portBASE_TYPE xStatus;
    unsigned portBASE_TYPE uxPriority;
    uxPriority=uxTaskPriorityGet(NULL);

    for( ;; )
    {
        if((( GPIO_PORTF_DATA_R >>0) &1)==0)
        {
            do
            {
                Delay(20);
            }
            while((( GPIO_PORTF_DATA_R >>0) &1)==1);
            vTaskPrioritySet(uxTask, (uxPriority-1));

            xStatus = xQueueSendToBack( xQueue, &counter, 0 );
            counter=0;
            vTaskPrioritySet(uxTask, (uxPriority+1));
            while((( GPIO_PORTF_DATA_R >>0) &1)==0);
            do
            {
                Delay(20);
            }
            while((( GPIO_PORTF_DATA_R >>0) &1)==0);
            taskYIELD();
        }
    }
}

/*-----*/

static void vReceiverTask( void *pvParameters )
{
    char lReceivedValue=0;
    portBASE_TYPE xStatus;
    const portTickType xTicksToWait = 100000 / portTICK_RATE_MS;
```

```
for( ;; )
{

    xStatus = xQueueReceive( xQueue, &lReceivedValue, xTicksToWait );
    UART0Txchar(lReceivedValue);

}
}

void Init(void){

    SYSCTL_RCGCGPIO_R |= 0X20;
    GPIO_PORTF_DIR_R |= 0X0E;

    GPIO_PORTF_LOCK_R = 0x4C4F434B;
    GPIO_PORTF_CR_R = 0x1F;
    GPIO_PORTF_PUR_R = 0x11;
    GPIO_PORTF_DEN_R = 0x1F;

    SYSCTL_RCGCUART_R |= 0X01;
    SYSCTL_RCGCGPIO_R |= 0X01;
    UART0_CTL_R = 0;          /* UART0 module disable */
    UART0_IBRD_R = 65;        /* for 9600 baud rate, integer = 104 */
    UART0_FBRD_R = 7;         /* for 9600 baud rate, fractional = 11 */
    UART0_CC_R = 0;           /*select system clock*/
    UART0_LCRH_R = 0x60;       /* data length 8-bit, not parity bit, no FIFO */
    UART0_CTL_R = 0x301;       /* Enable UART5 module, Rx and Tx */

    GPIO_PORTA_AFSEL_R = (1<<1)|(1<<0);
    GPIO_PORTA_PCTL_R = (1<<0)|(1<<4);
    GPIO_PORTA_DEN_R = (1<<0)|(1<<1);

}

void UART0Txchar(char a)
{
    while((UART0_FR_R & (1<<5)) != 0);
    UART0_DR_R = a;
    if (a==1)
    {
        GPIO_PORTF_DATA_R ^=0x3;
    }
}
```

```
    }  
}  
  
void Delay(int count)  
{  
    int i,j;  
  
    for(i = 0; i<count; i++)  
        for(j = 0; j<3180; j++);  
}
```