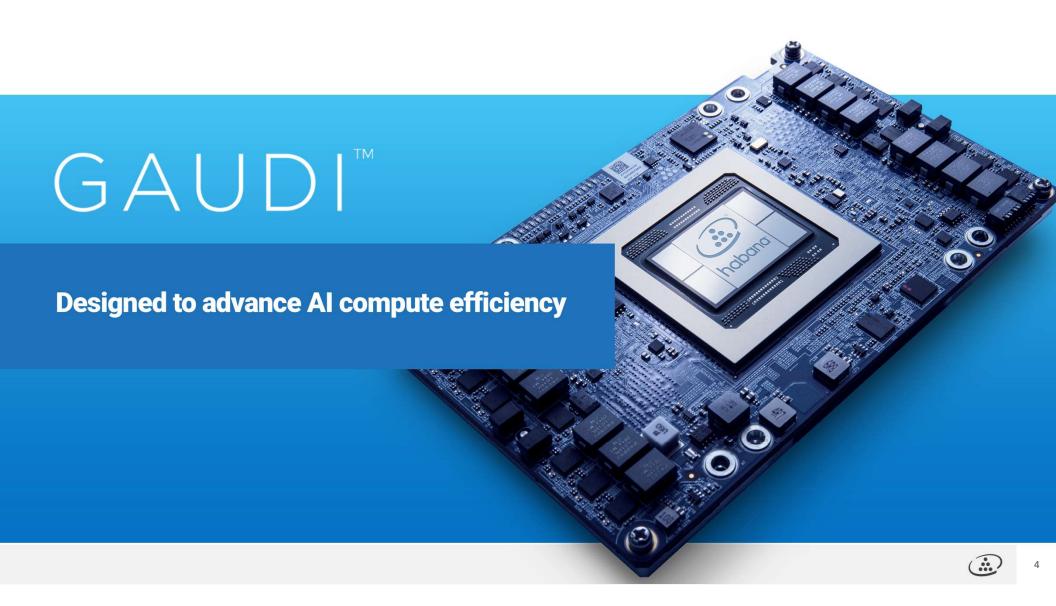


## Explosive demand for Deep Learning Training



- More complex models
- Many Iterations
  - 74% of IDC respondents indicate running
     5 10 iterations of training
  - >50% of respondents rebuild models weekly or more often; 26% rebuilding daily or hourly

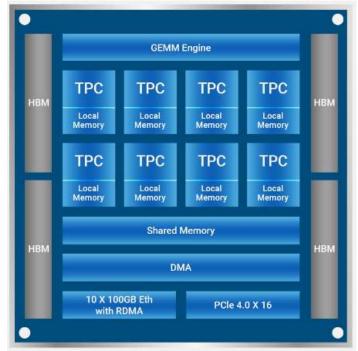




## Gaudi: architected for efficiency

Designed to optimize AI performance, delivering higher efficiency than traditional CPUs & GPUs

- Heterogeneous compute architecture
  - Configurable centralized GEMM engine (MME)
  - Fully programmable, Al-customized Tensor Processing Cores
- Software-managed memory architecture
- 32 GB of HBM2 memory
- Natively integrated 10 x 100Gb Ethernet RoCE for scaling

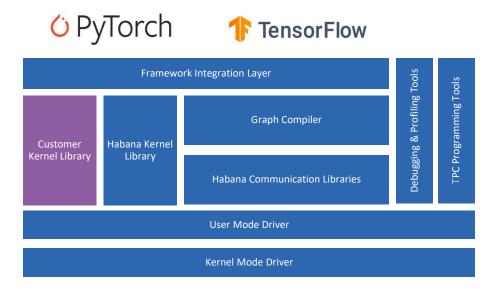




# SynapseAI® Software Suite: designed for performance and ease of use

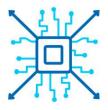
# Driving end-user efficiency for model build and migration

- Train deep learning models on Gaudi with minimal code changes
- Integrated with TensorFlow & PyTorch
- Habana Developer Site & GitHub
- Support with reference models, kernel libraries, documentation and "how tos"
- Advanced users can write their own custom kernels

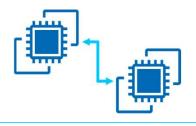




# DL1 instances powered by Gaudi processors features



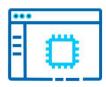
AWS Custom
2<sup>nd</sup> Gen Xeon Scalable Processors



All-to-all 100Gbps interconnect



Up to 8 Habana Gaudi accelerators with 32GB HBM per processor



SynapseAI SDK integrated with TensorFlow and PyTorch



400Gbps Networking & 4TB of NVMe Storage



Support for developing custom kernels



#### Use cases



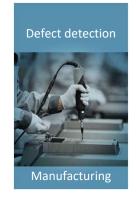
Object Detection & Segmentation



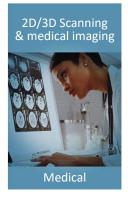
**Image Classification** 

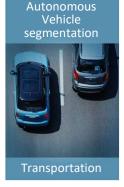


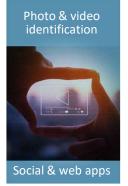


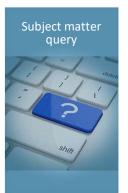
















#### Visualize Performance and Build Custom Kernels



#### Habana Profiling Tools

Monitor core utilization, enabling performance analysis and optimization



#### Habana Kernel Library

Rich TPC kernel library
with support for wide variety
of operators such
as non-linear, elementwise,
non-GEMM



# TPC Programming Tools

Build custom kernels using LLVM-based TPC-C compiler, simulator, and debugger



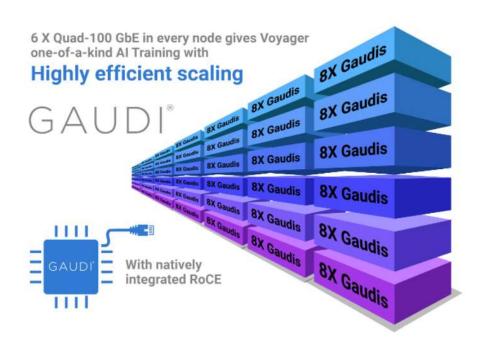
#### Habana Communication Libraries

Scale up to multiple
Gaudi cards within a
node or scale out across
nodes for distributed
training



### Gaudi is also driving efficiencies in HPC

SDSC Voyager Supercomputer powered by 336 Gaudi training processors



Voyager goes into service this fall

Supermicro X12 8-Gaudi Server powering Voyager

Funded by the National Science Foundation

Al research conducted across range of science and engineering domains

