

Computer Organization 2023 Lab 4

Finished part:

The modules I finished are 'ALU_Ctrl.v', 'ALU.v', 'Decoder.v', and 'Simple_Single_CPU.v', and the testcase I passed is 'CO_P4_test_data1.txt'.

Problems you met and solutions:

Most of the problem I met are minor logical errors, and all are solved by inspecting my code and the architecture diagram.

If you create additional module, please give a short explanation here:

I create two additional module, 'ALU_1bit.v' and 'Full_adder.v', both modules are used in 'ALU.v'. These two modules are the copy of lab3 modules. The function is same as mentioned in the textbook.