

Physics-Based Analytical Modeling of Electromigration Reliability for Multi-Segment Interconnect Wires

Abstract—Electromigration (EM) is the major concern for the VLSI back end of the line (BEOL) reliability. For EM modeling and assessment, one important problem is to perform fast EM time to failure analysis for practical interconnect layouts such as clock and power/ground networks consisting of many multi-segment wires. However, existing EM modeling and analysis techniques are mainly developed for a single wire. Although there are some early effects to resolve this issue, but there is no general analytic and compact model developed to estimate the EM-induced stress evolution, which is required to predict the time to failure. In this paper, we propose a new analytic expression hydrostatic stress calculation for general multi-segment interconnect wire with multiple terminals. The new method is based on the Laplace transformation method on the Korhonen's equation with blocked atom flux boundary conditions. The analytical solutions in terms of a set of auxiliary basis functions using the complementary error function agree well with the numerical analysis results. Our analysis further demonstrates that using the first one dominant basis functions can lead to 2% error, which is sufficient for practical EM analysis.

I. INTRODUCTION

Electromigration is considered the top killer for today's copper dual damascene interconnect technology. Recently there is a renewed interests in EM modeling and mitigation techniques as EM induced reliability is getting worse as technology advance. ITRS predicts that the EM induced life time of the interconnects will be reduced by half by each generation [1]. To make this situation more worse, many widely used EM models such as Black-Blech models are thought to be too conservative, which can lead to significant over designs [2].

Existing EM model and analysis techniques mainly focus on the simple straight line interconnect with two line end terminals [3]. Existing widely used EM models such as Blech limit [4] (for the out filtration of immortal segments) and Black's equation [5] (for calculating MTTFs for segments characterized by known current densities and temperatures) are subjects of the hard criticism [6] [7] [8] as those empirical models are stressing condition dependent and lacks of scalability over wide current and temperature stressing conditions. More importantly, those models can only applied to simple wire structures. For practical VLSI chips, the interconnects such as clock and power grid networks typically consist of multi-branch metal segments representing a continuously connected, highly conductive metal (Cu) lines within one layer of metallization, terminating at diffusion barriers. The EM effects in those branches are not independent and they have to be considered simultaneously.

Recently some physics-based EM analysis methods for the TSV and power grid networks have been proposed based on solving the basic mass transport equations [9]–[12]. Those models treat the resistance changes of a wire over time as the atomic concentration changes due to atomic flux. Since these proposed methods solve the basic mass transport equations using the finite element method, they can only solve for very small structures such as one TSV structure. Complicated look-up table or models have to be built for different TSVs and wire segments for full-chip power grid analysis at reduced accuracy.

To mitigate this problem, a more compact physics-based EM model was proposed recently in [13]. It is based on the hydrostatic stress diffusion equation [14]. However, this EM model still works for one single wire segment. Although the new EM model has been extended to deal with multiple branch tree wire based on projected steady-state stress. It still can't provide the time-dependent hydrostatic evolution of hydrostatic stress, which ultimately determines the failures for multi-branch interconnect wires. Recently EM models for multi-branch interconnect tree have been proposed [15], [16]. In this approach, analytic solutions for stress evolution were derived for a few specific interconnect structures. However, those analytic expressions are still can be applied to more general structures such as the multi-segment interconnect wires as showed Fig. ??, which are commonly seen in the power/ground network in practical VLSI layouts.

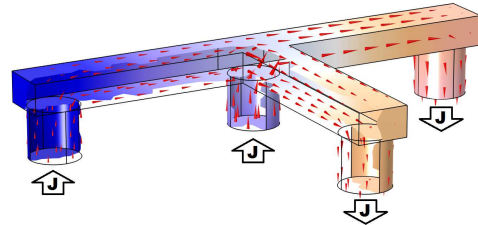


Fig. 1. Electrical current in a 3-terminal wire structure (J: current direction)

In this paper, we aim to resolve the mentioned problems for EM model. We propose a new analytic expression hydrostatic stress calculation for general multi-segment interconnect wire with multiple terminals. The new method is based on the Laplace transformation method on the Korhonen's equation with blocked atom flux boundary conditions. The analytical solutions in terms of a set of auxiliary basis functions using the complementary error function agree well with the numerical

analysis results. Our analysis further demonstrates that using the first two dominant basis functions can lead to xx% error, which is sufficient for practical EM analysis.

The remainder of the paper is organized as follows. In Section II, we induce the physics-based EM models and analysis methods for the stress evolution during EM in simple interconnect wires. Then we extend this analytic model to deal with a general multi-segment interconnect wire case in Section III. Experiment results and discussions for the stress evolution modeling multi-segment (three-, five-, six-terminal) interconnect wires are presented in Section IV. Concluding remarks are drawn in Section V.

II. REVIEW OF PHYSICS-BASED EM MODELLING AND ANALYSIS METHODS

EM is a physical phenomenon of the migration of metal atoms along a direction of the applied electrical field. This oriented atomic flow, which is caused mostly by the momentum exchange between atoms and the conducting electrons, results in metal density depletion at the cathode, and a corresponding metal accumulation at the anode end of the metal wire. Due to the passivation of copper wires from the dual damascene copper interconnect technology, hydrostatic stress will built up from the metal concentration changes at those nodes, which will lead to void nucleation when the stress reach to the critical stress and following up failures of the wires as results of the void growth.

EM phenomena and EM modeling have been intensively studied and some important physics-based EM models have been proposed in the past two decades [3]. Korhonen proposed the important EM model to describe the EM-induced stress evolution of passivated interconnect wires and its analytic solution for a simple wire structure in 1993 [14], which was further developed by many researchers [17], [18]. Specifically, for a confined/passivated metal line, the hydrostatic stress distribution $\sigma(x, t)$ along the metal wire can be described by a one-dimensional diffusion-like equation, given by: [14]

$$\frac{\partial \sigma(x, t)}{\partial t} = \frac{\partial}{\partial x} \left[\kappa \left(\frac{\partial \sigma(x, t)}{\partial x} + G \right) \right] \quad (1)$$

where $\kappa = \frac{D_a B \Omega}{kT}$ is the stress diffusivity affected by temperature T and $G = \frac{Z^* e \rho j}{\Omega}$ is the EM driving force. Moreover, $D_a = D_0 e^{-\frac{E_a}{kT}}$ is the effective atomic diffusion coefficient. D_0 and E_a stand for the pre-exponential factor and the activation energy, respectively. The Korhonen equation (1) has differen analytical solutions with different boundary conditions (BCs). Following the idea in [14], we assume that the stress diffusivity κ does not depend on time. During the void nucleation phase of EM, the atom fluxes are blocked at both wire ends $x = 0$ and $x = L$ at any moment in time. The initial stress value during the void nucleation phase is set to be zero. Thus, the closed-form solution of the stress evolution

equation for a single segment wire can be given as follows [14]:

$$\sigma(x, t) = \sigma_T + GL \left\{ \frac{1}{2} - \frac{x}{L} - 4 \sum_{n=0}^{\infty} \frac{\cos((2n+1)\pi \frac{x}{L})}{(2n+1)^2 \pi^2 \exp((2n+1)^2 \pi^2 \frac{\kappa t}{L^2})} \right\}. \quad (2)$$

where σ_T is the pre-existing residual stress due to thermal process. If we only keep the slowest decaying term of the infinite series in (2), we can compute the time to nucleation. This happens when the wire stress $\sigma(x, t)$ sigma has reached a critical state stress value σ_{crit} , then the void nucleation time t_{nuc} can be obtained by solving the equation $\sigma(x, t_{nuc}) = \sigma_{crit}$ [13].

The physics-based EM model proposed in [13], [19] was extended further to deal with multi-branch interconnects, in which the projected steady-state EM-induced stress of each branch induced by is used to compute the total stress distributions in a tree and then the relative branch stress is used to calculate the time to failure. The drawback of this work is that it totally ignore all the dynamic evolutions of stress over time and the interactions of void nucleation process among branches. As a result, it still can't provide analytic expression for provide the time-dependent hydrostatic evolution of hydrostatic stress to determine the failures for multi-branch interconnect wires.

Following another direction, a voltage-based EM modeling and immortality check technique for general interconnect tree structures has been proposed recently [20]. In this work, the projected steady-state stresses for each branch or segment also calculated and then the steady state stress at every node of the tree are then computed in terms of voltage voltages and wire layout parameters. By checking the largest stress over all the tree, one can ensure that the tree never fail if largest stress is less than the critical stress. But this method still can't give the time to failure the wire fail the immortality check. A good research effort was carried out recently to develop stress evolution analytic solution for multi-branch interconnect tree [15], [16]. In this approach, analytic solutions by means of Laplace transformation method for stress evolution were dervied for three specific interconnect strucutres: the straight-line 3-terminal wires, the T-shape 4-terminal wires and the cross-shape 5-terminal wires. It was also shown that a few dominant terms can be used for sufficiently accurate solution.

III. ACCURATE ANALYTICAL MODELING FOR HYDROSTATIC STRESS EVOLUTION

In this paper, an accurate analytical modeling for transient hydrostatic stress analysis for a general realistic interconnect structure as shown in Fig.2, which represents a long metal wire with n segments and $n+1$ terminal (vias) connecting different metal layers. Since many complex interconnect structures such as power grids could be decomposed to hierarchically in smaller interconnect tree components like this structure, the accurate analytic closed-form expressions describing hydrostatic stress evolution for multi-branch interconnect trees

will lay the groundwork for the EM reliability analysis of those global interconnect networks.

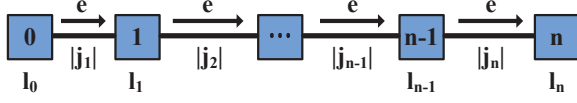


Fig. 2. An example of general realistic interconnect structure.

The stress evolution process for each segment in Fig.2 can be described by a single Korhonen's equation as shown in (1). But solving such equation directly is more difficult. In stead, in our approach, we decouple the original single Korhonen's equation into n decoupled Korhonen equations and each of them governing one segment of the multi-segment wire. The partial differential equations (PDE) are linked by the boundary conditions representing the continuity of stress and fluxes at the junctions in the long metal wire. As a result, the PED in (1) can be described as follows:

$$\begin{aligned} \frac{\partial \sigma_1}{\partial t} &= \frac{\partial}{\partial x} [\kappa_1 (\frac{\partial \sigma_1}{\partial x} + G_1)], \quad l_0 \leq x \leq l_1 \\ \frac{\partial \sigma_2}{\partial t} &= \frac{\partial}{\partial x} [\kappa_2 (\frac{\partial \sigma_2}{\partial x} + G_2)], \quad l_1 \leq x \leq l_2, \\ &\dots\dots\dots \\ \frac{\partial \sigma_{n-1}}{\partial t} &= \frac{\partial}{\partial x} [\kappa_{n-1} (\frac{\partial \sigma_{n-1}}{\partial x} + G_{n-1})], \quad l_{n-2} \leq x \leq l_{n-1}, \\ \frac{\partial \sigma_n}{\partial t} &= \frac{\partial}{\partial x} [\kappa_n (\frac{\partial \sigma_n}{\partial x} + G_n)], \quad l_{n-1} \leq x \leq l_n. \end{aligned} \quad (3)$$

In (3), κ_i and G_i ($i = 1, 2, \dots, n$) are the stress diffusivity and the EM driving force for each segment wire. Boundary conditions for these equations are given as the follows:

$$\begin{aligned} \kappa_1 (\frac{\partial \sigma_1}{\partial x} + G_1) &= 0, \quad x = l_0, \\ \sigma_1 &= \sigma_2, \quad x = l_1, \\ \kappa_1 (\frac{\partial \sigma_1}{\partial x} + G_1) &= \kappa_2 (\frac{\partial \sigma_2}{\partial x} + G_2), \quad x = l_1, \\ \sigma_2 &= \sigma_3, \quad x = l_2, \\ \kappa_2 (\frac{\partial \sigma_2}{\partial x} + G_2) &= \kappa_3 (\frac{\partial \sigma_3}{\partial x} + G_3), \quad x = l_2, \\ &\dots\dots\dots \\ \kappa_n (\frac{\partial \sigma_n}{\partial x} + G_n) &= 0, \quad x = l_n. \end{aligned} \quad (4)$$

Initial conditions for the void nucleation phase are given by $\sigma_i = 0$ at $t = 0$ at each segment.

To solve the resulting decoupled equations in (3) subject to the boundary conditions (4), the Laplace transformation technique is used to convert the PDE into Laplace domain. For the sake of simplicity, we assume that $\kappa_1 = \kappa_2 = \dots = \kappa_k$. After transforming (3)-(4) by the Laplace transformation technique, we get a system of ordinary differential equations

(ODEs). In order to derive the solution of each ODE, we first need to introduce the following notations:

$$\begin{aligned} \xi_{n,1}^i(m, x) &= l_n + 2m(l_n - l_0) - x, \\ \xi_{n,2}^i(m, x) &= (2l_n - l_0) + 2m(l_n - l_0) - x, \\ \xi_{n,2j+1}^i(m, x) &= (2l_n - l_j) + 2m(l_n - l_0) - x, \\ \xi_{n,2j+2}^i(m, x) &= \begin{cases} (2l_n - 2l_0 + l_j) + 2m(l_n - l_0) - x, \\ 0 < j < i, \\ l_j + 2m(l_n - l_0) - x, \quad i \leq j \leq n-1, \end{cases} \\ \eta_{n,1}^i(m, x) &= (l_n - 2l_0) + 2m(l_n - l_0) + x, \\ \eta_{n,2}^i(m, x) &= -l_0 + 2m(l_n - l_0) + x, \\ \eta_{n,2j+1}^i(m, x) &= (l_j - 2l_0) + 2m(l_n - l_0) + x, \\ \eta_{n,2j+2}^i(m, x) &= \begin{cases} -l_j + 2m(l_n - l_0) + x, \quad 0 < j < i, \\ (2l_n - l_j - 2l_0) + 2m(l_n - l_0) + x, \\ i \leq j \leq n-1. \end{cases} \end{aligned} \quad (5)$$

where n is the number of segments, i is the index of current segment, and j indicates (?)

Similar to the analytical method introduced in [15], we first introduce the following basic function:

$$g(x, t) = 2\sqrt{\frac{\kappa t}{\pi}} e^{-\frac{x^2}{4\kappa t}} - x \times \text{erfc}\left\{\frac{x}{2\sqrt{\kappa t}}\right\}. \quad (6)$$

where the complementary error function $\text{erfc}\{x\}$ is defined as $\text{erfc}\{x\} = \frac{2}{\sqrt{\pi}} \int_x^{+\infty} e^{-t^2} dt$.

As a result, we show that a general form of analytical solutions of stress evolution equations for all segments can be written as follows

$$\begin{aligned} \sigma_{n,i}(x, t) &= - \sum_{m=0}^{+\infty} \{G_n g(\xi_{n,1}^i, t) - G_1 g(\xi_{n,2}^i, t) \\ &\quad - \sum_{j=1}^{n-1} \frac{G_{j+1} - G_j}{2} (g(\xi_{n,2j+1}^i, t) - g(\xi_{n,2j+2}^i, t))\} \\ &\quad - \sum_{m=0}^{+\infty} \{G_n g(\eta_{n,1}^i, t) - G_1 g(\eta_{n,2}^i, t) \\ &\quad - \sum_{j=1}^{n-1} \frac{G_{j+1} - G_j}{2} (g(\eta_{n,2j+1}^i, t) - g(\eta_{n,2j+2}^i, t))\}. \end{aligned} \quad (7)$$

Since the solutions contain infinite number of terms in (7), we are more interested in a solution with a few dominant terms for fast computation. The approximate solution using the first dominant term for each segment in the interconnect wire can be written as

$$\begin{aligned} \sigma_{n,i}(x, t) &\approx - \{G_n g(\xi_{n,1}^i(0, x), t) - G_1 g(\xi_{n,2}^i(0, x), t) \\ &\quad - \sum_{j=1}^{n-1} \frac{G_{j+1} - G_j}{2} (g(\xi_{n,2j+1}^i(0, x), t) - g(\xi_{n,2j+2}^i(0, x), t))\} \\ &\quad - \{G_n g(\eta_{n,1}^i(0, x), t) - G_1 g(\eta_{n,2}^i(0, x), t) \\ &\quad - \sum_{j=1}^{n-1} \frac{G_{j+1} - G_j}{2} (g(\eta_{n,2j+1}^i(0, x), t) - g(\eta_{n,2j+2}^i(0, x), t))\}. \end{aligned} \quad (8)$$

IV. EXPERIMENTAL RESULTS AND DISCUSSIONS

In order to show the accuracy of the proposed model of EM reliability for multi-branch interconnect three with line structure shown in Fig. 2, the analytical solution (7) of stress evolution equations is calculated with the MATLAB environment, and we also compare the results from (7) with those obtained by the finite element tool COMSOL [?]. In our experiments, we test three cases of interconnect tree structure by changing n in (7) which is the number of wire segments in the interconnect tree. In the simulations to be described below, the following parameter values will be used: $Z^* = 10$, $\rho = 3 \times 10^{-8} \Omega/m$, $\Omega = 8.78 \times 10^{-30} m^3$, $B = 5.5 \times 10^{10} Pa$, $D_0 = 5.5 \times 10^{-5} m^2/s$, $E_a = 1.1 eV$, $e = 1.6 \times 10^{-19} C$, $k = 1.38 \times 10^{-23} J/K$, and $T = 350 K$.

A. Four-terminal interconnect wire ($n = 3$)

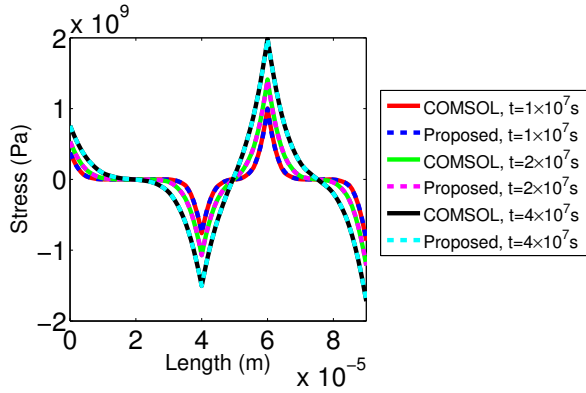


Fig. 3. EM-induced stress evolution along the four-terminal interconnect wire ($n = 3$).

We first analyze the four-terminal interconnect tree with three wire segments with the current flow directions as shown in Fig. 2. For this type of interconnect wire, the integer n in the stress evolution equation (3) is set to be 3. For our simulation, a constant current density of $j_1 = 2.2 \times 10^{10} A/m^2$ is applied in the left segment, while a current density with varying direction and magnitude, $j_2 = -6.6 \times 10^{10} A/m^2$ is used to stress the middle wire segment. The current density of the right wire segment is set to be $j_3 = 5 \times 10^{10} A/m^2$. The lengths for this four-terminal interconnect wire are set to be $l_0 = 0 \times 10^{-5} m$, $l_1 = 4 \times 10^{-5} m$, $l_2 = 6 \times 10^{-5} m$, and $l_3 = 9 \times 10^{-5} m$, respectively. Fig. 3 shows the EM stress development calculated by employing the one-term approximation form of the exact series solution (7) for the three-segment interconnect wire, in comparison with the finite element analysis results obtained from COMSOL. It can be seen from Fig. 3 that the analytical solution obtained with the proposed method fits well to the results of the numerical simulation at every time instance.

Localized tensile and compressive stresses are generated from the EM-induced material depletion and accumulation at the locations of atom flux divergence. The resulting stress gradient leads to a backflow flux of atoms to oppose the EM

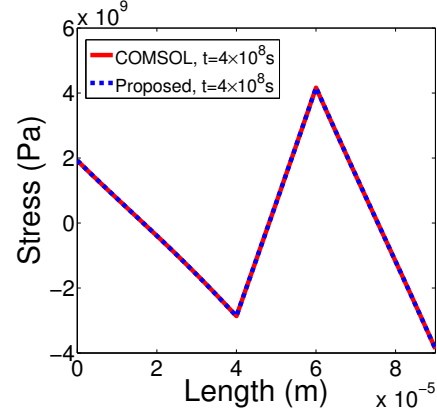


Fig. 4. Steady-state stress distributions along the four-terminal interconnect wire.

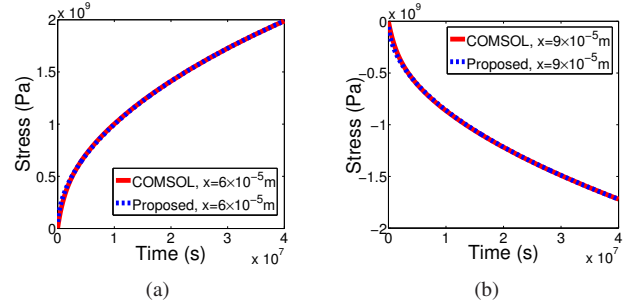


Fig. 5. EM-induced stress development over time for the four terminal interconnect wire: (a) the tensile stress at the position $x = 6 \times 10^{-5} m$; (b) the compressive stress at the position $x = 9 \times 10^{-5} m$.

flux. For the void nucleation phase, EM-induced forms will not form if the balance between the backflow and EM fluxes can be achieved before the tensile stress value along the metal wire reaches the critical stress boundary. In this situation, the wire interconnect structure is virtually immortal. The wire will be immortal if the maximum steady-state stress is less than the critical stress σ_{crit} . It is observed from Fig. 4 that the steady-state stress distributions from the proposed analytical model matches well with the COMSOL simulation results.

It should be noted that the current direction in the middle segment is opposite to the direction of the currents in the left and right segments. As a result, both the tensile and compressive stresses can be generated along this three-segment interconnect wire. Fig. 5(a) and Fig. 5(b) show the developments of the tensile stress at the position $x = 6 \times 10^{-5} m$ and the compressive stress at the position $x = 9 \times 10^{-5} m$, respectively. It can be seen from Fig. 5 that both the tensile and compressive stresses calculated by the proposed analytical model matches well the COMSOL simulation results.

B. Six-terminal interconnect wire ($n = 5$)

Now we calculate the EM-induced stress evolution by the proposed analytical model for the six-terminal interconnect wire. For this type of interconnect wire, the integer n in the closed-form expression (7) equals to 5. For the simulation of stress evolution during the void nucleation phase, the current

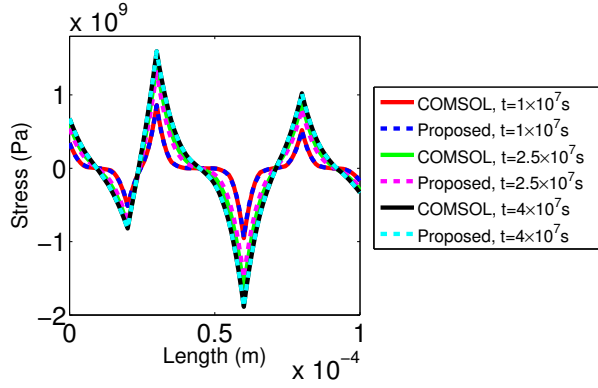


Fig. 6. EM-induced stress development along the six-terminal interconnect wire ($n = 5$).

densities in each segment are set to be $j_1 = 2 \times 10^{10} A/m^2$, $j_2 = -4 \times 10^{10} A/m^2$, $j_3 = 6 \times 10^{10} A/m^2$, $j_4 = -5 \times 10^{10} A/m^2$, and $j_5 = 1 \times 10^{10} A/m^2$, respectively. The lengths $l_i (i = 0, 1, \dots, 5)$ in (7) for this five-segment wire are chosen as $l_0 = 0 \times 10^{-5} m$, $l_1 = 2 \times 10^{-5} m$, $l_2 = 3 \times 10^{-5} m$, $l_3 = 6 \times 10^{-5} m$, $l_4 = 8 \times 10^{-5} m$, and $l_5 = 10 \times 10^{-5} m$, respectively. We use the one-term approximation of the exact series solution (7) to calculate the EM-induced stress development along this five-segment wire. Fig. 6 shows the time evolution of the stress during the void nucleation phase. It can be seen from Fig. 6 that the obtained results are in good agreement with the finite element simulation results.

As mentioned above, if the critical stress σ_{crit} during the void nucleation phase is greater than the maximum steady-state tensile stress value along the five-segment interconnect wire, it is immortal, i.e., no voids will form in the wire and it will never fail. Fig. 7 shows the steady-state stress distributions along the wire, from which we can see that the analytical solution can fit well with the COMSOL simulation results. It should be noted that the first, third, and fifth segments in the line wire have the same current direction that is opposite to the direction of the currents in the second and fourth segments. As a result, both the tensile and compressive stresses can be generated in this interconnect wire. Fig. 8(a) and Fig. 8(b) show the compressive stress development over time at the position $x = 6 \times 10^{-5} m$ and the tensile stress development at $x = 8 \times 10^{-5} m$, respectively. Again, the obtained results from the one-term approximation of the exact series solution match well with the numerical simulation results calculated by COMSOL.

C. Accuracy study for the analytic model

We now study the accuracy of the proposed analytic model using the one-term approximation of the exact series solution against the COMSOL simulation results. Due to limited space, only the results for the three-segment wire case are shown in the paper. The relative errors are plotted in Fig. 9 against the COMSOL simulation results for the case using the one-term approximation, i.e., the integer m in the series solution (7) is

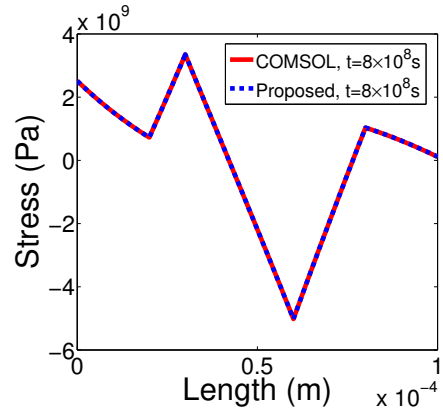


Fig. 7. Steady-state stress distributions along the six-terminal interconnect wire.

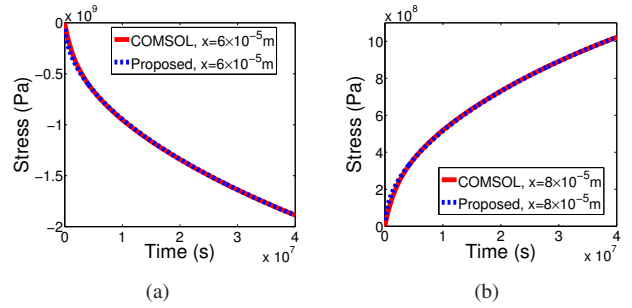


Fig. 8. EM-induced stress development over time for the six-terminal interconnect wire: (a) the compressive stress at the position $x = 6 \times 10^{-5} m$; (b) the tensile stress at the position $x = 8 \times 10^{-5} m$.

set to be zero. As we can see from Fig. 9, the resulting relative errors between the one-term approximation and the COMSOL simulation results are around 2%. In the experiments we noted that the accuracy cannot be improved significantly by adding more terms, which means that the one-term approximation is enough to calculate the EM-induced stress during the void nucleation phase for this three-segment interconnect tree.

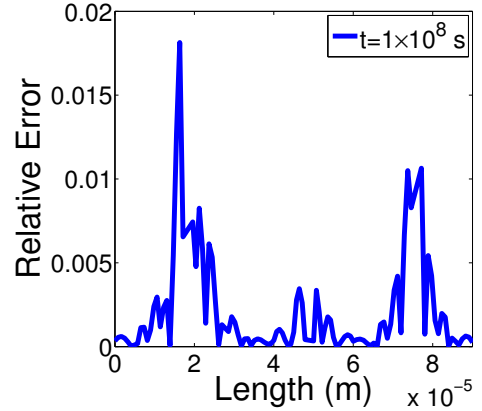


Fig. 9. Relative errors between the proposed analytical model and the COMSOL model for the three-segment interconnect wire.

V. CONCLUSION

In this paper, we have proposed a new analysis method for multi-segment wires considering EM effects during the void nucleation phase. The new analytical model shows an excellent agreement with the detailed numerical analysis obtained from COMSOL.

REFERENCES

- [1] "International technology roadmap for semiconductors (ITRS), 2014 update," 2012. <http://public.itrs.net>.
- [2] B. Bailey, "Thermally Challenged," in *Semiconductor Engineering*, 2013.
- [3] R. De Orio, H. Ceric, and S. Selberherr, "Physically based models of electromigration: From blacks equation to modern tcad models," *Microelectronics Reliability*, vol. 50, no. 6, pp. 775–789, 2010.
- [4] I. A. Blech, "Electromigration in Thin Aluminum Films on Titanium Nitride," *Journal of Applied Physics*, vol. 47, no. 4, pp. 1203–1208, 1976.
- [5] J. R. Black, "Electromigration-A Brief Survey and Some Recent Results," *IEEE Trans. on Electron Devices*, vol. 16, no. 4, pp. 338–347, 1969.
- [6] M. Ohring, *Reliability and Failure of Electronic Materials and Devices*. San Diego: Academic Press, 1998.
- [7] J. R. Lloyd, "New models for interconnect failure in advanced IC technology," *Physical and Failure Analysis of Integrated Circuits, 2008. IPFA 2008. 15th International Symposium on the*, pp. 1–7, 2008.
- [8] M. Hauschildt, C. Henneothal, G. Talut, O. Aubel, M. Gall, K. B. Yeap, and E. Zschech, "Electromigration Early Failure Void Nucleation and Growth Phenomena in Cu And Cu(Mn) Interconnects," in *IEEE International Reliability Physics Symposium (IRPS)*, pp. 2C.1.1–2C.1.6, 2013.
- [9] J. S. Pak, M. Pathak, S. K. Lim, and D. Pan, "Modeling of electromigration in through-silicon-via based 3D IC," in *Electronic Components and Technology Conference (ECTC), 2011 IEEE 61st*, pp. 1420–1427, 2011.
- [10] M. Pathak, J. S. Pak, D. Pan, and S. K. Lim, "Electromigration modeling and full-chip reliability analysis for BEOL interconnect in TSV-based 3D ICs," in *Computer-Aided Design (ICCAD), 2011 IEEE/ACM International Conference on*, pp. 555–562, 2011.
- [11] X. Zhao, Y. Wan, M. Scheuermann, and S. K. Lim, "Transient modeling of TSV-wire electromigration and lifetime analysis of power distribution network for 3D ICs," in *Computer-Aided Design (ICCAD), 2013 IEEE/ACM International Conference on*, pp. 363–370, 2013.
- [12] J. Pak, S. K. Lim, and D. Z. Pan, "Electromigration study for multi-scale power/ground vias in TSV-based 3D ICs," in *Computer-Aided Design (ICCAD), 2013 IEEE/ACM International Conference on*, pp. 379–386, 2013.
- [13] X. Huang, T. Yu, V. Sukharev, and S. X.-D. Tan, "Physics-based electromigration assessment for power grid networks," in *Proc. Design Automation Conf. (DAC)*, June 2014.
- [14] M. A. Korhonen, P. Borgesen, K. N. Tu, and C. Y. Li, "Stress Evolution Due to Electromigration in Confined Metal Lines," *Journal of Applied Physics*, vol. 73, no. 8, pp. 3790–3799, 1993.
- [15] H. Chen, S. X.-D. Tan, V. Sukharev, X. Huang, and T. Kim, "Interconnect reliability modeling and analysis for multi-branch interconnect trees," in *Proc. Design Automation Conf. (DAC)*, June 2015.
- [16] H. Chen, X. Tan, X. Huang, T. Kim, and V. Sukharev, "Analytical modeling and characterization of electromigration effects for multi-branch interconnect trees," *IEEE Trans. on Computer-Aided Design of Integrated Circuits and Systems*, vol. 35, pp. 1811–1824, Nov. 2016.
- [17] J. J. Clement, S. P. Riege, R. Cvijetic, and C. V. Thompson, "Methodology for Electromigration Critical Threshold Design Rule Evaluation," *IEEE Trans. on Computer-Aided Design of Integrated Circuits and Systems*, vol. 18, no. 5, pp. 576–581, 1999.
- [18] V. Sukharev, X. Huang, and S. X.-D. Tan, "Electromigration Induced Stress Evolution Under Alternate Current and Pulse Current Loads," *Journal of Applied Physics*, vol. 118, pp. 034504–1–034504–10, 2015.
- [19] X. Huang, A. Kteyan, X. Tan, and V. Sukharev, "Physics-based electromigration models and full-chip assessment for power grid networks," *IEEE Trans. on Computer-Aided Design of Integrated Circuits and Systems*, vol. 35, pp. 1848–1861, Nov. 2016.
- [20] Z. Sun, E. Demircan, T. Kim, X. Huang, T. Kim, and S. X.-D. Tan, "Voltage-based electromigration immortality check for general multi-branch interconnects," in *Proc. Int. Conf. on Computer Aided Design (ICCAD)*, 2016.