



上海FPGA:

PIN	Symbol	PIN	Symbol
1	DB00	31	RD_SDO
2	DB01	32	CS
3	DB02	33	RS_SDI
4	DB03	34	GND
5	DB04	35	WR_SCL
6	DB05	36	GND
7	DB06	37	DCLK
8	DB07	38	GND
9	GND	39	CS
10	DB08	40	RS_SDI
11	DB09	41	GND
12	DB10	42	SK
13	DB11	43	GND
14	DB12	44	RD_SDO
15	DB13	45	GND
16	DB14	46	VDD_2.8V
17	DB15	47	VDD_2.8V
18	GND	48	GND
19	DB16	49	GND
20	DB17	50	VBL
21	DB18	51	VBL
22	DB19	52	GND
23	DB20	53	GND
24	DB21	54	VDD_IN
25	DB22	55	VDD_IN
26	DB23	56	X+
27	GND	57	Y+
28	HSYNC	58	X-
29	VSYNC	59	Y-
30	EN	60	GND

1. 接口使用FH26-51S-0.3HW。
2. 背光使用恒流驱动 $I=120\text{mA}$ ，电压为 $V=3.2\text{V}$ 。
3. 视角为6:00方向。
4. 按图示跳线

 <div> <p>TIANMA MICROELECTRONICS CO.,LTD.</p> <p>8F 64th Building Along Majiang Industrial AreaJiankangDistrict, Shenzhen, China</p> </div>		
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