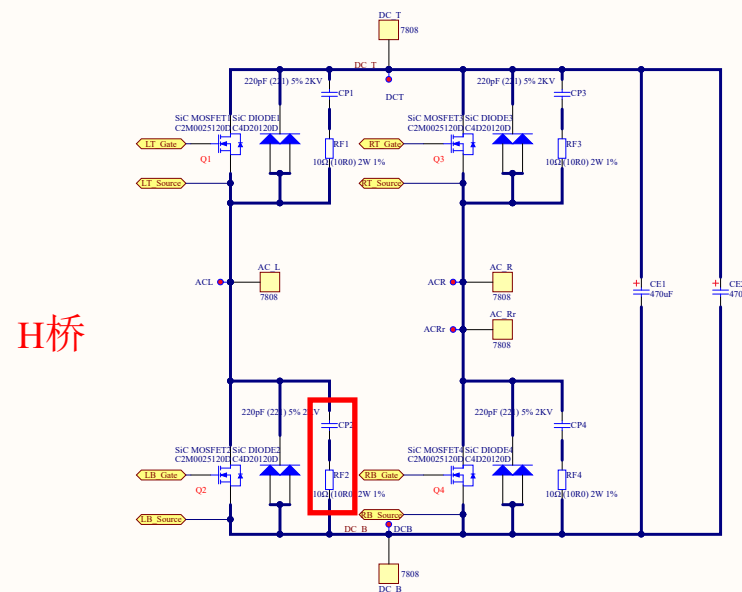
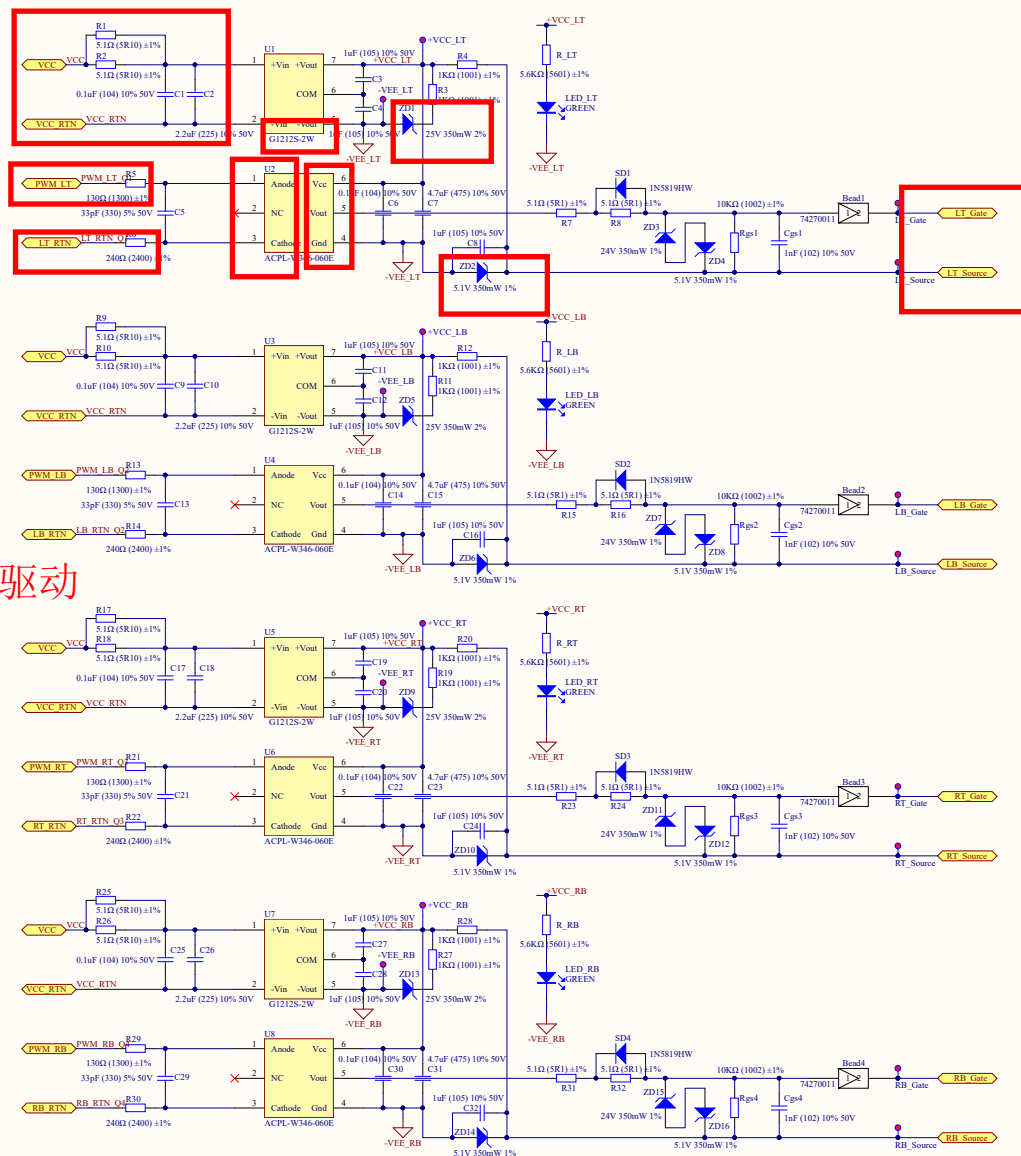


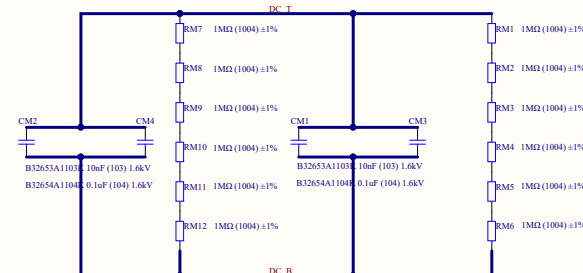
The schematic diagram illustrates the LED driver circuit. It features three PWM controllers, P1, P2, and P3. P1 is connected to a DC Voltage source and its output (P1_RTN) is connected to the LED. P2 and P3 are connected to their respective PWM sources and their outputs (P2_RTN, P3_RTN) are connected to the LED. The LED is connected to a common return (VCC_RTN).

PCB
LOGO
CAUTION

PCB
LOGO
CAUTION!!



H桥



滤波稳压

Title			高频全隔离碳化硅功率模块		
Size A2	Number				Revision V2.5
Date:	2019/3/25			Sheet of	
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