

#### Thursday, February 16, 2023

Met with Jackson [8 hours and 30 minutes] - I wish I was kidding

- Integration testing
- Unit testing
- Load-store.v
- Need to figure out branching
- Implemented hazards and stall
- Updated datapath
- Updated design doc

#### Wednesday, February 15, 2023

Met with Jackson [4 hours]

- Update the control design in the doc and add new signals
- Control implementation
- Start Integration testing
- Jackson finished the subsystem components and I began testing them

#### Tuesday, February 14, 2023

Only individual work today because Valentine's day <3

- Initial control revision
- Unit tests and debugging
- Worked on reister testing

#### Monday, February 13, 2023

Worked during class and then Jackson and I met for 3 hours

- Fixed alu behavior
- New alu unit tests
- Fixed two way mux
- Finalized memory stuff

#### Sunday, February 12, 2023

"Team" Meeting for 4 hours. Ethan 2 hours late

- Memory lab :(

February 8, 2023

Met with team

- 4 way mux
- 4 way mux test
- 2 way mux
- 2 way mux test

~Milestone 5 where we died and I didn't write any journal entries~

Tuesday, January 31, 2023

Met with team [60 minutes]

- Worked on implementation
- Worked on test files
- Corrected compilation errors

Monday, January 30, 2023

Just me [60 minutes]

- Worked on implementation of inst-reg

Saturday, January 28, 2023

Met with team [60 minutes]

- Divide up tasks
- Corrected changes from meeting with robert
- Started work on implementation

Wednesday, January 22, 2023

Met with Ethan [20 minutes]

- Assigned final tasks
- I will make the good copy of the datapath
- I will look at the list of controls

Sunday, January 22, 2023

Met with team [90 minutes]

- Cleaned up data path from milestone 2
- Decided to pipeline for speed

- Reevaluated RTL to match up with pipeline cycles
- Discussed testing strategies
- My task is to finish the datapath for the last 2 cycles of the pipeline

Saturday, January 21, 2023

Met with Jackson [30 minutes]

- Made minor changes to design doc after meeting with Robert
- Updated file names

Wednesday, January 18, 2023

Met with team [1 hour]

- Created the component chart for our RTL
- Determined naming conventions that were easy to read and consistent

Monday, January 16, 2023

Met with team [20 minutes]

- Peer Review of RTL

Sunday, January 15, 2023

Met with team [1 hours 30 minutes]

- We created the RTL for our instruction set. We grouped together instructions of the same type because they have similar behavior
- We created rough drafts of the datapath and controls for R type instructions to get a head start on the other milestones
- Made changes to Milestone 1 after meeting with Robert
- My task was to finish RTL for M type instructions (30 minutes outside of meeting)

Friday, January 11, 2023

Met with team [60 min]

- Finish translation to machine code
- Handle reading from input port and writing to output port

Sunday, January 8, 2023

Met with team [2 hours 30 minutes]

- We decided on a load store processor because it would accomplish our goal of speed and would be easy to program in
- We made our set of instructions. We have 3 instruction types. We completed the table and diagrams.
- We outlined our design philosophy
- Worked on writing code snippets in our language
- I will complete RelPrime and the goal of our next meeting will be to clean up the document and finalize today's wo

Friday, January 6, 2023

Met with team [35 min]

- This meeting was mostly setting up everything
  - Created the github classroom
  - Cloned copies of the code to all computers
  - Made all necessary google docs
  - Established some expectations for the team
- Down a team member so ended earlier and didn't make any critical decisions