

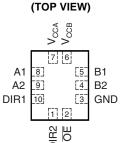
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DUAL-BIT DUAL-SUPPLY BUS TRANSCEIVER WITH CONFIGURABLE VOLTAGE TRANSLATION AND 3-STATE OUTPUTS

FEATURES

- Each Channel Has Independent Direction Control
- Control Inputs V_{IH}/V_{IL} Levels Are Referenced to V_{CCA} Voltage
- Fully Configurable Dual-Rail Design Allows Each Port to Operate Over the Full 1.2-V to 3.6-V Power-Supply Range
- I/Os Are 4.6-V Tolerant
- I_{off} Supports Partial-Power-Down Mode Operation
- Typical Data Rates
 - 500 Mbps (1.8-V to 3.3-V Translation)
 - 320 Mbps (<1.8-V to 3.3-V Translation)
 - 320 Mbps (Translate to 2.5 V or 1.8 V)
 - 280 Mbps (Translate to 1.5 V)
 - 240 Mbps (Translate to 1.2 V)

- Latch-Up Performance Exceeds 100 mA Per JESD 78, Class II
- ESD Protection Exceeds JESD 22
 - 5000-V Human-Body Model (A114-A)
 - 200-V Machine Model (A115-A)
 - 1500-V Charged-Device Model (C101)



RSW PACKAGE

DESCRIPTION/ORDERING INFORMATION

This dual-bit noninverting bus transceiver uses two separate configurable power-supply rails. The A port is designed to track V_{CCA} . V_{CCA} accepts any supply voltage from 1.2 V to 3.6 V. The B port is designed to track V_{CCB} . V_{CCB} accepts any supply voltage from 1.2 V to 3.6 V. This allows for universal low-voltage bidirectional translation between any of the 1.2-V, 1.5-V, 1.8-V, 2.5-V, and 3.3-V voltage nodes.

ORDERING INFORMATION

T _A	PACKAGE	(1)(2)	ORDERABLE PART NUMBER	TOP-SIDE MARKING
-40°C to 85°C	QFN - RSW	Tape and reel	SN74AVC2T245RSWR	TLO

(1) Package drawings, thermal data, and symbolization are available at www.ti.com/packaging.



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

⁽²⁾ For the most current package and ordering information, see the Package Option Addendum at the end of this document, or see the TI website at www.ti.com.



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DESCRIPTION/ORDERING INFORMATION (CONTINUED)

The SN74AVC2T245 is designed for asynchronous communication between two data buses. The logic levels of the direction-control (DIR) input and the output-enable (\overline{OE}) activate either the B-port outputs or the A-port outputs or place both output ports into the high-impedance mode . The device transmits data from the A bus to the B bus when the B-port outputs are activated and from the B bus to the A bus when the A-port outputs are activated. The input circuitry on both A and B ports always is active and must have a logic HIGH or LOW level applied to prevent excess I_{CC} and I_{CCZ} .

The SN74AVC2T245 is designed so that the control pins (DIR1, DIR2, and OE) are supplied by V_{CCA}.

This device is fully specified for partial-power-down applications using I_{off}. The I_{off} circuitry disables the outputs, preventing damaging current backflow through the device when it is powered down.

The V_{CC} isolation feature ensures that if either V_{CC} input is at GND, both ports are in the high-impedance state.

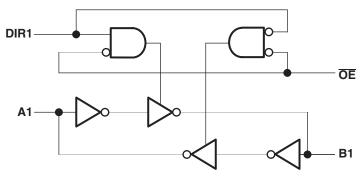
To ensure the high-impedance state during power up or power down, \overline{OE} must be connected to V_{CC} through a pull-up resistor; the minimum value of the resistor is determined by the current-sinking capability of the driver.

FUNCTION TABLE⁽¹⁾ (EACH TRANSCEIVER)

CONTRO	L INPUTS	OUTPUT	CIRCUITS	OPERATION
ŌĒ	DIR1	A PORT	B PORT	OPERATION
L	L	Enabled	Hi-Z	B data to A data
L	Н	Hi-Z	Enabled	A data to B data
Н	Χ	Hi-Z	Hi-Z	Isolation

(1) Input circuits of the data I/Os are always active.

LOGIC DIAGRAM (POSITIVE LOGIC)(1)



(1) Shown for a single channel



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TYPICAL APPLICATION

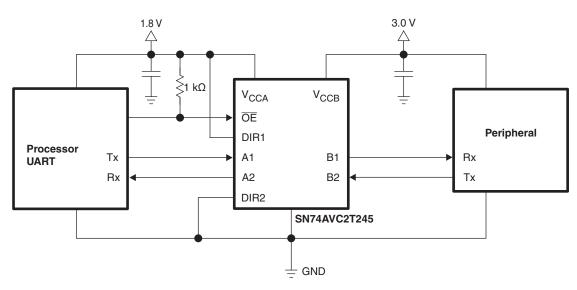


Figure 1. Typical Application of the SN74AVC2T245

ABSOLUTE MAXIMUM RATINGS(1)

over operating free-air temperature range (unless otherwise noted)

			MIN	MAX	UNIT
V_{CCA}	Supply voltage range		-0.5	4.6	V
		I/O ports (A port)	-0.5	4.6	
V_{I}	Input voltage range ⁽²⁾	I/O ports (B port)	-0.5	4.6	V
		Control inputs	-0.5	4.6	
V	Voltage range applied to any output in the high-impedance or power-off state ⁽²⁾	A port	-0.5	4.6	V
Vo	power-off state ⁽²⁾	B port	-0.5	4.6	V
	Valta as a second and the case of the state of the state of (2)(3)	A port	-0.5	V _{CCA} + 0.5	V
Vo	Voltage range applied to any output in the high or low state (2)(3)	B port	-0.5	V _{CCB} + 0.5	V
I _{IK}	Input clamp current	V _I < 0		-50	mA
lok	Output clamp current	V _O < 0		-50	mA
Io	Continuous output current			±50	mA
	Continuous current through V _{CCA} , V _{CCB} , or GND			±100	mA
		D package (4)		73	
		DB package ⁽⁴⁾		82	
θ_{JA}	Package thermal impedance	DGV package (4)		120	°C/W
		PW package ⁽⁴⁾		108	
		RGY package ⁽⁵⁾		39	
T _{stg}	Storage temperature range		-65	150	°C

⁽¹⁾ Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

⁽²⁾ The input voltage and output negative-voltage ratings may be exceeded if the input and output current ratings are observed.

⁽³⁾ The output positive-voltage rating may be exceeded up to 4.6 V maximum if the output current rating is observed.

⁽⁴⁾ The package thermal impedance is calculated in accordance with JESD 51-7.

⁵⁾ The package thermal impedance is calculated in accordance with JESD 51-5.

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RECOMMENDED OPERATING CONDITIONS(1)(2)(3)

			V _{CCI}	V _{cco}	MIN	MAX	UNIT
V_{CCA}	Supply voltage				1.2	3.6	٧
V_{CCB}	Supply voltage				1.2	3.6	٧
			1.2 V to 1.95 V		V _{CCI} × 0.65		
V_{IH}	High-level input voltage	Data inputs (4)	1.95 V to 2.7 V		1.6		V
	input voltage		2.7 V to 3.6 V		2		
			1.2 V to 1.95 V			$V_{CCI} \times 0.35$	
V_{IL}	Low-level input voltage	Data inputs ⁽⁴⁾	1.95 V to 2.7 V			0.7	V
	input voltage		2.7 V to 3.6 V			8.0	
			1.2 V to 1.95 V		$V_{CCA} \times 0.65$		
V_{IH}	High-level input voltage	DIR (referenced to V _{CCA}) ⁽⁵⁾	1.95 V to 2.7 V		1.6		V
	input voltago	(Totoronood to VCCA)	2.7 V to 3.6 V		2		
			1.2 V to 1.95 V			$V_{CCA} \times 0.35$	
V_{IL}	Low-level input voltage	DIR (referenced to V _{CCA}) ⁽⁵⁾	1.95 V to 2.7 V			0.7	V
	input voltage	(Totoronood to VCCA)	2.7 V to 3.6 V			0.8	
V_{I}	Input voltage				0	3.6	V
V	Output voltage	Active state			0	V _{cco}	V
Vo	Output voltage	3-state			0	3.6	V
				1.1 V to 1.2 V		-3	
				1.4 V to 1.6 V		-6	
I_{OH}	High-level output c	urrent		1.65 V to 1.95 V		8–	mA
				2.3 V to 2.7 V		-9	
				3 V to 3.6 V		-12	
				1.1 V to 1.2 V		3	
				1.4 V to 1.6 V		6	
I_{OL}	Low-level output cu	ırrent		1.65 V to 1.95 V		8	mA
				2.3 V to 2.7 V		9	
				3 V to 3.6 V		12	
Δt/Δν	Input transition rise	or fall rate				5	ns/V
T _A	Operating free-air t	emperature			-40	85	°C

V_{CCI} is the V_{CC} associated with the input port.
 V_{CCO} is the V_{CC} associated with the output port.
 All unused data inputs of the device must be held at V_{CCI} or GND to ensure proper device operation. Refer to the TI application report, *Implications of Slow or Floating CMOS Inputs*, literature number SCBA004.
 For V_{CCI} values not specified in the data sheet, V_{IH} min = V_{CCI} × 0.7 V, V_{IL} max = V_{CCI} × 0.3 V
 For V_{CCI} values not specified in the data sheet, V_{IH} min = V_{CCA} × 0.7 V, V_{IL} max = V_{CCA} × 0.3 V



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ELECTRICAL CHARACTERISTICS (1)(2)

over recommended operating free-air temperature range (unless otherwise noted)

D^	RAMETER	TEST CONDI	LIONE	V	V	T _A =	25°C		-40°C to 8	5°C	UNIT
PA	KAWEIEK	TEST CONDI	IIONS	V _{CCA}	V _{CCB}	MIN	TYP	MAX	MIN	MAX	UNII
		$I_{OH} = -100 \mu A$		1.2 V to 3.6 V	1.2 V to 3.6 V				V _{CCO} - 0.2		
		$I_{OH} = -3 \text{ mA}$		1.2 V	1.2 V	(0.95				
\/		I _{OH} = -6 mA	\ \ \ \ \ \	1.4 V	1.4 V				1.05		V
V _{OH}		I _{OH} = -8 mA	$V_I = V_{IH}$	1.65 V	1.65 V				1.2		V
		$I_{OH} = -9 \text{ mA}$		2.3 V	2.3 V				1.75		
		I _{OH} = -12 mA		3 V	3 V				2.3		
		I _{OL} = 100 μA		1.2 V to 3.6 V	1.2 V to 3.6 V					0.2	
		$I_{OL} = 3 \text{ mA}$		1.2 V	1.2 V	(0.25				
\/		$I_{OL} = 6 \text{ mA}$	$V_I = V_{IL}$	1.4 V	1.4 V					0.35	V
V_{OL}		$I_{OL} = 8 \text{ mA}$	$V_{\parallel} = V_{\parallel}$	1.65 V	1.65 V					0.45	V
		I _{OL} = 9 mA		2.3 V	2.3 V					0.55	
		I _{OL} = 12 mA		3 V	3 V					0.7	
I _I	Control inputs	V _I = V _{CCA} or GND		1.2 V to 3.6 V	1.2 V to 3.6 V	±0.	.025	±0.25		±1	μΑ
	A D	V == V = 0.0		0 V	0 V to 3.6 V	:	±0.1	±1		±5	
l _{off}	A or B port	V_{I} or $V_{O} = 0$ to 3.6	V	0 V to 3.6 V	0 V	:	±0.1	±1		±5	μΑ
I _{OZ}	A or B port	$V_O = V_{CCO}$ or GND $V_I = V_{CCI}$ or GND,	OE = V _{IH}	3.6 V	3.6 V	:	±0.5	±2.5		±5	μΑ
				1.2 V to 3.6 V	1.2 V to 3.6 V					8	
I_{CCA}		$V_I = V_{CCI}$ or GND,	I _O = 0	0 V	0 V to 3.6 V					-2	μΑ
				0 V to 3.6 V	0 V					8	
				1.2 V to 3.6 V	1.2 V to 3.6 V					8	
I_{CCB}		$V_I = V_{CCI}$ or GND,	I _O = 0	0 V	0 V to 3.6 V					8	μΑ
				0 V to 3.6 V	0 V					-2	
I _{CCA} -	+ I _{CCB}	$V_I = V_{CCI}$ or GND,	I _O = 0	1.2 V to 3.6 V	1.2 V to 3.6 V					16	μΑ
Ci	Control inputs	V _I = 3.3 V or GND		3.3 V	3.3 V		3.5			4.5	pF
Cio	A or B port	V _O = 3.3 V or GND)	3.3 V	3.3 V		6			7	рF

 $[\]begin{array}{ll} \hbox{(1)} & V_{CCO} \text{ is the } V_{CC} \text{ associated with the output port.} \\ \hbox{(2)} & V_{CCI} \text{ is the } V_{CC} \text{ associated with the input port.} \\ \end{array}$



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SWITCHING CHARACTERISTICS

over recommended operating free-air temperature range, $V_{CCA} = 1.2 \text{ V}$ (unless otherwise noted) (see Figure 2)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	V _{CCB} = 1.2 V	V _{CCB} = 1.5 V ± 0.1 V	V _{CCB} = 1.8 V ± 0.15 V	V _{CCB} = 2.5 V ± 0.2 V	V _{CCB} = 3.3 V ± 0.3 V	UNIT					
	(INFOT)	(0011-01)	TYP	TYP	TYP	TYP	TYP						
t _{PLH}	А	В	2.5	2.1	1.9	1.9	1.9	20					
t _{PHL}	A	Б	2.5	2.1	1.9	1.9	1.9	ns					
t _{PLH}	В	А	2.5	2.2	2	1.8	1.7	no					
t _{PHL}	ь	A	2.5	2.2	2	1.8	1.7	ns					
t _{PZH}	ŌĒ	۸	3.8	3.1	2.7	2.6	3	20					
t _{PZL}	OE	A	3.8	3.1	2.7	2.6	3	ns					
t _{PZH}	ŌĒ	В	3.7	3.7	3.7	3.7	3.7	20					
t _{PZL}	OE	Б	3.7	3.7	3.7	3.7	3.7	ns					
t _{PHZ}	ŌĒ	۸	4.4	3.6	3.5	3.3	4.1	20					
t _{PLZ}	OE .	Α	4.4	3.6	3.5	3.3	4.1	ns					
t _{PHZ}		Б	4.2	4.2	4.3	4.1	4.2	20					
t _{PLZ}	ŌĒ	OE	OE	OE	OE	В	4.2	4.2	4.3	4.1	4.2	ns	

SWITCHING CHARACTERISTICS

over recommended operating free-air temperature range, V_{CCA} = 1.5 V \pm 0.1 V (see Figure 2)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	V _{CCB} = 1.2 V	V _{CCB} = ± 0.1		V _{CCB} = ± 0.1		V _{CCB} = ± 0.2		V _{CCB} = ± 0.	= 3.3 V 3 V	UNIT																
	(INPOT)	(001F01)	TYP	MIN	MAX	MIN	MAX	MIN	MAX	MIN	MAX																	
t _{PLH}	А	В	2.2	0.3	4.4	0.2	3.9	0.1	3.6	0.1	3.9	20																
t _{PHL}	A	Б	2.2	0.3	4.4	0.2	3.9	0.1	3.6	0.1	3.9	ns																
t _{PLH}	В	Α	2	0.6	5.1	0.4	4.9	0.2	4.6	0.1	4.5	20																
t _{PHL}	Ь	A	2	0.6	5.1	0.4	4.9	0.2	4.6	0.1	4.5	ns																
t _{PZH}	ŌĒ	Α	3.4	1.1	7.1	0.9	6.2	0.7	5.5	0.1	6.4	20																
t _{PZL}	OE	A	3.4	1.1	7.1	0.9	6.2	0.7	5.5	0.1	6.4	ns																
t _{PZH}	ŌĒ	В	2.5	1.1	8.2	1.1	8.2	1.1	8.2	1.1	8.2	20																
t _{PZL}	OE	Б	2.5	1.1	8.2	1.1	8.2	1.1	8.2	1.1	8.2	ns																
t _{PHZ}	ŌĒ	Α	4.1	1.2	7.1	0.8	6.7	0.4	5.6	1	74	20																
t _{PLZ}	OE	A	4.1	1.2	7.1	0.8	6.7	0.4	5.6	1	7.4	ns																
t _{PHZ}	ŌĒ B	В	3.3	0.3	7.4	0.2	5.7	0.3	5.6	0.3	5.6	20																
t _{PLZ}	ŌĒ	ŌĒ	ŌĒ	OĒ	OE	OE	OĒ	ŌĒ	ŌĒ	ŌĒ	ŌĒ	ŌĒ	OĒ	OĒ	OE	ŌĒ	ŌĒ E	В	3.3	0.3	7.4	0.2	5.7	0.3	5.6	0.3	5.6	ns



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SWITCHING CHARACTERISTICS

over recommended operating free-air temperature range, $V_{CCA} = 1.8 \text{ V} \pm 0.15 \text{ V}$ (see Figure 2)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	V _{CCB} = 1.2 V	V _{CCB} = ± 0.1		V _{CCB} = ± 0.7		V _{CCB} = ± 0.2		V _{CCB} = 3.3 V ± 0.3 V		UNIT															
	(INFOT)	(OUTPUT)	TYP	MIN	MAX	MIN	MAX	MIN	MAX	MIN	MAX																
t _{PLH}	Α	В	2	0.1	4.1	0.1	3.6	0.1	3.1	0.1	3.3	ns															
t _{PHL}	A	Б	2	0.1	4.1	0.1	3.6	0.1	3.1	0.1	3.3	115															
t _{PLH}	В	Α	1.9	0.4	4.3	0.1	4.1	0.1	3.8	0.1	3.7	ns															
t _{PHL}	ь	Α	1.9	0.4	4.3	0.1	4.1	0.1	3.8	0.1	3.7	115															
t _{PZH}	ŌĒ	Α	3.2	0.8	6.7	0.4	5.8	0.4	4.8	0.3	4.6	ns															
t _{PZL}	OE	A	3.2	0.8	6.7	0.4	5.8	0.4	4.8	0.3	4.6	115															
t _{PZH}	ŌĒ	В	1.9	0.2	6.7	0.2	6.6	0.2	6.7	0.2	6.7	ns															
t _{PZL}	OL	ь	1.9	0.2	6.7	0.2	6.6	0.2	6.7	0.2	6.7	115															
t _{PHZ}	ŌĒ	Α	3.8	0.7	6.2	0.3	6.5	0.1	5.2	0.8	6.5	ns															
t_{PLZ}	OL	Α	3.8	0.7	6.2	0.3	6.5	0.1	5.2	0.8	6.5	115															
t _{PHZ}	ŌĒ	В	3.4	0.1	6.8	0.1	6.8	0.1	6.7	0.1	6.7	ns															
t _{PLZ}	ŌĒ	OE	OĒ	OE	OE	OE	OE	OE	OE	OE	OE	OE	OE	OE	OE	OE	В	3.4	0.1	6.8	0.1	6.8	0.1	6.7	0.1	6.7	115

SWITCHING CHARACTERISTICS

over recommended operating free-air temperature range, V_{CCA} = 2.5 V \pm 0.2 V (see Figure 2)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	V _{CCB} = 1.2 V	V _{CCB} = ± 0.1		V _{CCB} = ± 0.1		V _{CCB} = ± 0.2		V _{CCB} = ± 0.3		UNIT													
	(INPUT)	(001701)	TYP	MIN	MAX	MIN	MAX	MIN	MAX	MIN	MAX														
t _{PLH}	А	В	1.9	0.1	3.8	0.1	3.2	0.1	2.7	0.1	2.6														
t _{PHL}	A	В	1.9	0.1	3.8	0.1	3.2	0.1	2.7	0.1	2.6	ns													
t _{PLH}	В	Α	1.8	0.5	3.4	0.2	3.1	0.1	2.8	0.1	2.6	no													
t _{PHL}	D	A	1.8	0.5	3.4	0.2	3.1	0.1	2.8	0.1	2.6	ns													
t _{PZH}	ŌĒ	Α	3.1	0.7	6.2	0.5	5.2	0.3	4.1	0.3	3.6														
t _{PZL}	OE	A	3.1	0.7	6.2	0.5	5.2	0.3	4.1	0.3	3.6	ns													
t _{PZH}	ŌĒ	В	1.4	0.4	4.5	0.4	4.5	0.4	4.5	0.4	4.5	20													
t _{PZL}	OE	В	1.4	0.4	4.5	0.4	4.5	0.4	4.5	0.4	4.5	ns													
t _{PHZ}	ŌĒ	Α	3.6	0.2	5.2	0.1	5.4	0.1	4.5	0.7	6	20													
t _{PLZ}	OE	A	3.6	0.2	5.2	0.1	5.4	0.1	4.5	0.7	6	ns													
t _{PHZ}	OF	D	2.1	0.1	4.7	0.1	4.6	0.1	4.7	0.1	4.7														
t _{PLZ}	ŌĒ	ŌĒ	ŌĒ	OE	ŌĒ	ŌĒ	ŌĒ	ŌĒ	ŌĒ	ŌĒ	ŌĒ	ŌĒ	ŌĒ	ŌĒ	ŌE B	2.1	0.1	4.7	0.1	4.6	0.1	4.7	0.1	4.7	ns

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SWITCHING CHARACTERISTICS

over recommended operating free-air temperature range, $V_{CCA} = 3.3 \text{ V} \pm 0.3 \text{ V}$ (see Figure 2)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	V _{CCB} = 1.2 V				V _{CCB} = 1.8 V ± 0.15 V		2.5 V 2 V	V _{CCB} = 3.3 V ± 0.3 V		UNIT																	
	(INPUI)	(OUTPUT)	TYP	MIN	MAX	MIN	MAX	MIN	MAX	MIN	MAX																		
t _{PLH}	А	В	1.8	0.1	3.6	0.1	3	0.1	2.6	0.1	2.4	20																	
t _{PHL}	A	Ь	1.8	0.1	3.6	0.1	3	0.1	2.6	0.1	2.4	ns																	
t _{PLH}	В	Α	1.9	0.5	3.4	0.2	2.9	0.1	2.5	0.1	2.3	ns																	
t _{PHL}	Ь	A	1.9	0.5	3.4	0.2	2.9	0.1	2.5	0.1	2.3	110																	
t _{PZH}	ŌĒ	Α	3.1	0.9	5.9	0.5	5	0.3	3.8	0.3	3.3	20																	
t _{PZL}	OE	A	3.1	0.9	5.9	0.5	5	0.3	3.8	0.3	3.3	ns																	
t _{PZH}	ŌĒ	В	1.2	0.4	3.6	0.4	3.6	0.4	3.6	0.4	3.6	ns																	
t _{PZL}	OL	В	1.2	0.4	3.6	0.4	3.6	0.4	3.6	0.4	3.6	110																	
t _{PHZ}	ŌĒ	Α	3.4	0.1	4.6	0.1	4.7	0.3	4.8	0.7	4.5	ns																	
t _{PLZ}	OE	A	3.4	0.1	4.6	0.1	4.7	0.3	4.8	0.7	4.5	115																	
t _{PHZ}	ŌĒ	ŌĒ	ŌĒ	ŌĒ	ŌĒ	ŌĒ	ŌĒ	ŌĒ	ŌĒ	ŌĒ	ŌĒ	ŌĒ	ŌĒ	ŌĒ	ŌĒ	ŌĒ	ŌĒ	ŌĒ	В	2.9	0.1	5.4	0.1	5.3	0.1	5.3	0.1	5.3	ne
t _{PLZ}																		ם	2.9	0.1	5.4	0.1	5.3	0.1	5.3	0.1	5.3	ns	

OPERATING CHARACTERISTICS

 $T_A = 25$ °C

F	PARAME	TER	TEST CONDITIONS	V _{CCA} = V _{CCB} = 1.2 V	V _{CCA} = V _{CCB} = 1.5 V	V _{CCA} = V _{CCB} = 1.8 V	V _{CCA} = V _{CCB} = 2.5 V	V _{CCA} = V _{CCB} = 3.3 V	UNIT
	A 40 D	Outputs enabled		3	3	3	3	4	
C _{pdA} ⁽¹⁾	A to B	Outputs disabled	$C_L = 0,$ f = 10 MHz,	1	1	1	2	2	n.E
O _{pd} A ` /	B to A	Outputs enabled	$t_r = t_f = 1 \text{ ns}$	12	13	13	15	15	pF
	B to A	Outputs disabled		1	2	2	2	2	
	A to B	Outputs enabled		12	13	13	14	16	
C _{pdB} ⁽¹⁾	AIOB	Outputs disabled	$C_L = 0,$ f = 10 MHz,	1	2	2	2	2	n.E
□pdB ` /	B to A	Outputs enabled	$t_r = t_f = 1 \text{ ns}$	3	3	3	4	4	pF
	D 10 A	Outputs disabled		1	1	1	2	2	

⁽¹⁾ Power dissipation capacitance per transceiver

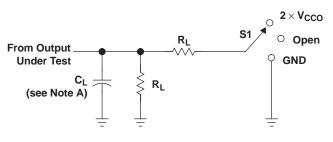
 V_{CCA}

CCA/2



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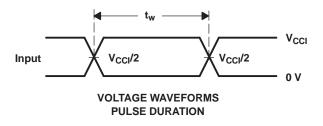
PARAMETER MEASUREMENT INFORMATION



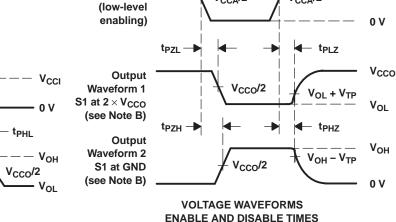
TEST	S 1
t _{pd}	Open
t _{PLZ} /t _{PZL}	$2 \times V_{CCO}$
t _{PHZ} /t _{PZH}	GND

LOAD CIRCUIT

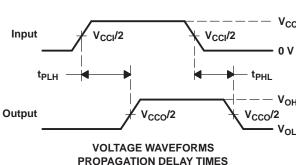
V _{CCO}	CL	R_L	V _{TP}
1.2 V	15 pF	2 k Ω	0.1 V
1.5 V \pm 0.1 V	15 pF	2 k Ω	0.1 V
1.8 V \pm 0.15 V	15 pF	2 k Ω	0.15 V
2.5 V \pm 0.2 V	15 pF	2 k Ω	0.15 V
3.3 V \pm 0.3 V	15 pF	2 k Ω	0.3 V



V_{CCA}/2



Output Control



NOTES: A. C_L includes probe and jig capacitance.

- B. Waveform 1 is for an output with internal conditions such that the output is low, except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high, except when disabled by the output control.
- C. All input pulses are supplied by generators having the following characteristics: PRR \leq 10 MHz, $Z_0 = 50 \Omega$, $dv/dt \geq 1 V/ns$.
- D. The outputs are measured one at a time, with one transition per measurement.
- E. $t_{Pl,7}$ and t_{PH7} are the same as t_{dis} .
- F. t_{PZL} and t_{PZH} are the same as t_{en} .
- G. t_{PLH} and t_{PHL} are the same as t_{pd} .
- H. V_{CCI} is the V_{CC} associated with the input port.
- I. V_{CCO} is the V_{CC} associated with the output port.

Figure 2. Load and Circuit and Voltage Waveforms



PACKAGE OPTION ADDENDUM

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PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins F	Package Qty	Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp ⁽³⁾
SN74AVC2T245RSWR	ACTIVE	UQFN	RSW	10	3000	Green (RoHS &	CU NIPDAU	Level-1-260C-UNLIM
						no Sb/Br)		

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

(3) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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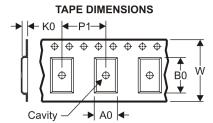
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PACKAGE MATERIALS INFORMATION

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TAPE AND REEL INFORMATION





A0	Dimension designed to accommodate the component width
В0	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



*All dimensions are nominal

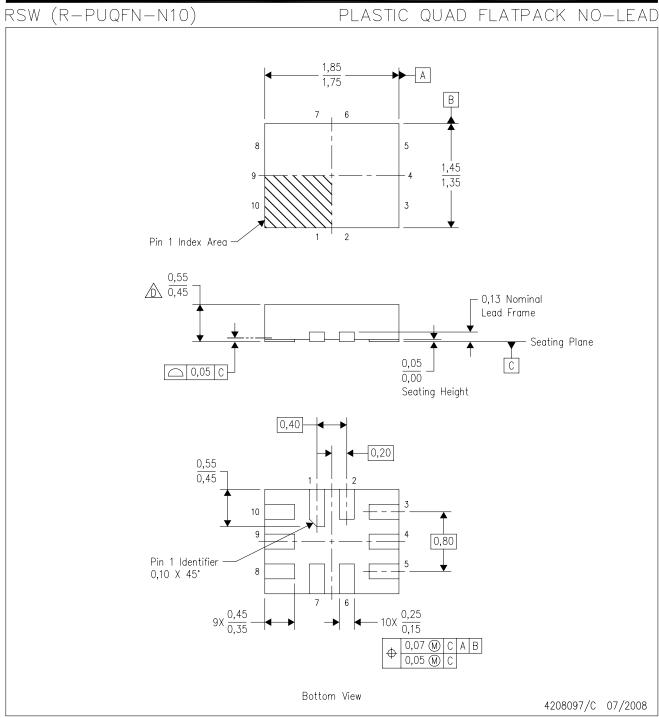
Device	Package Type	Package Drawing			Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
SN74AVC2T245RSWR	UQFN	RSW	10	3000	179.0	8.4	1.7	2.1	0.7	4.0	8.0	Q1

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*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)	
SN74AVC2T245RSWR	UQFN	RSW	10	3000	220.0	205.0	50.0	

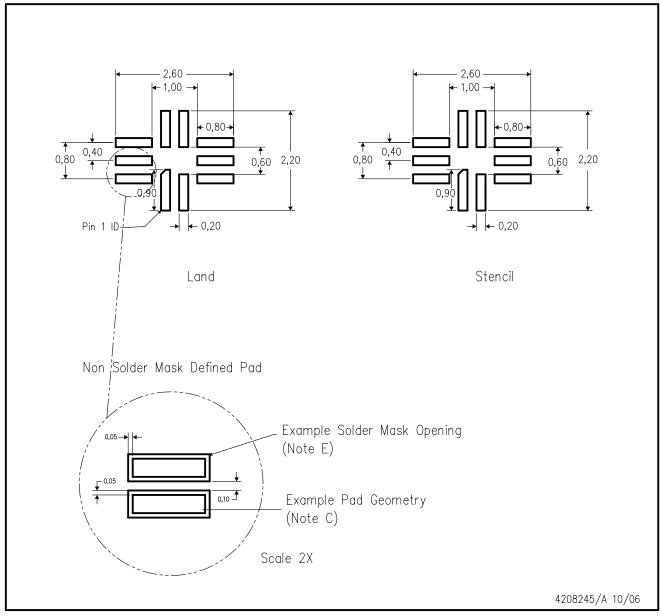


NOTES: A. All linear dimensions are in millimeters. Dimensioning and tolerancing per ASME Y14.5M-1994.

- B. This drawing is subject to change without notice.
- C. QFN (Quad Flatpack No-lead) package configuration.
- This package complies to JEDEC MO-288 variation UDEE, except minimum package height.



RSW (R-PQFP-N10)



- NOTES: A. All linear dimensions are in millimeters.
 - B. This drawing is subject to change without notice.
 - C. Publication IPC-7351 is recommended for alternate designs.
 - D. Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Refer to IPC 7525 for stencil design considerations.
 - E. Customers should contact their board fabrication site for minimum solder mask web tolerances between signal pads.



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