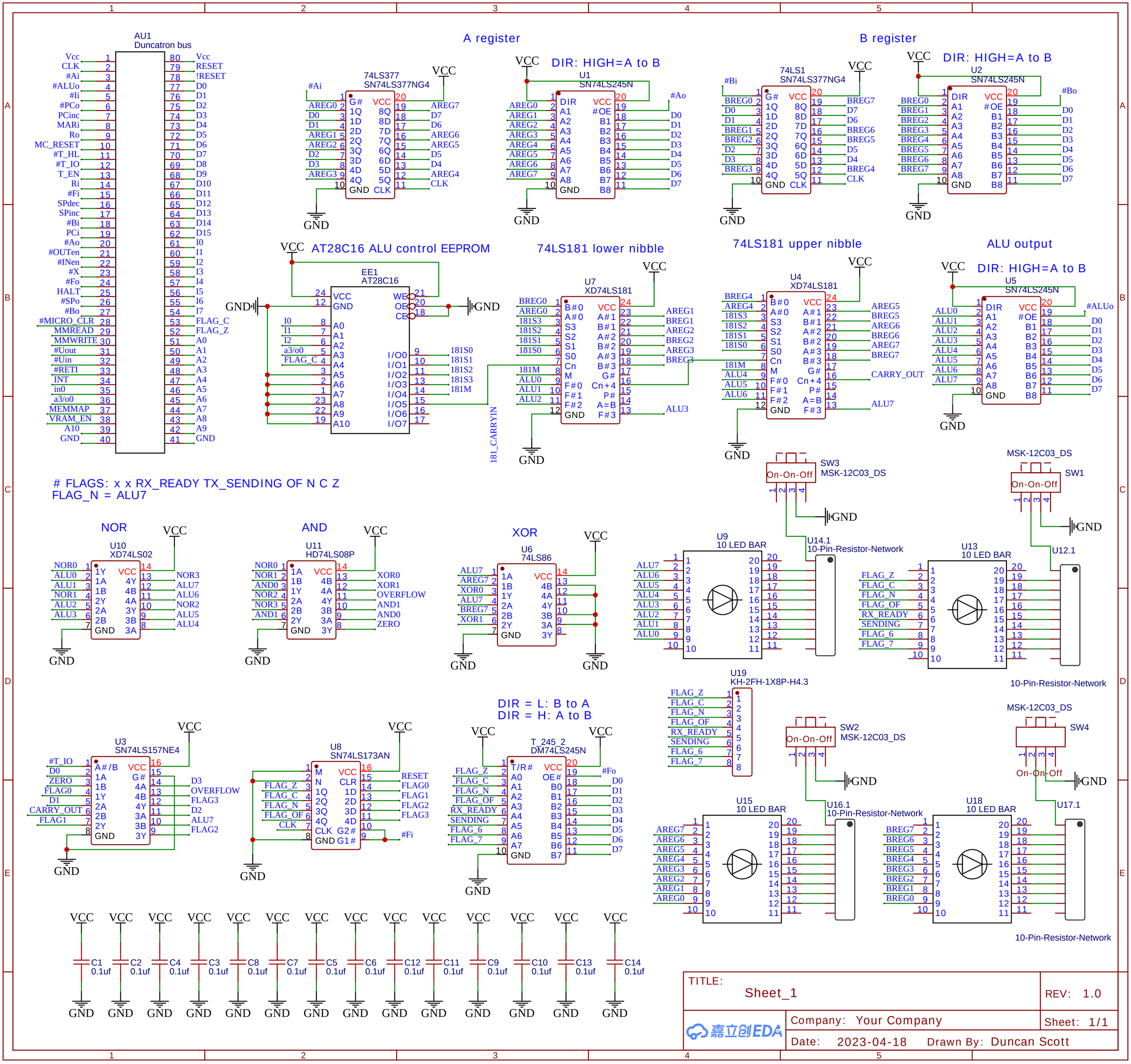
AU1
Duncatron bus
A register
B register
DIR: HIGH=A to B
74LS377
SN74LS377NG4
U1
SN74LS245N
74LS1
SN74LS377NG4
U2
SN74LS245N
AT28C16 ALU control EEPROM
EE1
AT28C16
74LS181 lower nibble
U7
XD74LS181
74LS181 upper nibble
U4
XD74LS181
ALU output
U5
SN74LS245N
# FLAGS: x x RX_READY TX_SENDING OF N C Z
FLAG_N = ALU7
NOR
U10
XD74LS02
AND
U11
HD74LS08P
XOR
U6
74LS86
SW3
MSK-12C03_DS
On-On-Off
SW1
U9
10 LED BAR
U14.1
10-Pin-Resistor-Network
U13
10 LED BAR
U12.1
10-Pin-Resistor-Network
U19
KH-2FH-1X8P-H4.3
SW2
MSK-12C03_DS
SW4
U3
SN74LS157NE4
U8
SN74LS173AN
DIR = L: B to A
DIR = H: A to B
T_245_2
DM74LS245N
U15
10 LED BAR
U16.1
10-Pin-Resistor-Network
U18
10 LED BAR
U17.1
10-Pin-Resistor-Network
C1 0.1uf
C2 0.1uf
C4 0.1uf
C3 0.1uf
C8 0.1uf
C7 0.1uf
C5 0.1uf
C6 0.1uf
C12 0.1uf
C11 0.1uf
C9 0.1uf
C10 0.1uf
C13 0.1uf
C14 0.1uf
VCC
GND
TITLE:
Sheet_1
REV: 1.0
Company: Your Company
Sheet: 1/1
Date: 2023-04-18
Drawn By: Duncan Scott