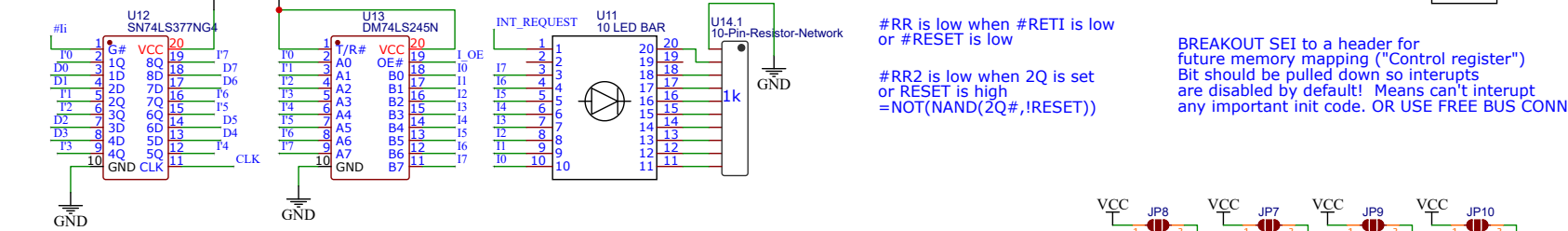
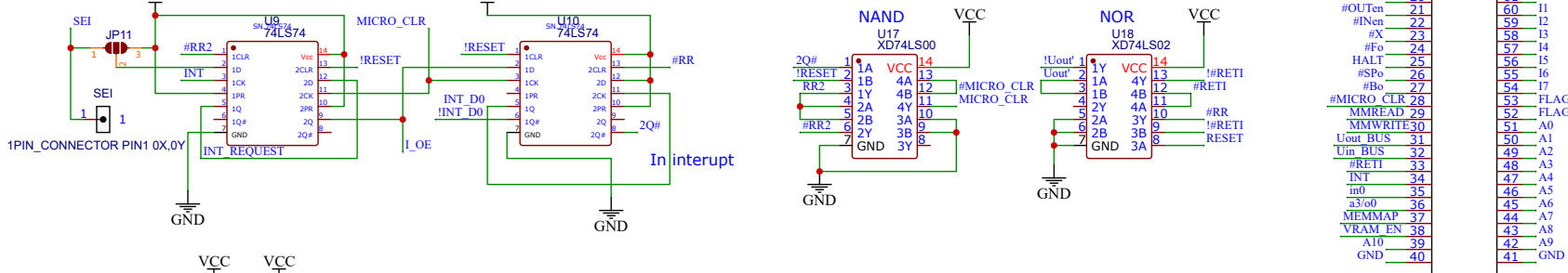


A8/9/10 is not output onto bus in v1.0 of memory map
 Could use a wire on backplane from SEI pin to A8/9
 or frontplane to A10



This board implements six general purpose hardware registers r0-r6
 The instruction register (and interrupt logic)
 Interrupts run PUSH_PC+1 and INT in sequence. Opcode can be hardwired with solder jumpers
 Transfer register also provided for moving data between hi and low 8-bit buses.
 Can also perform a SHIFT RIGHT if X is enabled (HIGH) and LOAD is HIGH
 (T_EN=1 and #T_IO=0)
 Possess LED bank instruction register (not output selection?)

TITLE: Duncatron register module		REV: 1.0
Company: Your Company		Sheet: 1/1
Date: 2024-01-03	Drawn By: Duncan	