

# Topic V0B

Procedure Calls, Stack,  
and Memory Layout

Readings: (Section 2.8)

A word about naming...

# Callable Unit

Procedure

Function ← “Pure functions” have no side effect  
See “functional programming languages”

Routine

Subroutine

Subprogram

Method ← Methods are usually associated with objects  
See “object-oriented programming languages”

# Procedure Calls

```
1: void foo()  
2: {  
3:   int a, b;  
4:   scanf("%d %d", &a, &b);  
5:   ...  
6:   r = bar(a, b);  
7:   t = 2 * r;  
8:   ...  
9:   r = bar(b, a);  
10:  c = c + 4 * r;  
11:  ...  
12: }
```

```
1: int bar(int x, int y)  
2: {  
3:   int i, p, t;  
4:   t = 1;  
5:   for(i = 0 ; i < y ; i++){  
6:       t = baz(x);  
7:       p = p * t;  
8:   }  
9:   return p;  
10: }
```

```
1: int baz(int d)  
2: {  
3:   int z;  
4:   z = 100 - d;  
5:   return z;  
6: }
```

# Procedure Calls

caller

```
1: void foo()  
2: {  
3:   int a, b;  
4:   scanf("%d %d", &a, &b);  
5:   ...  
6:   r = bar(a, b);  
7:   t = 2 * r;  
8:   ...  
9:   r = bar(b, a);  
10:  c = c + 4 * r;  
11:  ...  
12: }
```

must remember  
return address

callee

```
1: int bar(int x, int y)  
2: {  
3:   int i, p, t;  
4:   t = 1;  
5:   for(i = 0 ; i < y ; i++){  
6:     t = baz(x);  
7:     p = p * t;  
8:   }  
9:   return p;  
10: }
```

callee

```
1: int baz(int d)  
2: {  
3:   int z;  
4:   z = 100 - d;  
5:   return z;  
6: }
```

return address

return address

must return to correct  
return address

return address

# Problem

In RISC-V there is a single register, ra, to store the return address

```
1: void foo()
2: {
3:   int a, b;
4:   scanf("%d %d", &a, &b);
5:   ...
6:   r = bar(a, b);
7:   t = 2 * r;
8:   ...
9:   r = bar(b, a);
10:  c = c + 4 * r;
11:  ...
12: }
```

```
1: int bar(int x, int y)
2: {
3:   int i, p, t;
4:   t = 1;
5:   for(i = 0 ; i < y ; i++){
6:       t = baz(x);
7:       p = p * t;
8:   }
9:   return p;
10: }
```

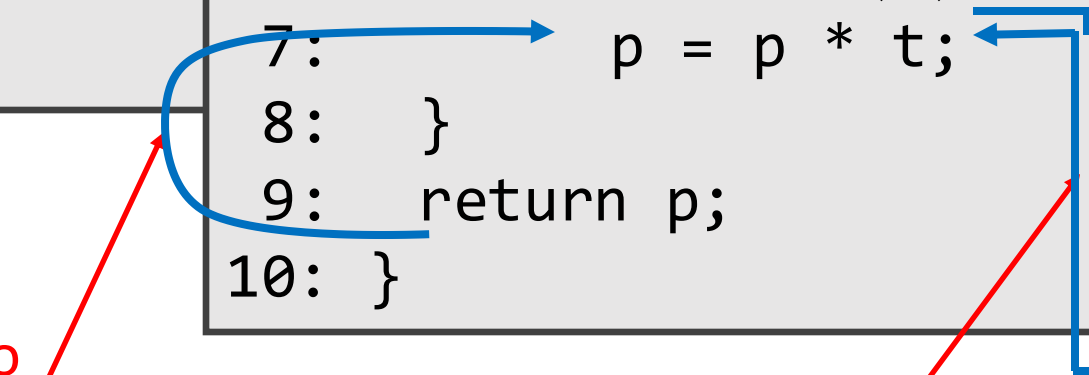
```
1: int baz(int d)
2: {
3:   int z;
4:   z = 100 - d;
5:   return z;
6: }
```

write return  
address into ra

overwrite return  
address into ra

return to  
wrong address

return to  
correct address



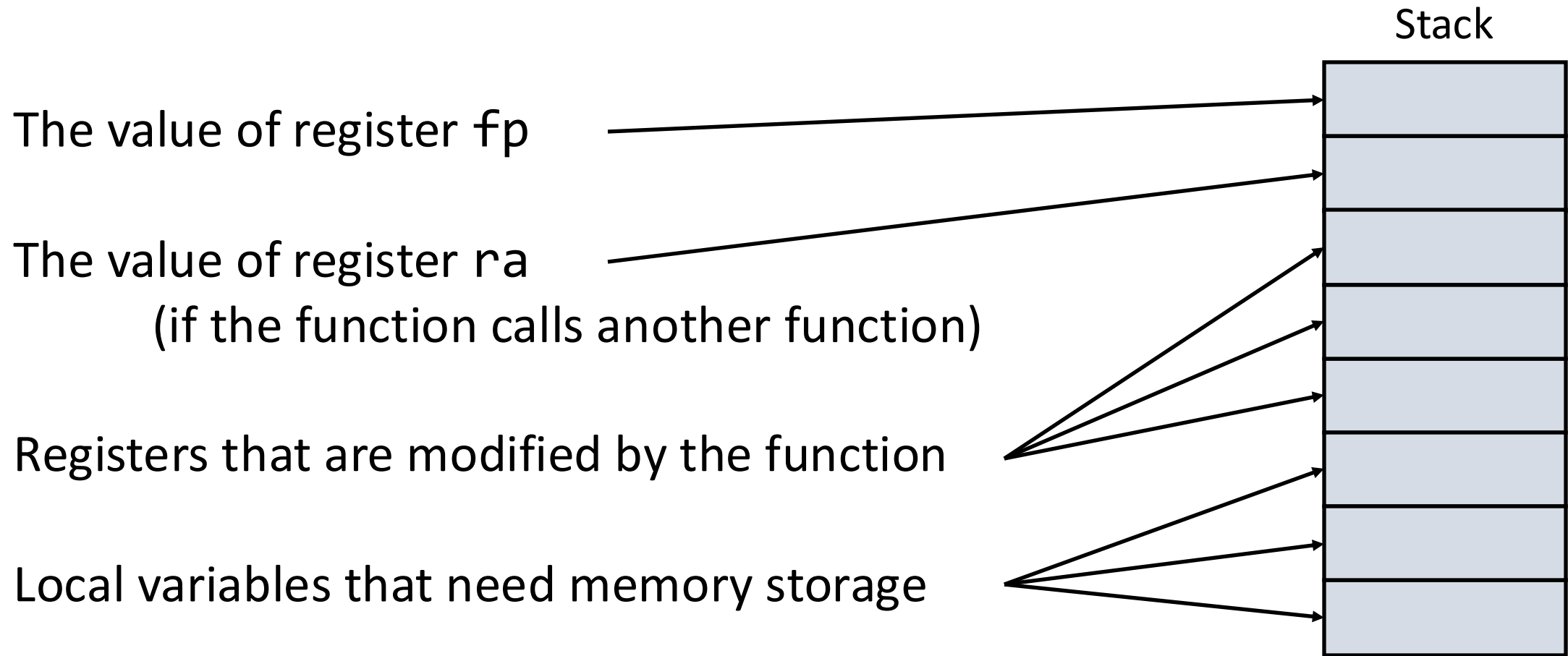
# Solution

We need to save the value of `ra` somewhere

Where?

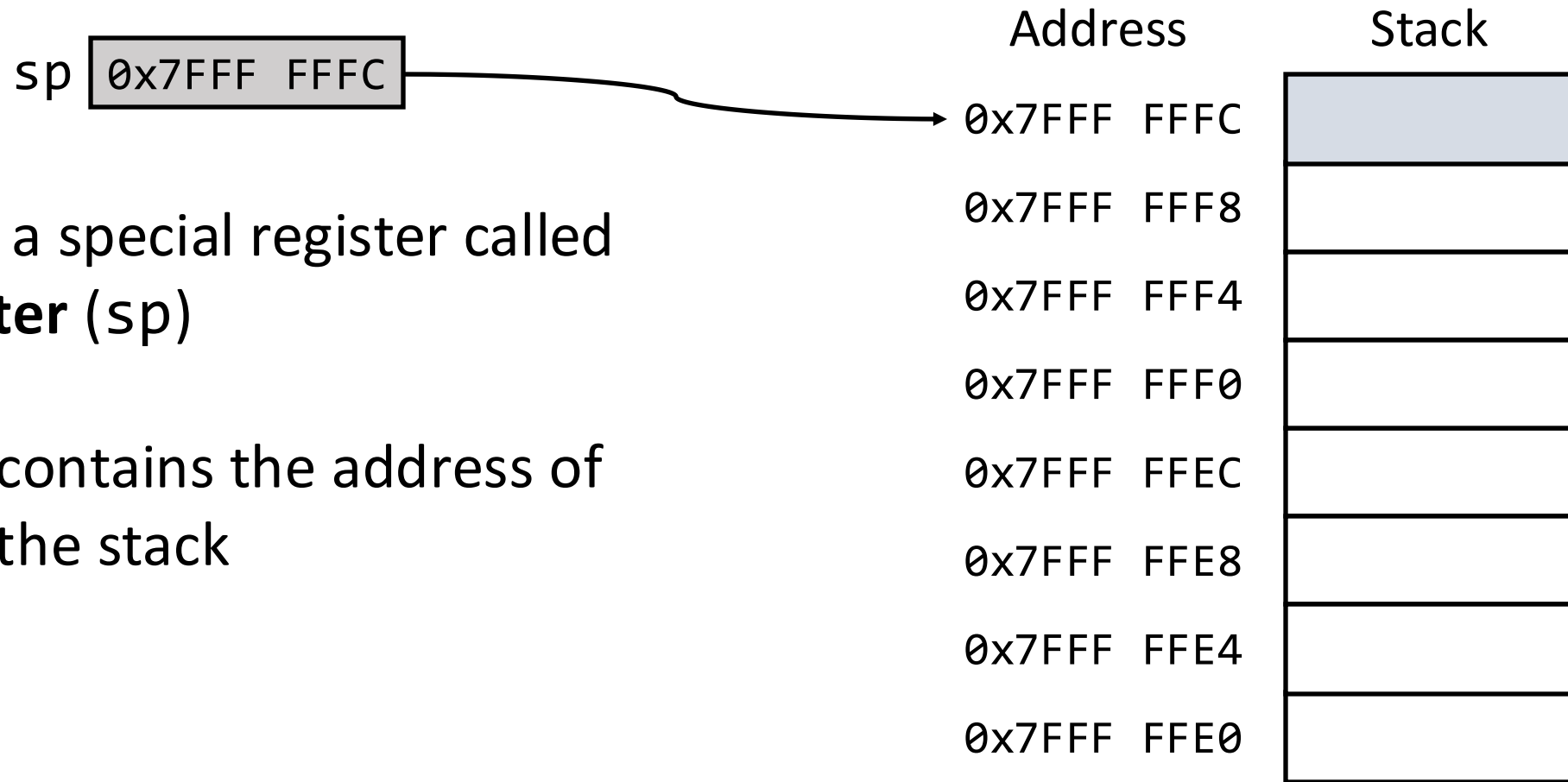
Into a stack

# What Goes Into the Stack





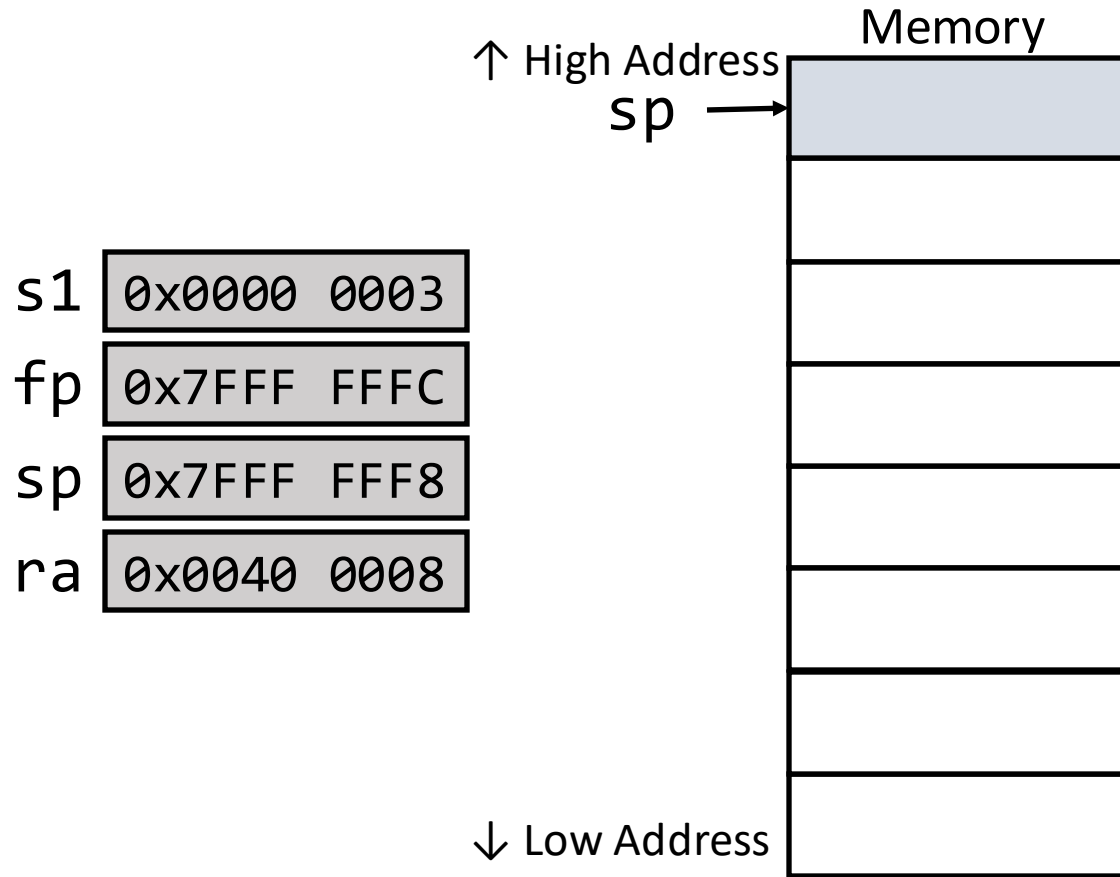
# Where is the Top of the Stack?



RISC-V has a special register called **stack pointer (sp)**

sp always contains the address of the top of the stack

# Saving Registers Into the Stack



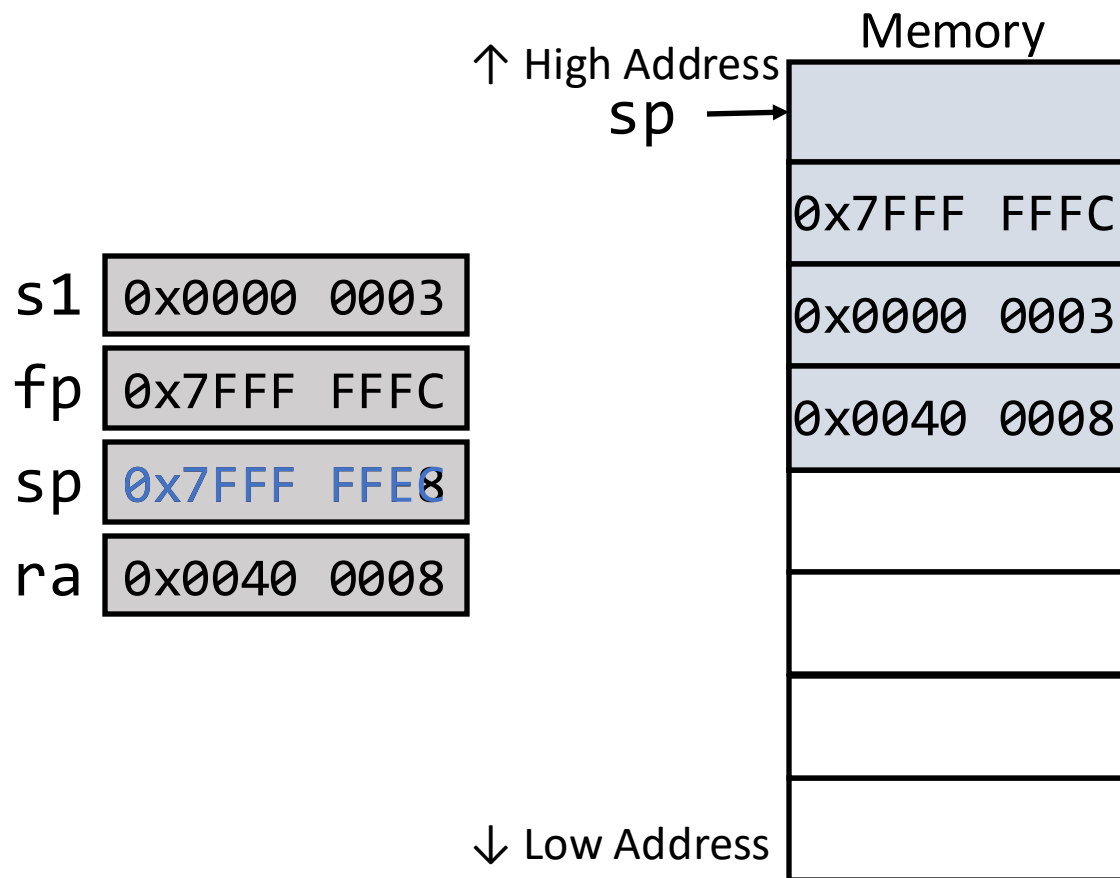
The stack grows towards lower memory addresses

Situation: a procedure will write on s1 and call another subroutine

Must save values of s1, fp, and ra into the stack

How do we make room in the stack for these three registers?

# Saving Registers Into the Stack



We need to decrement the value of sp by 12

How do we actually 'save' s1, fp, and ra?

Write their values, using the sw instruction, to the stack:

```
sw    s1, 4(sp)
```

# Procedure Calls

In RISC-V there is a single register, ra, to store the return address

```
1: void foo()
2: {
3:   int a, b;
4:   scanf("%d %d", &a, &b);
5:   ...
6:   r = bar(a, b);
7:   t = 2 * r;
8:   ...
9:   r = bar(b, a);
10:  c = c + 4 * r;
11:  ...
12: }
```

return to  
correct address

restore ra  
from stack

write return  
address into ra

save ra  
into stack

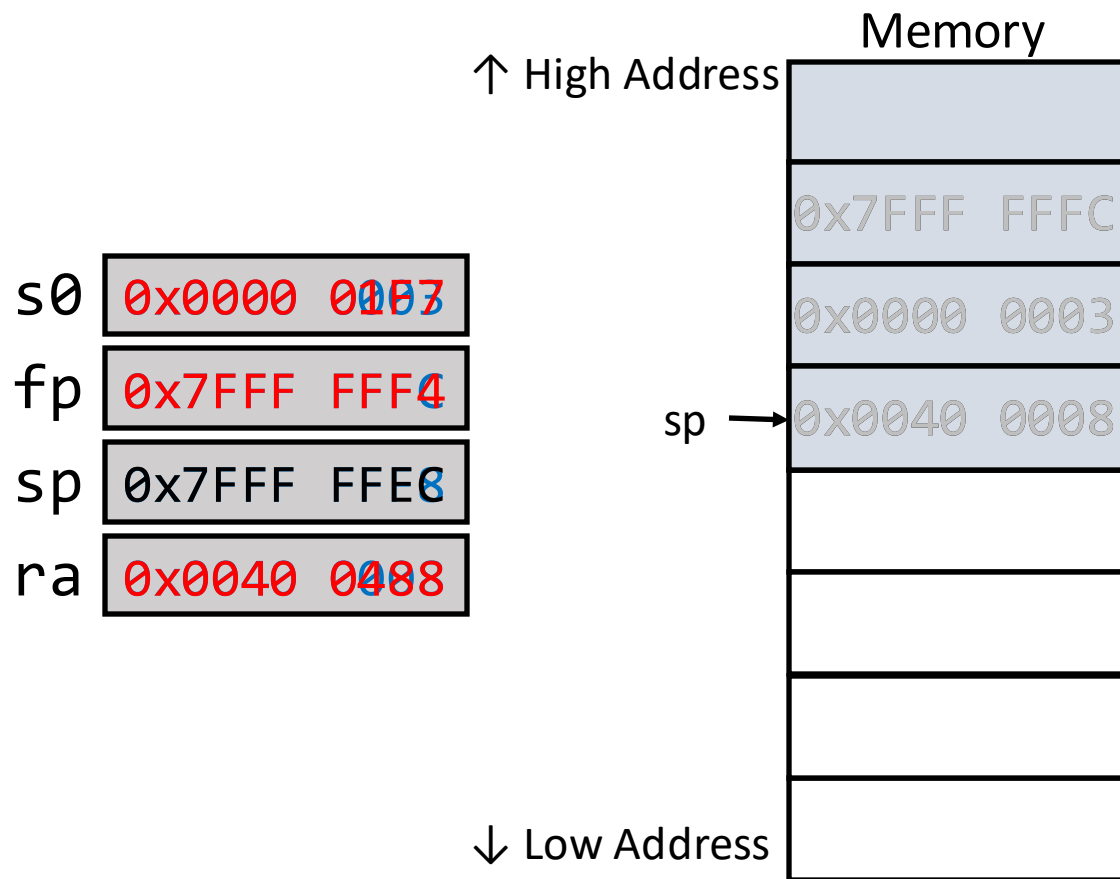
```
1: int bar(int x, int y)
2: {
3:   int i, p, t;
4:   t = 1;
5:   for(i = 0 ; i < x ; i++){
6:     t = baz(x);
7:     p = p * t;
8:   }
9:   return p;
10: }
```

return to  
correct address

safe to  
overwrite return  
address into ra

```
1: int baz(int d)
2: {
3:   int z;
4:   z = 100 - d;
5:   return z;
6: }
```

# Saving Registers Into the Stack



Now that the procedure has completed its execution it is time to return to the caller

How do we 'restore' the value of s0, fp, and ra?

Read their values, using the lw instruction, from the stack

Do we need to do anything to the value of sp?

Yes, we need to increment the value of sp by 12

# Memory Layout

Text: program code (instructions)

Static data: global variables

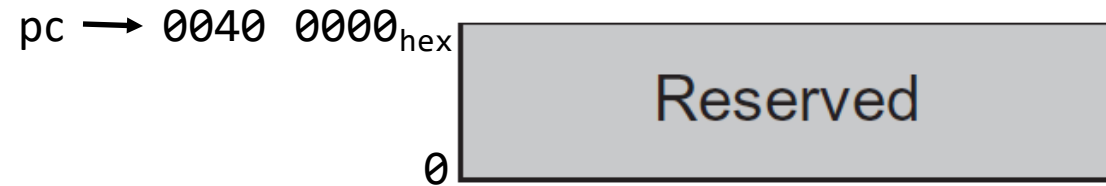
e.g. variables in C, constant arrays  
and strings

gp initialized to address allowing  $\pm$   
offsets into this segment

Dynamic data: heap

e.g. malloc in C, new in Java

Stack: automatic storage



# Memory Layout

Text: program code (instructions)

sp → 7FFF FFFC<sub>hex</sub>

Static data: global variables

e.g. variables in C, constant arrays  
and strings

gp initialized to address allowing  $\pm$   
offsets into this segment

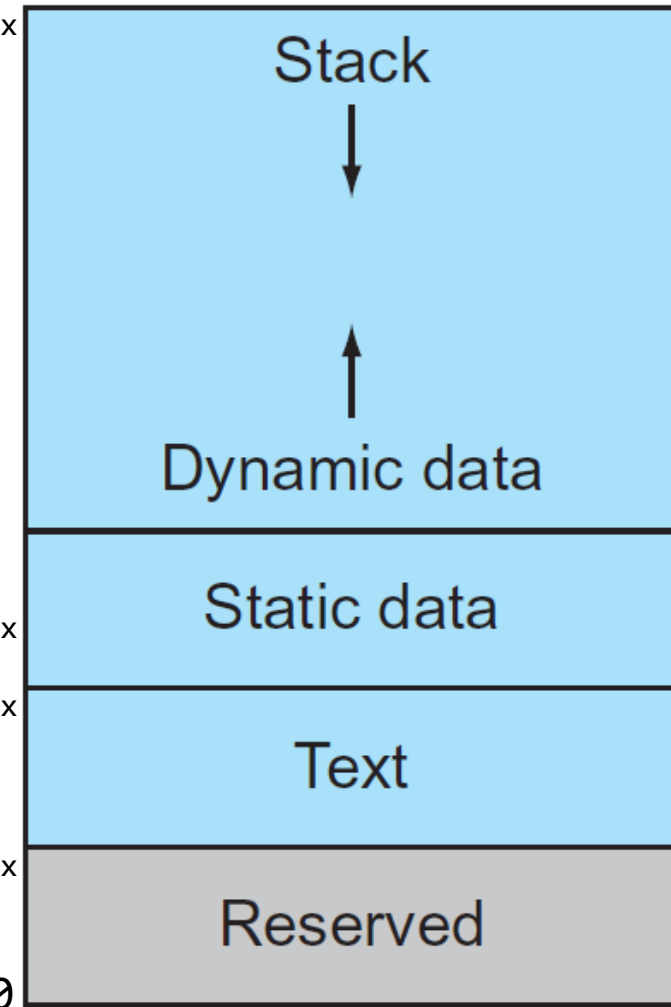
gp → 1000 8000<sub>hex</sub>  
1000 0000<sub>hex</sub>

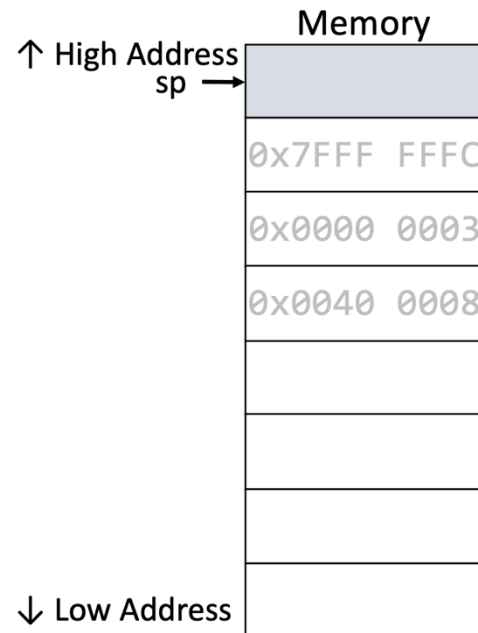
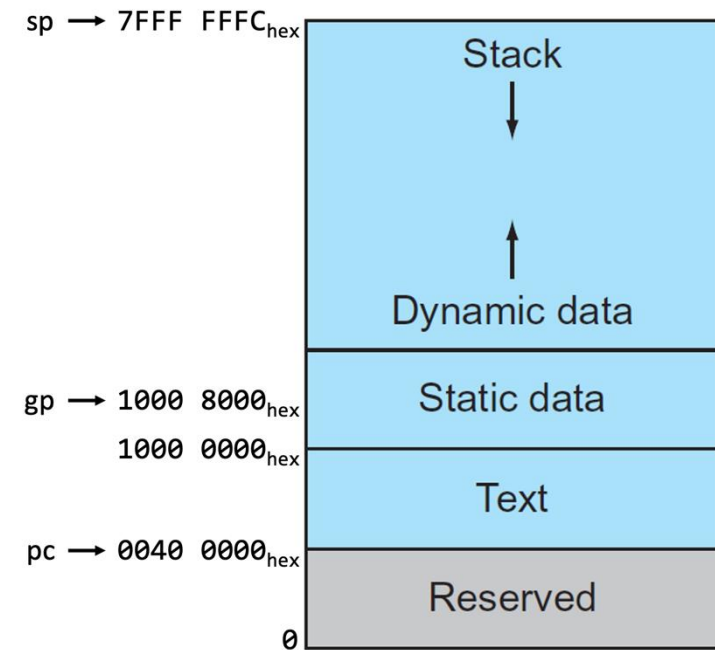
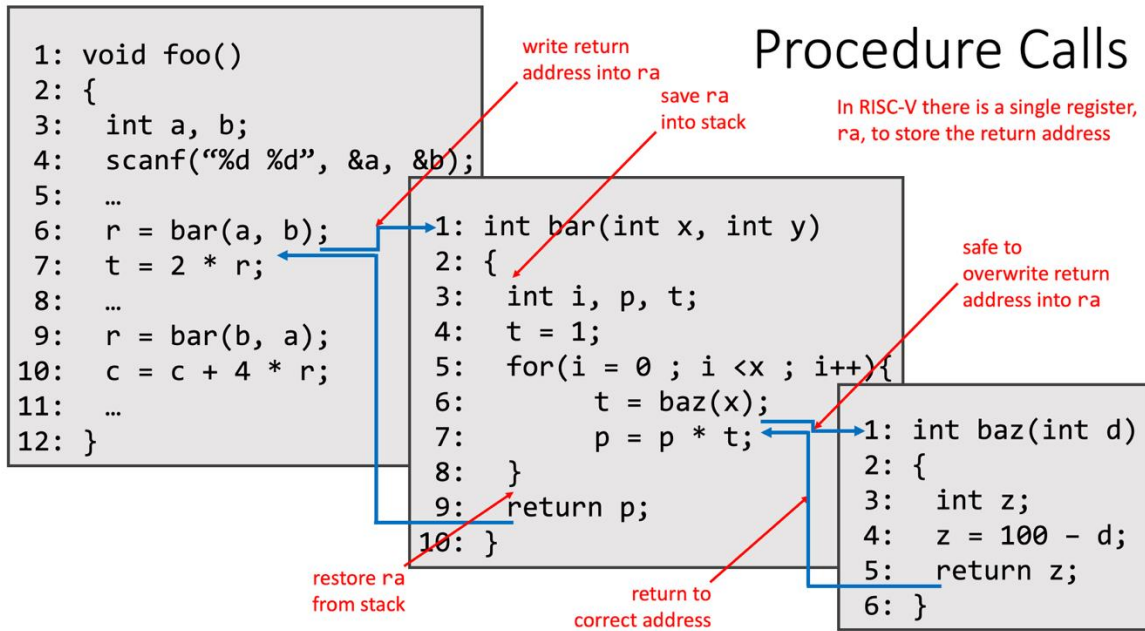
Dynamic data: heap

e.g. malloc in C, new in Java

pc → 0040 0000<sub>hex</sub>

Stack: automatic storage





# Recap