

Lab-4 Report

K-Map for 3 variables

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Aim: To design an optimal circuit for the given problem and implement them using the both 2 input logic gates and only NAND gates by using techniques of K-Map for 3 variables for simplifying the boolean expression.

Components used:

1. IC 7404(NOT)
2. IC 7408(AND)
3. IC 7432(OR)
4. IC 7400(NAND)
5. Resistor array
6. DIP switches
7. LED displays
8. Breadboard
9. Power supply
10. Multimeter

Design Procedure & Circuit diagram:

Problem Statements:

An industrial unit rings an emergency alarm based on the sensor output from the pressure and temperature units . In addition there is also a water level indicator which turns on this alarm. The alarm rings for the following conditions,

- i. If the water level is high when the other sensors are off.
- ii. If the temperature sensor is active and the water level indicator is high . iii. Both pressure and temperature sensors are active.

A	B	C	Output
0	0	0	0
0	0	1	1
0	1	0	0
0	1	1	1
1	0	0	0
1	0	1	0
1	1	0	1
1	1	1	1

$A \rightarrow$ Pressure
 $B \rightarrow$ Temperature
 $C \rightarrow$ Water level

By using KMAP of 3 variables

	$\bar{B}\bar{C}$	$\bar{B}C$	BC	$B\bar{C}$
\bar{A}	0	1	1	0
A	1	0	1	1

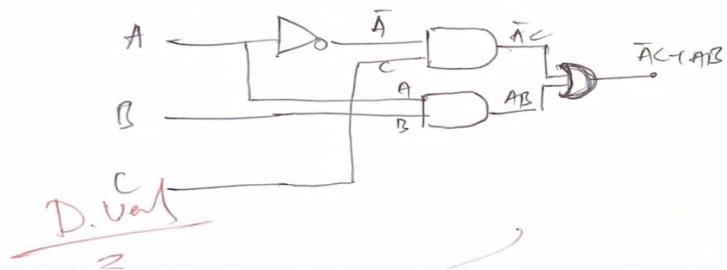
So, simplified boolean expression is

a) $\bar{A}C + AB$

b) $\bar{A}C + AB = \overline{\bar{A}C + AB} = \overline{(\bar{A}C) \cdot (\bar{A}B)}$

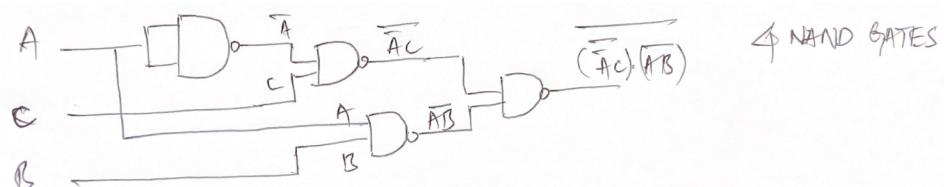
a. Design a digital circuit with basic gates(AND, OR, NOT) that controls the buzzer action for the above conditions.

Solution:



b. Repeat using a minimum number of 2 input NAND gates.

Solution:



Note: Choose pressure sensor output to be the MSB bit and water level indicator to be the LSB bit.

These Logic expressions gates can be implemented by using IC gates. The logic tables/Truth tables for these gates are:

AND GATE

INPUT 1	INPUT 2	OUTPUT
0	0	0
0	1	0
1	0	0
1	1	1

NOT GATE

INPUT	OUTPUT
0	1
1	0

NAND GATE

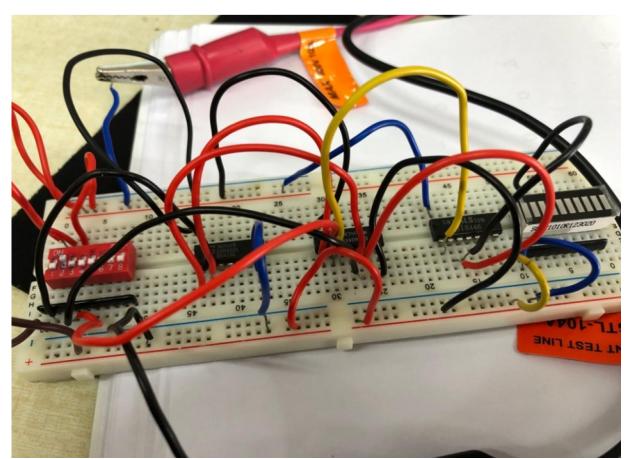
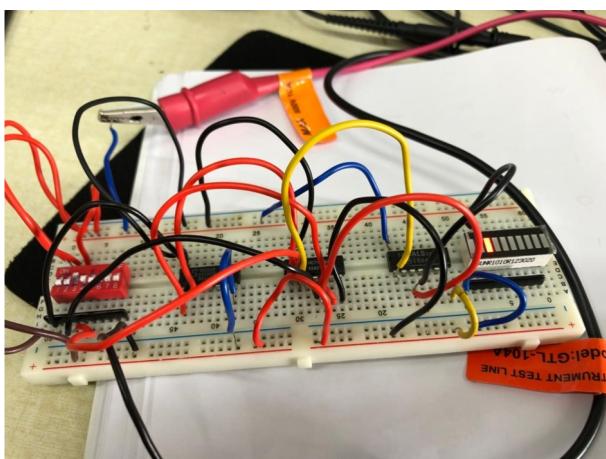
INPUT 1	INPUT 2	OUTPUT
0	0	1
0	1	1
1	0	1
1	1	0

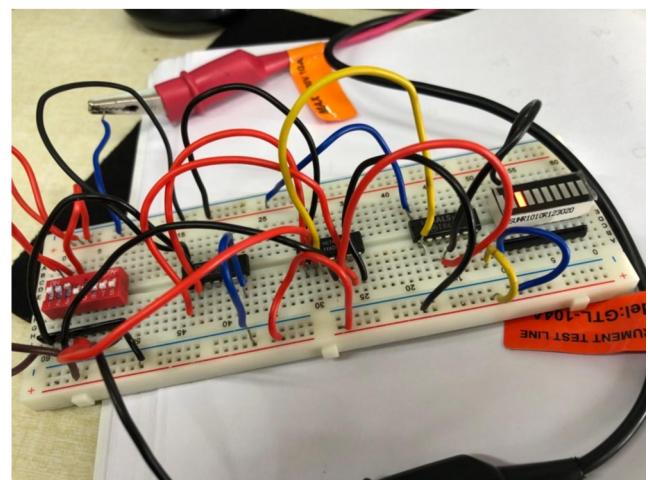
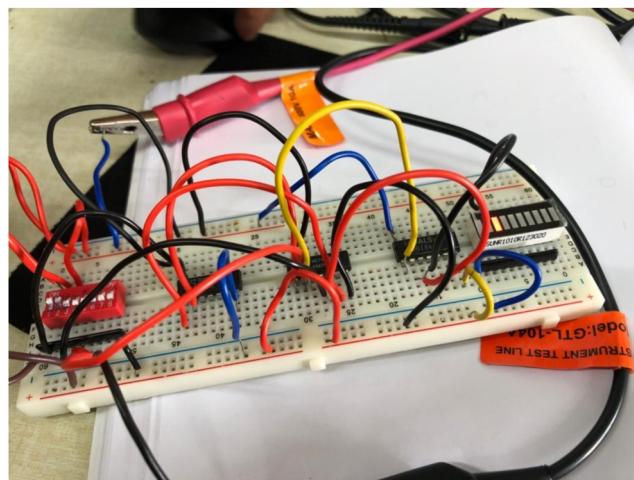
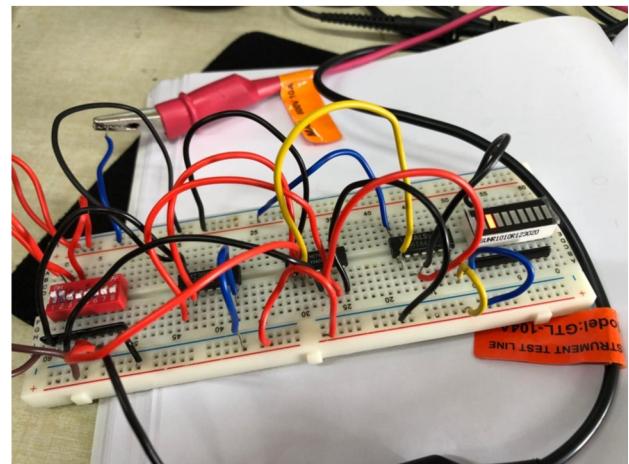
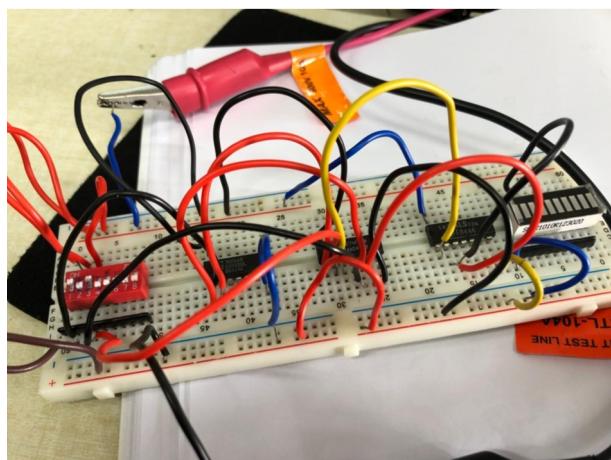
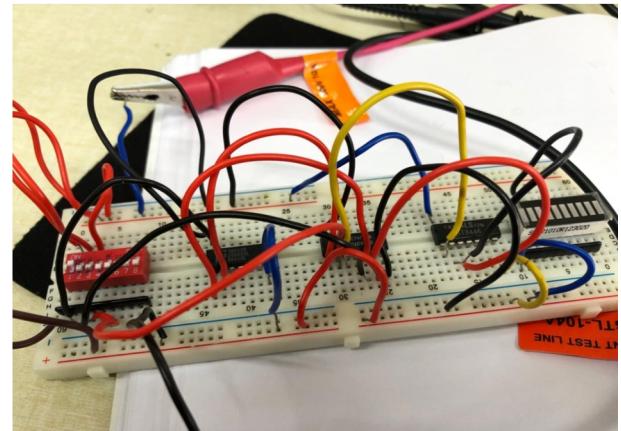
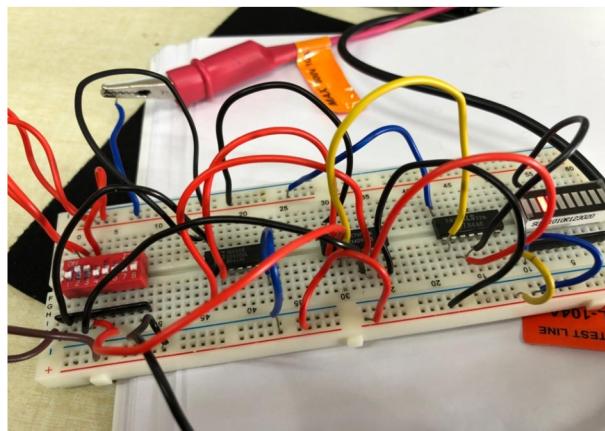
OR GATE

INPUT 1	INPUT 2	OUTPUT
0	0	0
0	1	1
1	0	1
1	1	1

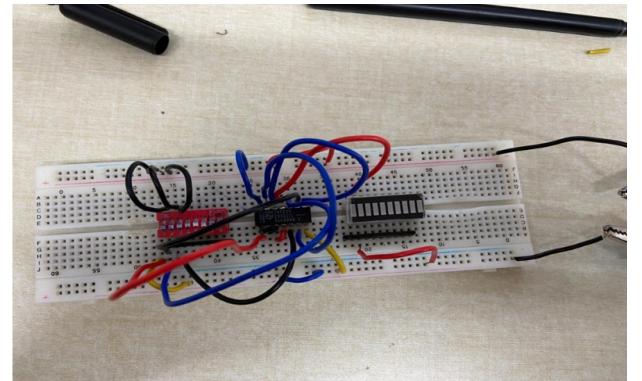
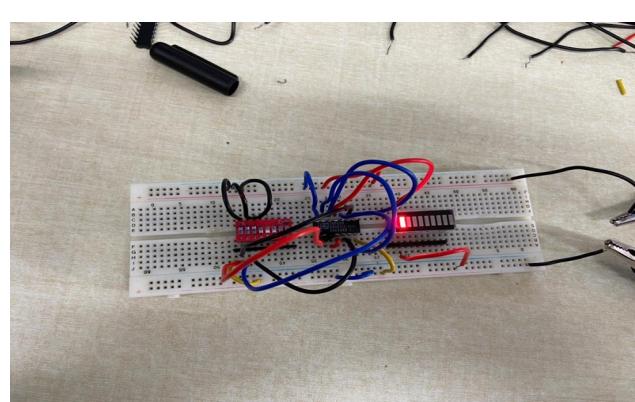
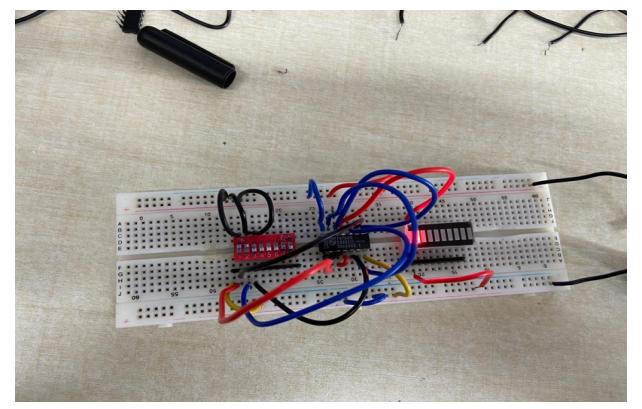
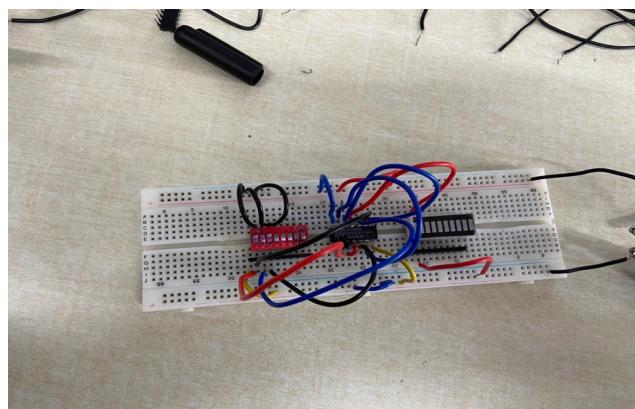
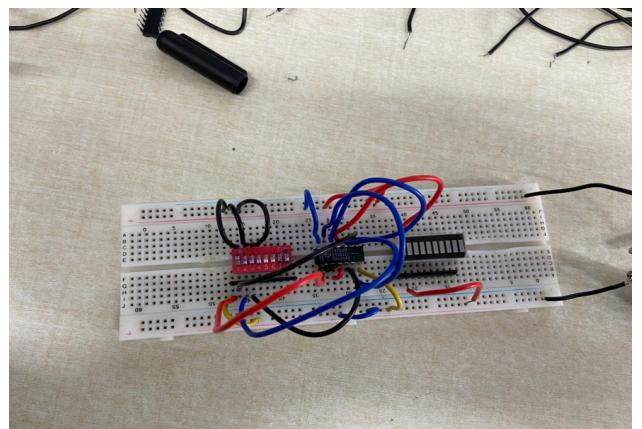
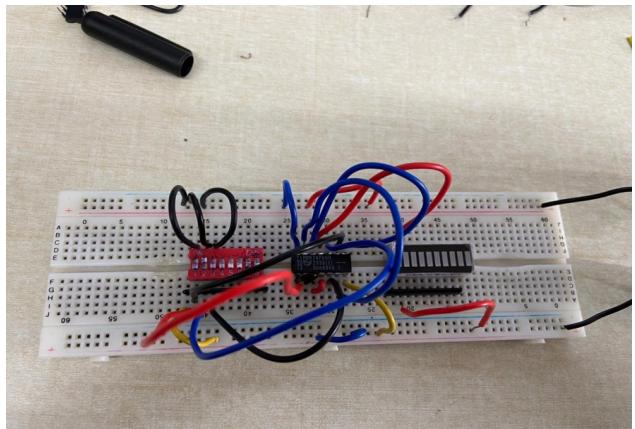
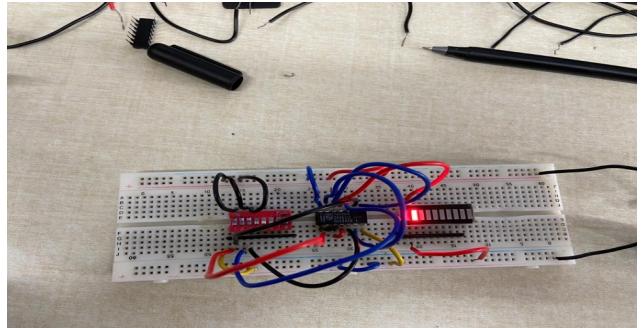
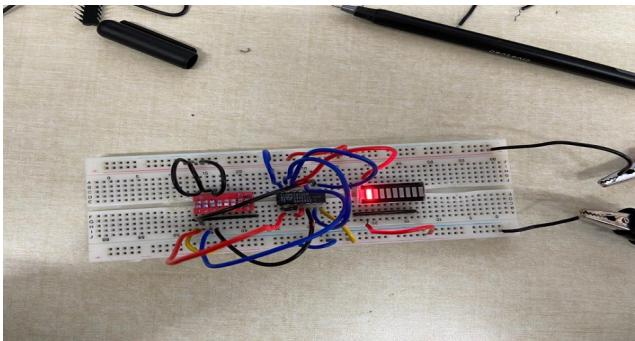
Circuit Snapshots:

Design a digital circuit with basic gates(AND, OR, NOT) that controls the buzzer action for the above conditions.





Repeat using a minimum number of 2 input NAND gates.



Results and Discussions:

The snapshots corresponding to each of the gates exactly represents the required logic hence we have implemented the solution using both 2 input basic logic gates and only NAND gates.

Conclusion:

We have designed the optimal circuit for the given problem and implemented it using the both 2 input logic gates and only NAND gates.