



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
HYDERABAD - 500 085, ANDHRA PRADESH, INDIA

CONSOLIDATED MARKS MEMO / CREDIT SHEET



BACHELOR OF TECHNOLOGY- COMPUTER SCIENCE & ENGINEERING

CMM. No. : **C 0327325**

Serial No. : 21073000984

Name : GUJARATHI SHASHANK

Name of the College : 61-P R R M E C, SHABAD

Month & Year of Final Exam : May, 2013

Hall Ticket No. : 09611A0532

Year of Admission : 2009-2010

Class Awarded : FIRST CLASS

S.No.	SUBJECT TITLE	INT MARKS	EXT MARKS	TOTAL	CREDITS	S.No.	SUBJECT TITLE	INT MARKS	EXT MARKS	TOTAL	CREDITS
	Maximum Marks in Theory	25	75	100			Maximum Marks in Lab	25	50	75	

I YEAR

1	ENGLISH	22	43	65	4	2	MATHEMATICS - I	15	26	41	6
3	MATHEMATICAL METHODS	16	33	49	6	4	ENGINEERING PHYSICS	21	42	63	4
5	ENGINEERING CHEMISTRY	23	46	69	4	6	COMPUTER PROGRAMMING & DATA STRUCTURES	20	52	72	6
7	ENGINEERING DRAWING	24	39	63	4	8	COMPUTER PROGRAMMING LAB	20	44	64	4
9	ENGG. PHYSICS & ENGG. CHEMISTRY LAB	24	49	73	4	10	ENGLISH LANGUAGE COMMUNICATION SKILLS LAB	22	46	68	4
11	IT WORKSHOP & ENGINEERING WORKSHOP	19	42	61	4						

I SEMESTER

II YEAR

II SEMESTER

1	PROBABILITY AND STATISTICS	15	48	63	3	1	COMPUTER ORGANIZATION	18	30	48	4
2	MATHEMATICAL FOUNDATION OF COMPUTER SCIENCE	21	41	62	3	2	DATA BASE MANAGEMENT SYSTEMS	18	28	46	4
3	DATA STRUCTURES THROUGH C++	22	27	49	4	3	OBJECT ORIENTED PROGRAMMING	16	33	49	4
4	DIGITAL LOGIC DESIGN	23	30	53	4	4	ENVIRONMENTAL STUDIES	19	47	66	3
5	ELECTRONIC DEVICES & CIRCUITS	20	26	46	4	5	FORMAL LANGUAGES & AUTOMATA THEORY	19	26	45*	3
6	BASIC ELECTRICAL ENGG	17	26	43*	3	6	DESIGN & ANALYSIS OF ALGORITHMS	22	26	48	3
7	ELECTRICAL AND ELECTRONICS LAB	18	47	65	2	7	OBJECT ORIENTED PROGRAMMING LAB	16	44	60	2
8	DATA STRUCTURES LAB THROUGH C++	17	43	60	2	8	DATA BASE MANAGEMENT SYSTEMS LAB	21	45	66	2

I SEMESTER

III YEAR

II SEMESTER

1	PRINCIPLES OF PROGRAMMING LANGUAGES	18	64	82	4	1	OBJECT ORIENTED ANALYSIS AND DESIGN	17	34	51	4
2	OPERATIONS RESEARCH	22	41	63	3	2	VLSI DESIGN	22	39	61	3
3	SOFTWARE ENGINEERING	21	48	69	3	3	NETWORK SECURITY	17	37	54	3
4	MICROPROCESSORS & INTERFACING	22	37	59	3	4	COMPILER DESIGN	19	53	72	4
5	OPERATING SYSTEMS	22	42	64	4	5	MANAGERIAL ECONOMICS & FINANCIAL ANALYSIS	20	50	70	4
6	DATA COMMUNICATION & COMPUTER NETWORKS	22	44	66	4	6	WEB TECHNOLOGIES	18	47	65	3
7	MICROPROCESSORS & INTERFACING (LAB)	21	47	68	2	7	ADVANCED ENGLISH COMMUNICATION SKILLS (LAB)	20	42	62	2
8	COMPUTER NETWORKS & OPERATING SYSTEMS (LAB)	21	40	61	2	8	WEB TECHNOLOGIES & COMPILER DESIGN (LAB)	18	45	63	2

I SEMESTER

IV YEAR

II SEMESTER

1	LINUX PROGRAMMING	21	29	50	4	1	MANAGEMENT SCIENCE	22	44	66	3
2	SOFTWARE TESTING METHODOLOGIES	20	35	55	4	2	WEB SERVICES	17	50	67	3
3	DATA WAREHOUSING AND DATA MINING	20	66	86	4	3	STORAGE AREA NETWORKS	22	53	75	3
4	COMPUTER GRAPHICS	21	35	56	3	4	INDUSTRY ORIENTED MINI PROJECT	-	43	43	2
5	ADVANCED COMPUTER ARCHITECTURE	19	57	76	3	5	SEMINAR	44	-	44	2
6	DESIGN PATTERNS	16	53	69	3	6	PROJECT WORK	42	140	182	10
7	LINUX PROGRAMMING AND DATA MINING LAB	23	47	70	2	7	COMPREHENSIVE VIVA	-	86	86	2
8	CASE TOOLS AND SOFTWARE TESTING LAB	23	47	70	2						

(# Project Internal= 50, External=150)

Number of Credits registered for : 200 Aggregate Marks Secured for best: 194

Aggregate Marks Secured : 3594 OUT OF 5250 (68.46%)

Date of Issue : July 17, 2013

(see overleaf for Rules concerned to award of class)

A indicates 'ABSENT'

(*Courses registered but not counted for calculation of aggregate)



CONTROLLER OF EXAMINATIONS