## Massachusetts Institute of Technology Department of Electrical Engineering and Computer Science

# Proposal for Thesis Research in Partial Fulfillment of the Requirements for the Degree of Doctor of Philosophy

Title: Parallel Processor Architecture

SUBMITTED BY: Peter Nuth

305 Memorial Drive, 606C Cambridge, MA 02139

(Signature of Author)

Date of Submission: May 23, 2016
Expected Date of Completion: September 1990

Laboratory: Artificial Intelligence Laboratory

#### Brief Statement of the Problem:

The proposed research is a study of processor architecture for large scale parallel computer systems. The thesis introduces mechanisms for fast context switching, synchronization between tasks, and run-time binding of variable names to processor memory. Various design tradeoffs are evaluated through simulation of a processor running a typical load. This work contains estimates of the speed and complexity of the different alternatives as implemented in VLSI.

#### **Doctoral Thesis Supervision Agreement**

Professor William J. Dally

From:

Department Graduate Committee

The program outlined in the proposal:			
TITLE: Parallel Processor Architecture AUTHOR: Peter Nuth DATE: May 23, 2016			
is adequate for a Doctoral thesis. I believe that thesis would be:	appropriate readers for this		
Reader 1: Professor Arvind Reader 2: Professor Thomas Knight			
Facilities and support for the research outlined in the proposal are available. I am willing to supervise the thesis and evaluate the thesis report.			
Signed:			
	Associate Professor of Electrical Engineering and Computer Science		
Date:			
Comments:			

#### Doctoral Thesis Reader Agreement

Department Graduate Committee

From: Professo	r Arvind	
The program outli	ned in the proposal:	
TITLE: AUTHOR: DATE: SUPERVISOR: OTHER READER:	Parallel Processor Architect Peter Nuth May 23, 2016 Professor William J. Dally Professor Thomas Knight	eture
	Octoral thesis. I am willing thesis report as a reader.	to aid in guiding the research
	Signed:	Professor of Electrical Engineering and Computer Science
	DATE:	
Comments:		

### Doctoral Thesis Reader Agreement

	nent Graduate Committee or Thomas Knight	
The program outli	ned in the proposal:	
DATE: Supervisor:	Parallel Processor Architec Peter Nuth May 23, 2016 Professor William J. Dally Professor Arvind	ture
	Octoral thesis. I am willing thesis report as a reader.	o aid in guiding the research
	Signed:	Assistant Professor of Electrical Engineering and Computer Science
	DATE:	
Comments:		

#### Doctoral Thesis Reader Agreement

To: Departn	nent Graduate Committee			
From: Professo	or William J. Dally			
The program outli	ined in the proposal:			
Supervisor: Other Reader:	Parallel Processor Architec Peter Nuth May 23, 2016 Professor William J. Dally Professor Arvind Professor Thomas Knight	ture		
is adequate for a Doctoral thesis. I am willing to aid in guiding the research and in evaluating the thesis report as a reader.				
	Signed:	Associate Professor of Electrical Engineering		
		AND COMPUTER SCIENCE		
	DATE:			
Comments:				