East West University
Department of Computer Science and Engineering
Course Title: Computer Architecture, Course code: CSE360(3)
Summer 2021, Quiz 2, Total Mark: 10, Time: 15 Minutes, Set A
Instructor: Dr. Md. Nawab Yousuf Ali

Answer all of the following questions.   Total Marks: 10

Name: Id: Sec: Roll:

		Marks
Q1.	Memory access time is the time it takes to position the read-write mechanism at the desired location for a) Cache memory b) Read only memory c) Random access memory d) None of them	1
Q2.	Calculate the memory access time when cycle time is 520ms and recovery is 108.25ms.	1
Q3.	Calculate the average time to read or write 100000 bits of data, when the data transfer rate is 10000 bps, and average access time is 50 second.	3
Q4.	When a cache miss occurs the data transfer takes place between a) Cache memory and CPU b) Cache memory and Main Memory c) CPU and Main Memory d) None of them	1
Q5.	When a cache hit occurs the CPU retrieves data from a) Cache memory b) RAM c) Hard disk d) ROM	1
Q6.	Calculate hit ratio when miss ratio is 0.31?	1
Q7.	Which of the following uses capacitors to store data? a) SRAM b) DRAM c) PROM d) EPROM	1
Q8.	Data is written without erasing the whole contents on a) PROM b) Flash memory c) EEPROM d) EPROM	1