

East West University

Department of Computer Science and Engineering

Course Title: Computer Architecture, Course code: CSE360(3)

Summer 2021, Quiz 2, Total Mark: 10, Time: 15 Minutes, Set B

Instructor: Dr. Md. Nawab Yousuf Ali

Answer all of the following questions.

Total Marks: 10

Name:

Id:

Sec:

Roll:

		Marks
Q1.	Calculate the memory access time when cycle time is 20ms and recovery is 18.25ms.	1
Q2.	What is the data transfer rate of CPU in bps when average time to read or write 10000 bits is 200S, and average access time is 100S?	3
Q3.	When a cache miss occurs the CPU retrieves data from a) Cache memory b) RAM c) Hard disk d) ROM	1
Q4.	Data and address buffers are disabled when the CPU retrieves data from a) Hard disk b) ROM c) Main memory d) Cache memory	1
Q5.	Calculate miss ratio when hit ratio is 0.67?	1
Q6.	Which of the following is used for cache memory? a) SRAM b) DRAM c) PROM d) EPROM	1
Q7.	Which of the following is volatile form of memory? a) ROM b) RAM c) Hard disk d) Pen drive	1
Q8.	Which of the following erase entire memory or a block within one or few seconds in a single action a) EPROM b) EEPROM c) PROM d) Flash memory	1