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Course: CSE-405   Sec: 03

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Ans: to the Q. No: (2)

Given bit pattern,

01010000000110111110000010010111111001

Using bit stuffing,

01010000000110111110000010010100000

01111110, 01010000000110111111 $\underset{sb}{0}$ 0000 10010 11111

$\underset{sb}{0}$ 001 01111110,

Here, sb → stuffed bit.

[A] There will be an extra 'O' after each 5 consecutive 1's.

Ans: to the Q: No: (3)

Given stream,

| B | ESC | Flag | D | ESC | ESC |

Stream pattern after Byte stuffing.

| Flag | B | ESC | ESC | ESC | Flag | D | ESC | ESC |
| | | sb | sb | sb | | | sb | |

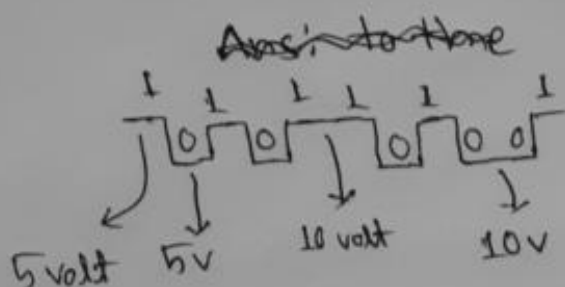| ESC | ESC | Flag |
| | sb | |

Here, sb → stuffed byte.

Ans: to the Q: No: (4)

In 'Physical layer coding violation' framing technique, consecutive bits are identified by Multiplier of a specific voltage.

If the first bit is high voltage and its value is @5 then if in the strem there are any consecutive high voltage then it will be

10, 15, 20 ---. etc. Same goes for low voltage.
And this is the multiplier of the specific voltage
with which is 5. This voltage value isn't fixed.

1010110100 1

~~Ans: to the~~

5 volt   5v   10 volt   10v

Ans: to the Q.No. (1)

In TCP/IP Ref Model,

Source   Destination

	Application layer			Data	Application layer
Data ←	Transport layer			TPDU	Transport "
TPDU ←	Network "			Packet	Network "
Packet ←	Datalink "			Frame	Datalink "
Frame ←	Physical "				Physical "
Bit ↑					Bit