

# SHIH-HAO TSENG

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## EDUCATION

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| <b>Cornell University (CU)</b> , Ithaca, NY, U.S.A.<br><i>PhD in Electrical and Computer Engineering (Advisor: Dr. A. Tang)</i> <ul style="list-style-type: none"><li>Relevant Coursework:<br/>Practicum in Operating Systems, Advanced Computer Networking, Approximation Algorithm, Convex Analysis, Functional Analysis, Statistical Inference and Decision, Stochastic Systems: Estimation and Control.</li></ul> | Aug. 2013 - Dec. 2018  |
| <b>National Taiwan University (NTU)</b> , Taipei, Taiwan<br><i>Bachelor of Science in Engineering (minor in Economics)</i> <ul style="list-style-type: none"><li>GPA: 92.20/100.00; ranking 2<sup>nd</sup> in a class of 226</li></ul>  | Sept. 2008 - June 2012 |

## RESEARCH INTERESTS

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- Networked systems, in-network processing, edge computing, Internet of Things, scheduling algorithms.
  - Control theory, deployment architecture, controller synthesis, stability and optimization.
  - Network simulation and emulation, controller performance evaluation.

## SELECTED PUBLICATIONS

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- S.-H. Tseng**, S. Agarwal, R. Agarwal, H. Ballani, and A. Tang, "CodedBulk: Inter-Datacenter Bulk Transfers using Network Coding," in *Proc. USENIX NSDI*, 2021.
  - S.-H. Tseng**, "Realization, Internal Stability, and Controller Synthesis," in *Proc. IEEE ACC*, 2021.
  - S.-H. Tseng** and J. Anderson, "Deployment Architectures for Cyber-Physical Control Systems," in *Proc. IEEE ACC*, 2020.
  - S.-H. Tseng**, A. Tang, G. Choudury, and S. Tse, "Routing Stability in Hybrid Software-Defined Networks," in *IEEE/ACM Trans. Netw.*, 2019.
  - S.-H. Tseng**, "Perseverance-Aware Traffic Engineering in Rate-Adaptive Networks with Reconfiguration Delay," in *Proc. IEEE ICNP*, 2019.
  - S.-H. Tseng**, B. Bai, and J. C. S. Lui, "Hybrid Circuit/Packet Network Scheduling with Multiple Composite Paths," in *Proc. IEEE INFOCOM*, 2018.
  - S.-H. Tseng**, E. Bitar, and A. Tang, "Random Convex Approximations of Ambiguous Chance Constrained Programs," in *Proc. IEEE CDC*, 2016.
  - A. Gushchin, **S.-H. Tseng**, and A. Tang, "Optimization-Based Network Flow Deadline Scheduling," in *Proc. IEEE ICNP*, 2016.

## EXPERIENCE

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| <b>California Institute of Technology</b> , Pasadena, CA, U.S.A.<br><i>Postdoctoral Scholar Research Associate</i> <ul style="list-style-type: none"><li>Lead the project of freshness-driven network control.</li><li>Mentor graduate students on parallel model predictive control and formal test-case generation.</li><li>Derived realization-stability lemma that unifies existing controller synthesis methods.</li><li>Investigated the controller deployment architecture for cyber-physical systems.</li></ul> | Oct. 2018 - Present   |
| <b>Cornell University</b> , Ithaca, NY, U.S.A.<br><i>Graduate Research Assistant/Teaching Assistant</i> <ul style="list-style-type: none"><li>Built CodedBulk to boost the throughput of inter-datacenter bulk transfers using network coding.</li><li>Studied time-aware network management under software-defined networking.</li><li>Helped teach Introduction to Probability and Inference for Random Signals and Systems.</li></ul>  | Aug. 2014 - Aug. 2018 |
| <b>The Chinese University of Hong Kong</b> , Shatin, NT, Hong Kong<br><i>Research Assistant</i> <ul style="list-style-type: none"><li>Developed efficient scheduling algorithms for hybrid packet/circuit networks.</li></ul>   | June 2017 - Aug. 2017 |
| <b>AT&amp;T</b> , Middletown, NJ, U.S.A.<br><i>Student Intern - Technical II</i> <ul style="list-style-type: none"><li>Developed routing algorithms to stabilize carrier-grade hybrid software-defined networks.</li></ul>  | June 2016 - Aug. 2016 |

## SKILLS

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**Programming Languages:** Proficient in C++, Python and Verilog.

**Programming Skills:** Working knowledge of linux based C++ socket and multithreaded programming.

**Simulation Tools:** Proficient in PSpice, MATLAB, and NS-3.

**Languages:** Fluent in English; native in Mandarin Chinese and Taiwanese Hokkien.

## HONORS AND AWARDS

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**Winner of the AT&T SDN Network Design Challenge** 2016

- Awarded to the top team providing the most efficient and cost effective routing solution to carrier-grade networks.

**Jacobs Fellowship (CU)** 2014

**Studying Abroad Scholarship (Ministry of Education, Taiwan(R.O.C.))** 2013

**Honorary Member of the Phi Tau Phi Scholastic Honor Society** 2012

- Presented to seniors from each college in Taiwan ranking within top 1% of their department.

**President's Awards (NTU)** 2009, 2010, 2011, 2012

- Four-time recipient; awarded to students ranking within top 5% of their department.

**Outstanding Project Award** 2011

- Awarded to the top 10 teams of Cross-Strait Finals of 2011 Innovate Asia Competition (FPGA design).