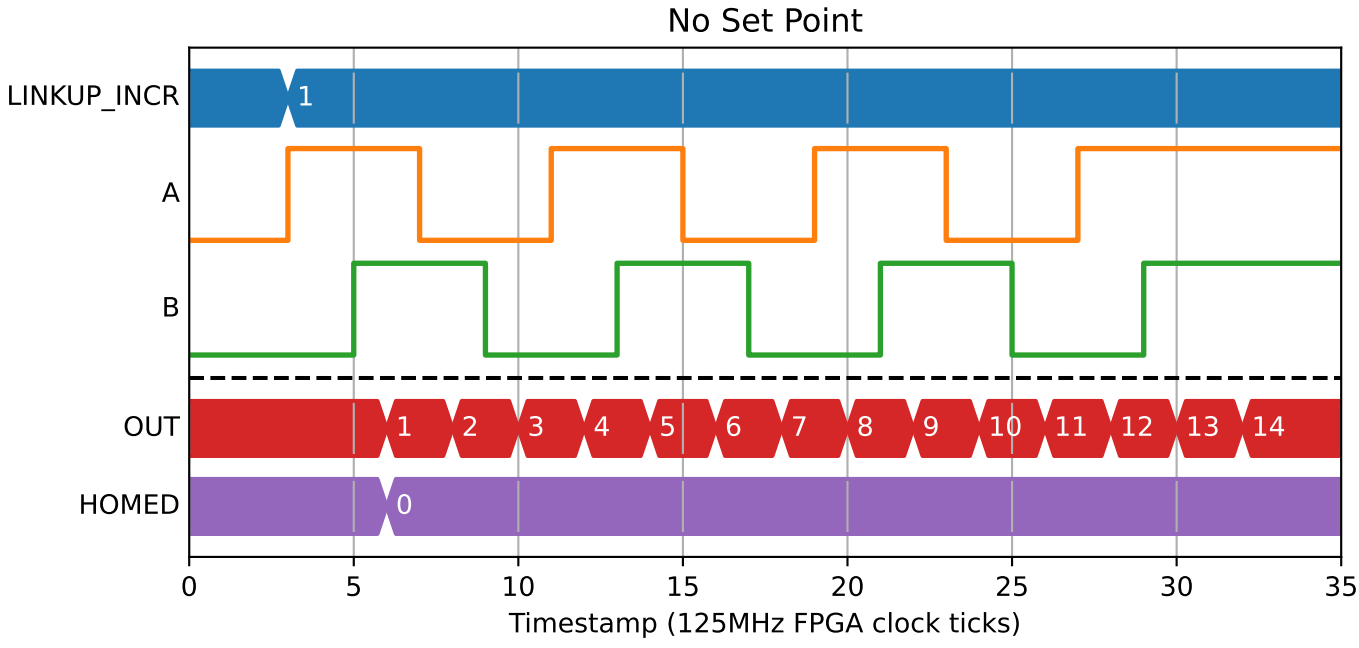
No Set Point
LINKUP_INCR
A
B
OUT
HOMED
1
1
2
3
4
5
6
7
8
9
10
11
12
13
14
0
0
5
10
15
20
25
30
35
Timestamp (125MHz FPGA clock ticks)