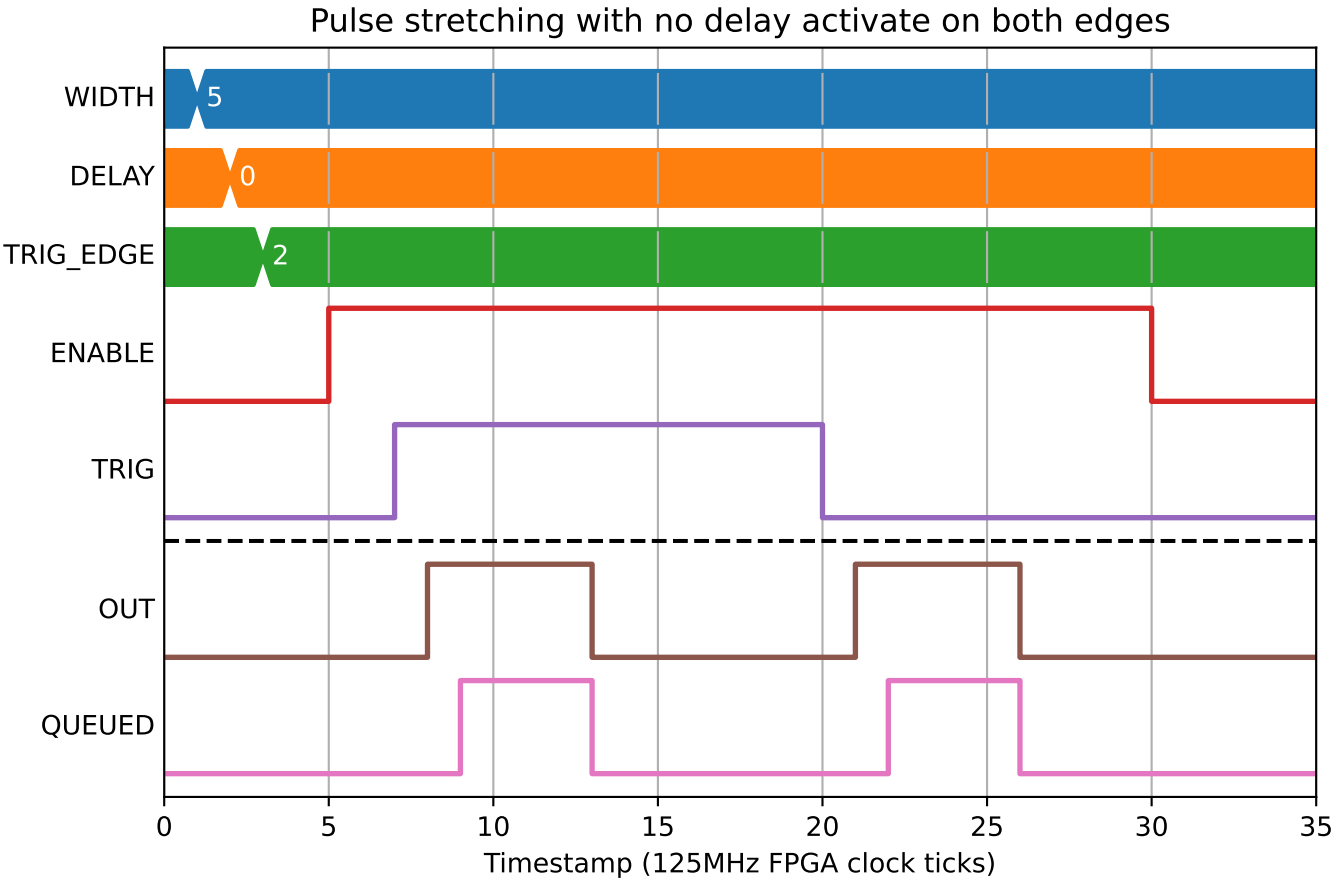
Pulse stretching with no delay activate on both edges
WIDTH
5
DELAY
0
TRIG_EDGE
2
ENABLE
TRIG
OUT
QUEUED
0
5
10
15
20
25
30
35
Timestamp (125MHz FPGA clock ticks)