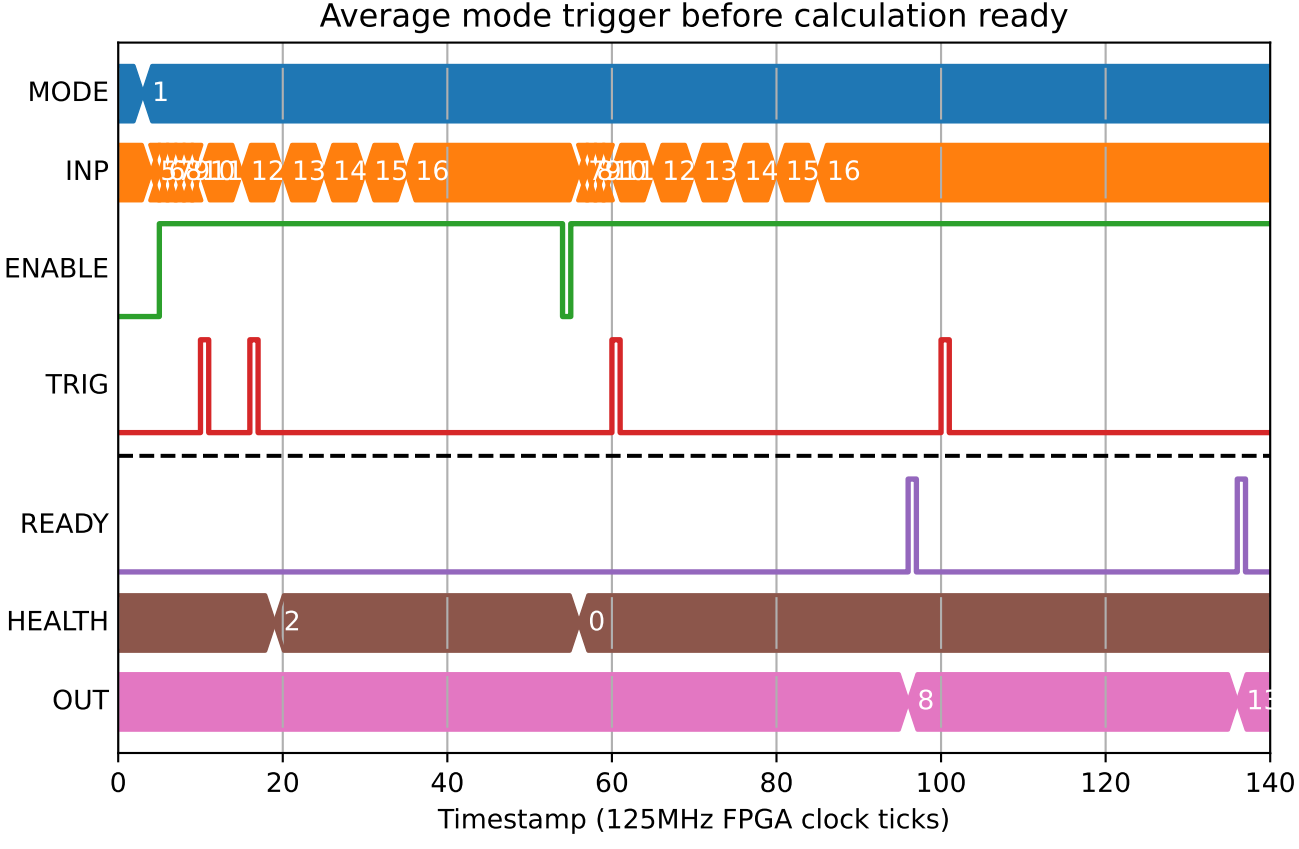
Average mode trigger before calculation ready
MODE
INP
ENABLE
TRIG
READY
HEALTH
OUT
1
2
0
8
0
20
40
60
80
100
120
140
Timestamp (125MHz FPGA clock ticks)