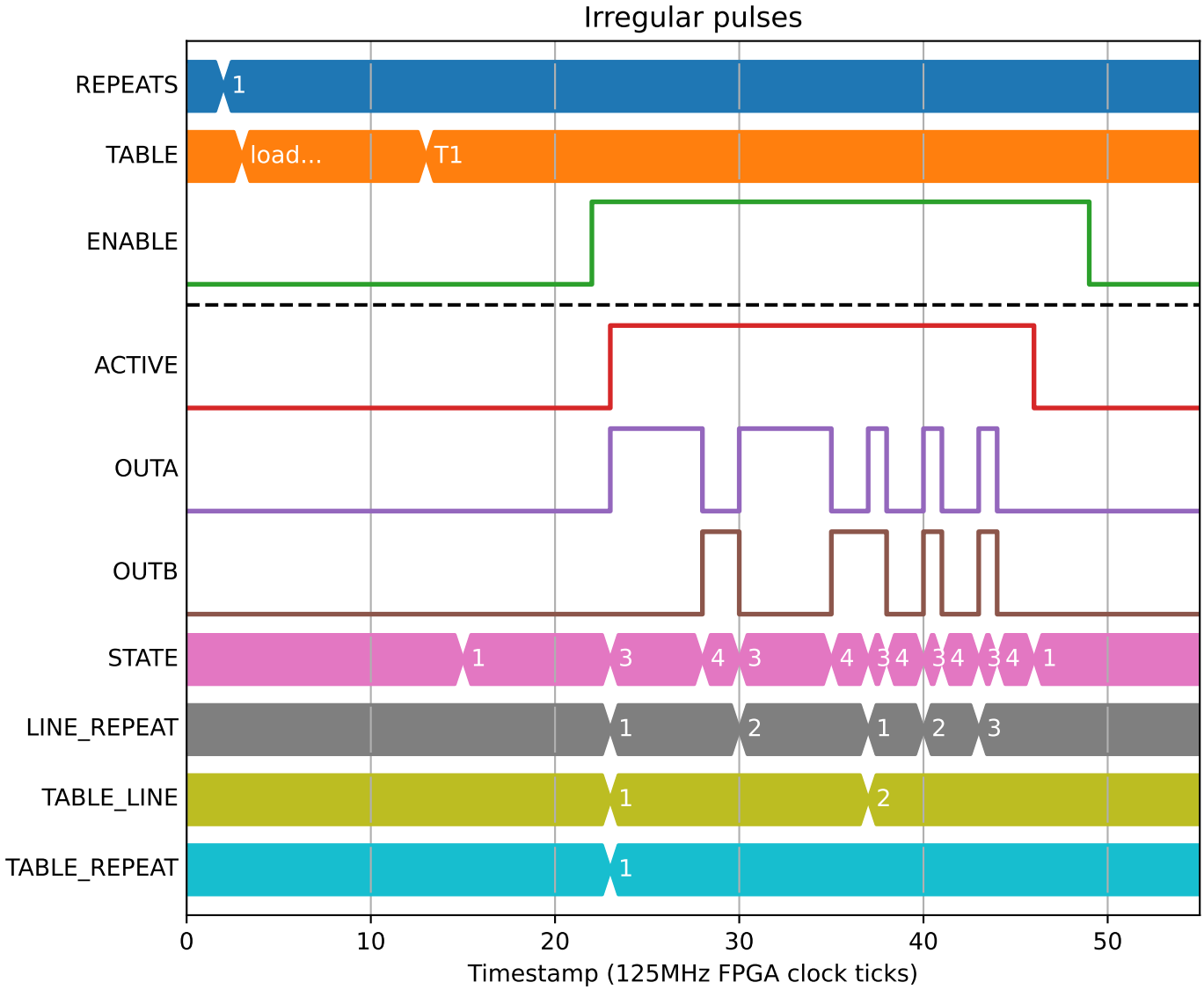
Irregular pulses
REPEATS
TABLE
ENABLE
ACTIVE
OUTA
OUTB
STATE
LINE_REPEAT
TABLE_LINE
TABLE_REPEAT
1
load...
T1
1 3 4 3 4 3 4 3 4 3 4 1
1 2 1 2 3
1 2
1
0
10
20
30
40
50
Timestamp (125MHz FPGA clock ticks)