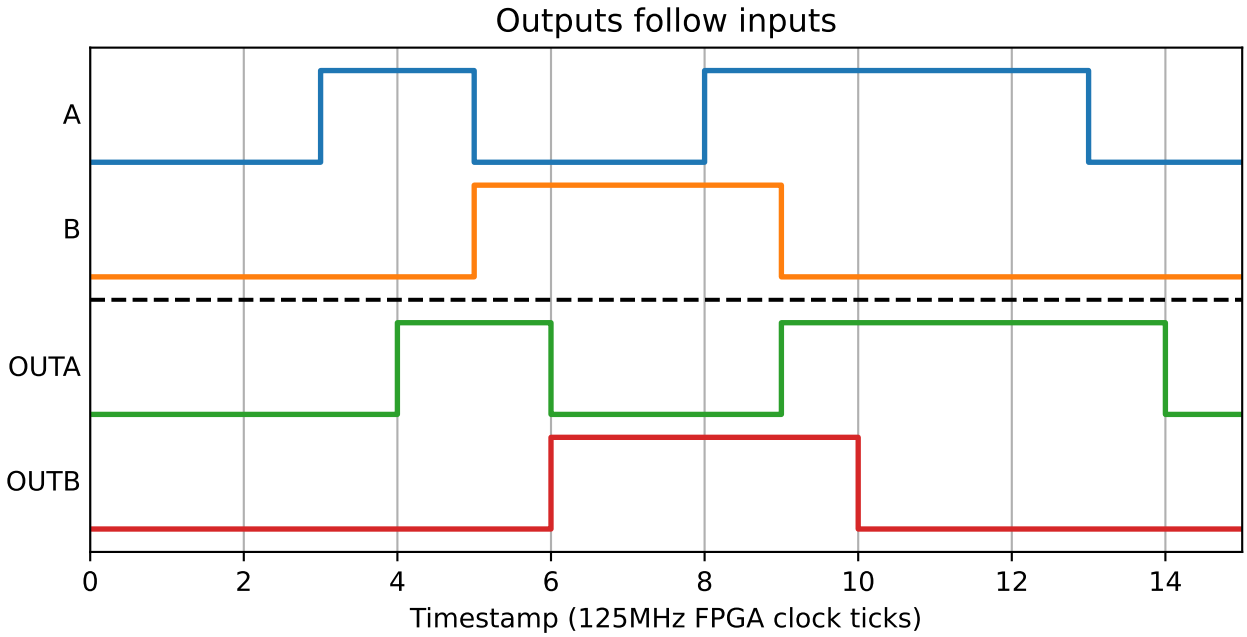
Outputs follow inputs
A
B
OUTA
OUTB
0
2
4
6
8
10
12
14
Timestamp (125MHz FPGA clock ticks)