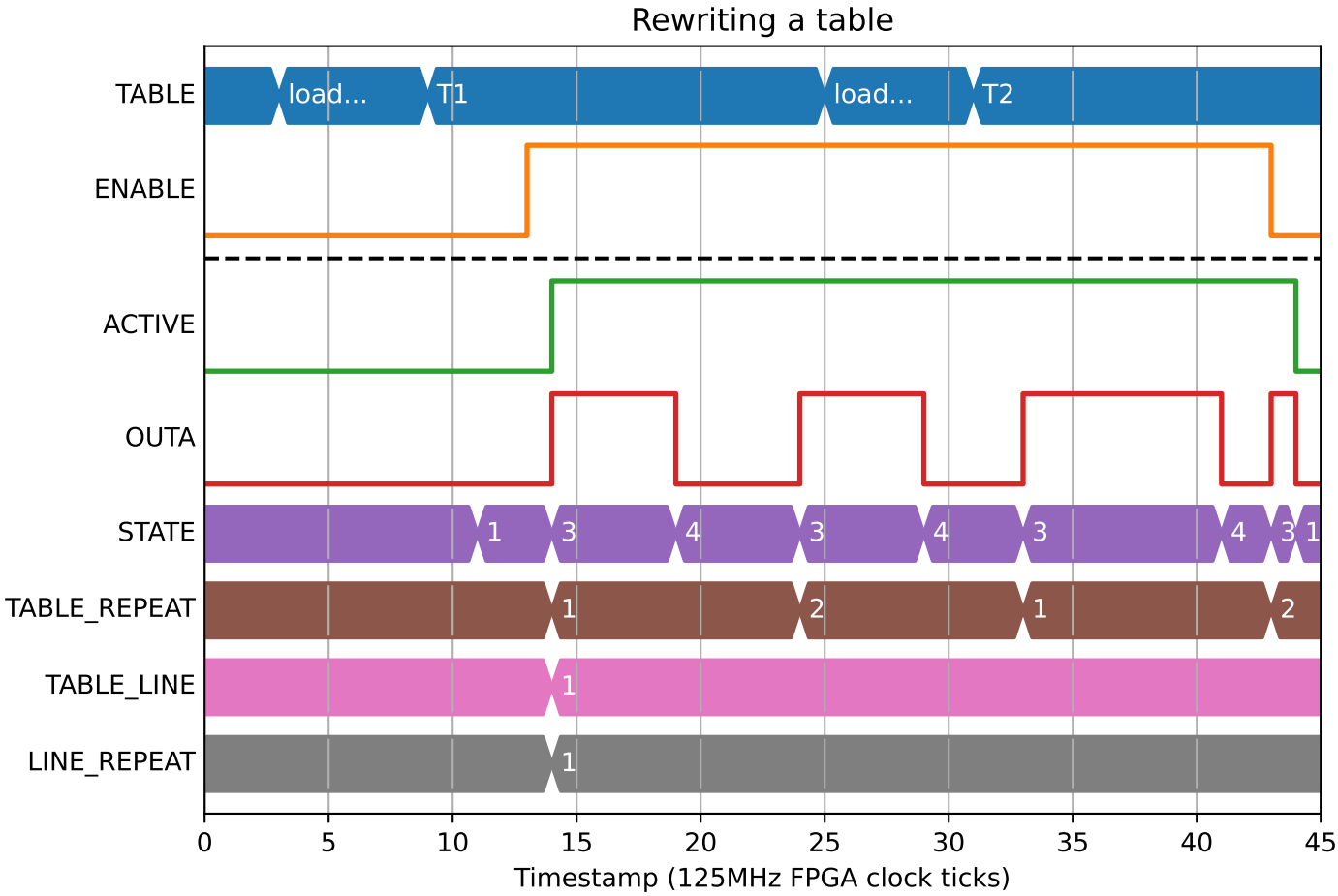
Rewriting a table
TABLE
load...
T1
load...
T2
ENABLE
ACTIVE
OUTA
STATE
1
3
4
3
4
3
4
3
1
TABLE_REPEAT
1
2
1
2
TABLE_LINE
1
LINE_REPEAT
1
0
5
10
15
20
25
30
35
40
45
Timestamp (125MHz FPGA clock ticks)