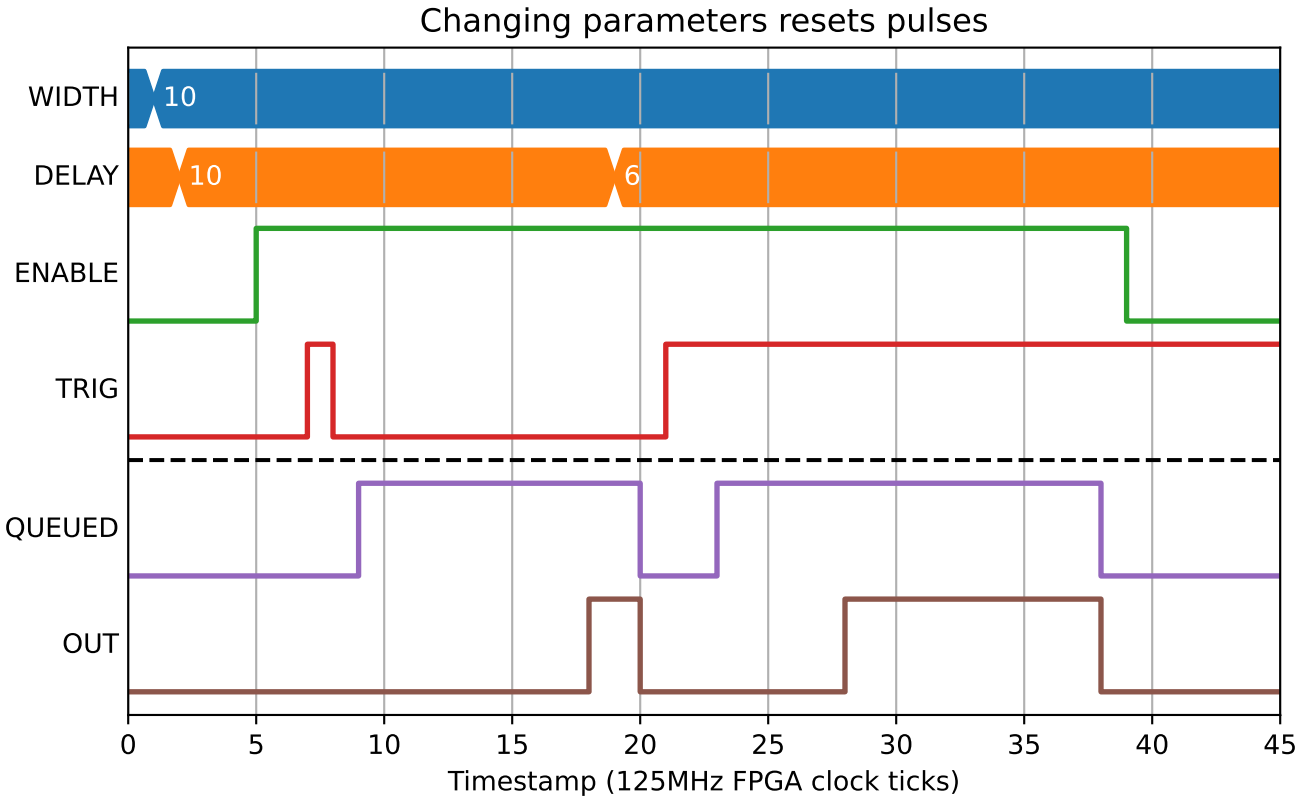
Changing parameters resets pulses
WIDTH
10
DELAY
10
6
ENABLE
TRIG
QUEUED
OUT
0
5
10
15
20
25
30
35
40
45
Timestamp (125MHz FPGA clock ticks)