

# SOT-363





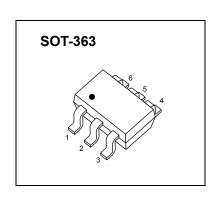
## 2N7002KDW

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## **Plastic-Encapsulate MOSFETS**

N-channel MOSFET

V <sub>(BR)DSS</sub>	R <sub>DS(on)</sub> MAX	I <sub>D</sub>
60 V	5Ω@10V	
60 V	5.3Ω@4.5V	300mA



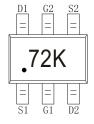
#### **FEATURE**

- High density cell design for Low  $R_{DS\ (on)}$
- Voltage controlled small signal switch
- Rugged and reliable
- High saturation current capability
- ESD protected

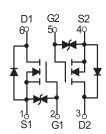
#### **APPLICATION**

- Load Switch for Portable Devices
- DC/DC Converter

#### **MARKING**



#### **Equivalent Circuit**



MOSFE	IOSFET MAXIMUM RATINGS (T <sub>a</sub> = 25°C unless otherwise noted)					
Symbol	Parameter	Value	Unit			
<b>V</b> DS	Drain-Source voltage	60	V			
Vgs	Gate-Source voltage	±20	V			
lo	Drain Current	300	mA			
Po	Power Dissipation	0.15	W			
TJ	Junction Temperature	150	$^{\circ}$			
Tstg	Storage Temperature	-55-150	°C			
Reja	Thermal Resistance fromJunction to Ambient	833	°C /W			



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#### **MOSFET ELECTRICAL CHARACTERISTICS**

#### T<sub>a</sub>=25 °C unless otherwise specified

Parameter	Symbol	Test Condition	Min	Тур	Max	Units
Static Characteristics						
Drain-Source Breakdown Voltage	VDS	V <sub>GS</sub> = 0V, I <sub>D</sub> =250µA	60			V
Gate Threshold Voltage*	VGS(th)	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =1mA	1	1.3	2.5	V
Zero Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =48V,V <sub>GS</sub> = 0V			1	μA
Gate –Source leakage current	Igss1	V <sub>GS</sub> =±20V, V <sub>DS</sub> = 0V			±10	μA
Danier Courses On Desirtance	Б	V <sub>GS</sub> = 4.5V, I <sub>D</sub> =200mA		1.1	5.3	Ω
Drain-Source On-Resistance*	RDS(on)	V <sub>GS</sub> =10V,I <sub>D</sub> =500mA		0.9	5	Ω
Diode Forward Voltage	VsD	V <sub>GS</sub> =0V, I <sub>S</sub> =300mA			1.5	V
Recovered charge	Qr	V <sub>GS</sub> =0V,I <sub>S</sub> =300mA,V <sub>R</sub> =25V, dI <sub>s</sub> /d <sub>t</sub> =-100A/μs		30		nC
Dynamic Characteristics**			•			•
Input Capacitance	Ciss				40	pF
Output Capacitance	Coss	V <sub>DS</sub> =10V,V <sub>GS</sub> =0V,f =1MHz			30	pF
Reverse Transfer Capacitance	Crss				10	pF
Switching Characteristics**						
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>GS</sub> =10V,V <sub>DD</sub> =50V,R <sub>G</sub> =50Ω,			10	ns
Turn-Off Delay Time	t <sub>d(off)</sub>	R <sub>GS</sub> =50 $\Omega$ , R <sub>L</sub> =250 $\Omega$			15	ns
Reverse recovery Time	t <sub>rr</sub>	$V_{GS}$ =0V, $I_{S}$ =300mA, $V_{R}$ =25V, $dI_{s}/dt$ =-100A/ $\mu s$		30		ns
GATE-SOURCE ZENER DIODE			1	1	1	
Gate-Source Breakdown Voltage	BVgso	I <sub>gs</sub> =±1mA (Open Drain)	±21.5		±30	V

<sup>\*</sup>Pulse Test : Pulse Width ≤300µs, Duty Cycle ≤2%.

<sup>\*\*</sup>These parameters have no way to verify.

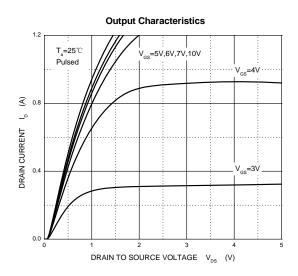


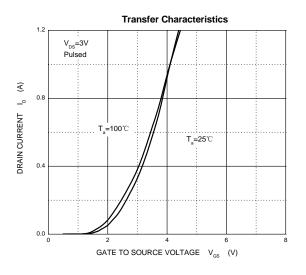


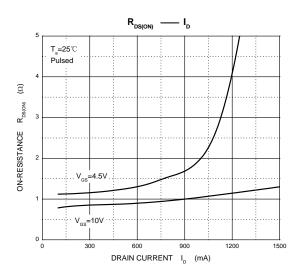


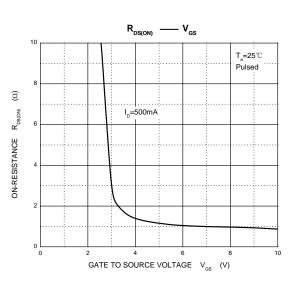
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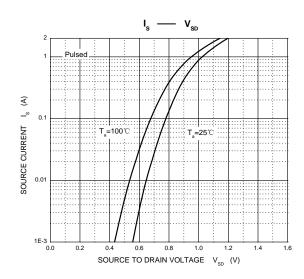
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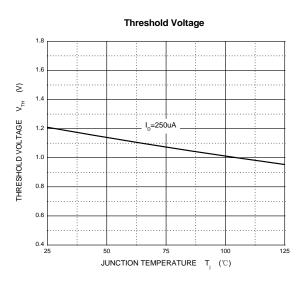














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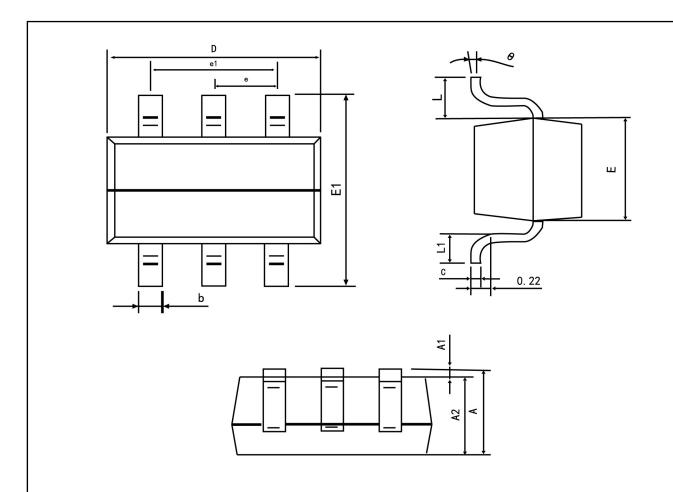




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#### **SOT-363-Package Outline Dimensions**



Come le al	Dimension in Millimeters		
Symbol	Min	Max	
Α	0.900	1.100	
A1	0.000	0.100	
A2	0.900	1.000	
b	0.150	0.350	
С	0.080	0.150	
D	2.000	2.200	
E	1.150	1.350	
E1	2.150	2.450	
е	0.650 TYP		
e1	1.200	1.400	
L	0.525 REF		
L1	0.260	0.460	
θ	0°	8°	