United College of Engineering & Research, Allahabad

Third Sessional

Compiler Design (RCS-602)

B.Tech VIth Sem(CS+IT)

Time : 120 Min. M.M. 30

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[Section –A] [Attempt all part] [1X10=10]

1. Define the term Bootstrapping?
2. Differentiate between Annoted parse tree and Syntax tree.
3. Explain how Lex tool may be used to create lexical analyzer?
4. What is activation tree?
5. How is scope information represented in symbol table?
6. What is ambiguity in grammar?
7. Differentiate between Quadruple and Triples.
8. Discuss the challenges in compiler design?
9. What is cross compiler?
10. What is basic block?

[Section –B] [Attempt any 3 part] [4X3=12]

1. Discuss the various data structure used for symbol table with suitable example.
2. Generate three address code for the following code?

```
switch(ch)
{
        case 1:
                c=a+b;
                break;
        case 2:
                c=a-b;
                break;
}
```

3. What are the lexical phase error, syntactic phase error and semantic phase errors? Explain with suitable example.
4. Why run-time storage management is required? How simple stack implementation is implemented?
5. Construct a DFA which accepts set of all strings over {a,b} which starts and ends with same symbol.

[Section –C] [Attempt any 2 part] [2X4=8]

1. What is DAG? How DAG is created from three address code? Write algorithm for it and explain it with a relevant example.
2. A) Write the short notes on the following :
   I. Dead code elimination
   II. Loop invariant and strength reduction
   III. Code motion
   IV. Copy propagation
   B) Explain the working of operator precedence parsing technique with Example.
3. Generate three address code for C[A[i,j]]=B[i,j]+C[A[i,j]]+D[i+j].(You can assume any data for solving question, if needed). Assuming that all array elements are integer. Let A and B a 10X20 array with low1=low2=1