# NXP Semiconductor Women in Tech

## The World of semiconductor innovation welcomes the WOMEN CHANGEMAKERS

The time has come for women to embrace early to make a difference in the semiconductor space and empower the future with their knowledge, prowess, and creativity.

Let's be a part of the "Women in Tech" program to design and fabricate a safe, secure, and connected world.

# **Key features are:**

- Enhance skill through training
- Build experience through hands-on work
- Embrace technology to build enhanced livelihood

#### **Features and Benefits**

- Monetary reward worth INR 50,000 from NXP
- Preference in internship & job opportunities at NXP
- Technical classes by industry experts for enabling innovation
- Focus on industry relevant practical sessions along with theory
- Student friendly schedule from the comfort of your home

#### **Timelines**

# **Program Timeline**

- **Registration** 20<sup>th</sup> August 19<sup>th</sup> September, 2025
- Tests Process
  - Initial Shortlisting 30<sup>th</sup> September, 2025
  - o Cognitive Assessment and Composition 6<sup>th</sup> 7<sup>th</sup> October, 2025
  - o Test Result Declaration 16th October, 2025

#### • Interview Process

- o Interviews 21st 30th October, 2025
- o Final Result Declaration 6th November, 2025
- Launch 11<sup>th</sup> November, 2025

# Eligibility Criteria

- Women students graduating from their B.E./B.Tech. first year in India.
  - o Currently pursuing 3<sup>rd</sup> semester of B.E./B.Tech. Program (2024-2028).
- Students from electronics, computer science and other related engineering courses.
- A regular student from college/university approved by UGC/AICTE in India.

#### **Course Curriculum:**

- 1. SOC Architecture Introduction
- 2. Digital
- 3. Analog Design Introduction
- 4. Design Flow
- 5. Design Architecture, RISC-V & ARM
- **6.** Verification Introduction
- 7. Introduction to Emulation
- 8. DFT Basics (Scan, JTAG, BIST)
- 9. Introduction to Synthesis & Timing Constraints
- **10.** Placement, Routing & CTS
- 11. Validation
- **12.** C and OOPs Basics
- 13. Verilog & System Verilog
- 14. Embedded Software
- **15.** Python Scripting
- **16.** Mini Projects (frontend & backend)
- **17.** Major Project (frontend/backend)
- **18.** Summary

## Why Should You Join This Course?

- Opportunity to build a network with silicon tech mentors.
- Selected 75 students will get training and monetary reward based on merit and performance for this program.
- The monetary reward of INR 50,000 per selected student will be awarded on successful completion of the program (TDS will be deducted u/s 194B or as applicable).
- The top performing students will get preference for internship in the 4th year (with the applicable internship norms as per NXP).

## **Registration Requirements**

- Valid email ID/Phone number
- Professional Photograph (Scanned)
- College ID card (Scanned)
- 10<sup>th</sup> and 12<sup>th</sup> Marksheet (Scanned)
- B.Tech Semesters Marksheet (Scanned)