shine Saea Mathewo.

Computer Organization

) Advantages of Von Neuman Architecture.

- · Conteol Unit gets data and instruction in the same way from one memory. It simplifies design and development of the Control Unit.
- · Data from memory and from, devices are accessed in the same way.
- · Memory organization is in the hande of programmers.

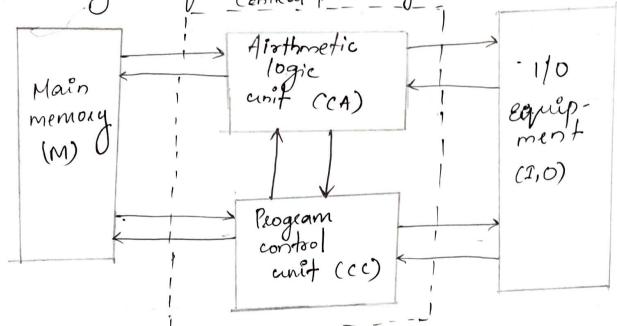
 · Development of Control Unit is cheaper and faster than Harward.
 - It is better for desktop computers, laptops, workstalions and high performance computers.
- · The peograms can be optimised in smaller size
- . The code is executed serially and takes more clock eyeles.

Deans Working Prénuiple of Hon Van Neuman Architecture. The Von Neuman computer auchitecture follows a step-by-step peocess called the jetch-execute cycle. The jetched instantions is then decoded to determine the operation and opeands involved. The instention is executed, which may involve arithmetic, logic of control which may involve arithmetic, logic of control operations. It necessary, the computer accessess memory or registers. The cycle repeats fetching the next

instruction and executing it, until the program is complete or a specific condition is not this sequential process allows the von newman computer system to execute instructions one after another renabling wide range of computing tasks

Block diagram of von newmann computer.

Central processing unit (PU)



Memory: It stores data & instructions. It is a storage component.

Control Unit: The control unit is responsible for coordinating
Control Unit: The controlling the activities of the computer
gt fetches instructions from memory.

Airthmotic and Logic Unit: The ALU performs
air anothmetic operations subtraction,
multiplication, addition, division. It is
the component that caronies.

4. Type of but arbituation

Bus Arbituation is the procedure by which the active

bus master accesses the bus.

bus master accesses the bus.

Juo types of bus arbiteration

Centralized Bus Arbiteration in which the necessary

Centralized Bus Arbiteration in which every device takes

arbitration is crassical out by a lone bus arbitrates

arbitration in which every device takes

Distributive Bus Arbitration in which every device part in choosing a new bus master.

and the state of

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