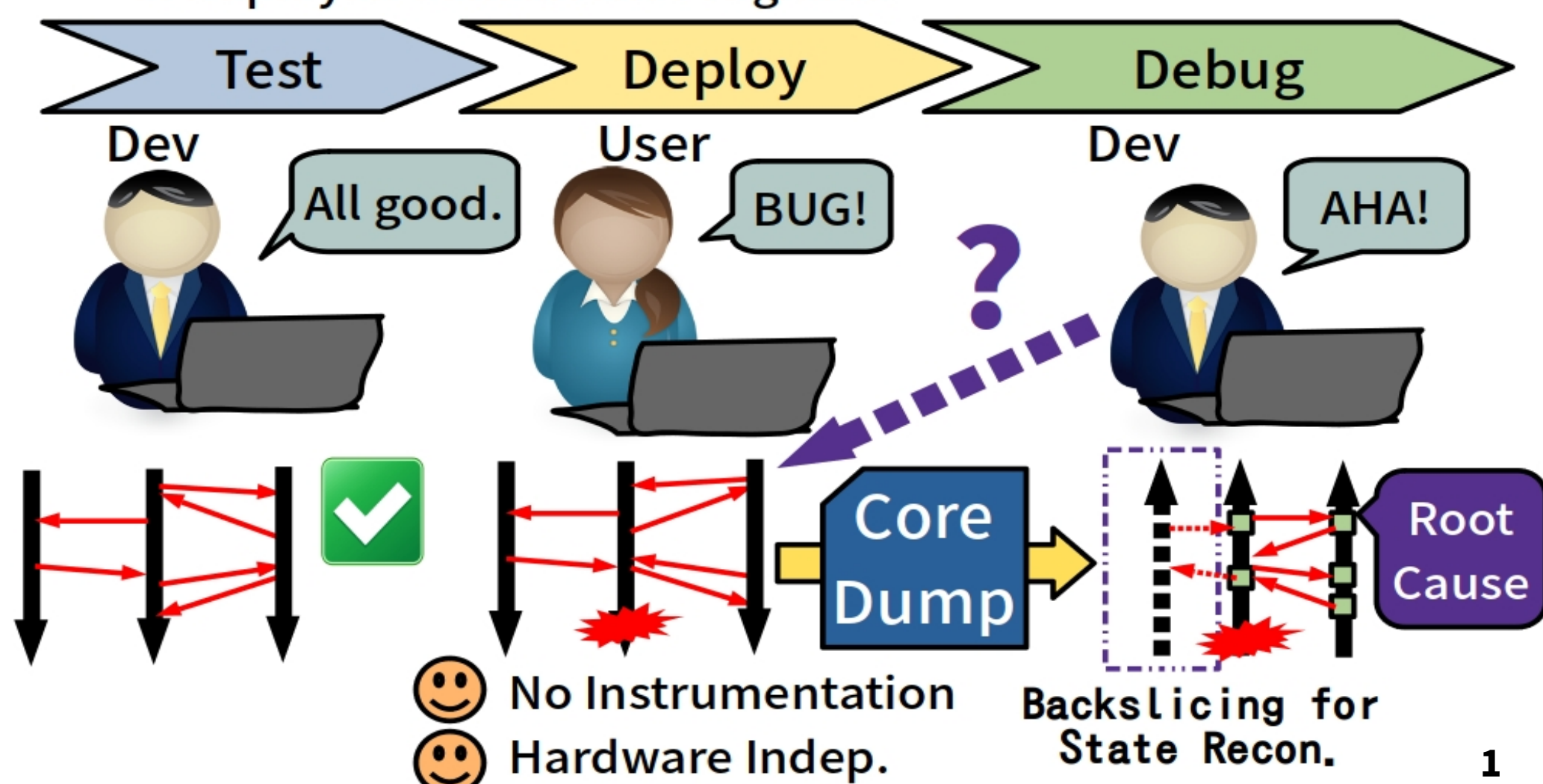


# Static Analysis for Efficient Reverse-Debugging of Concurrency Bugs

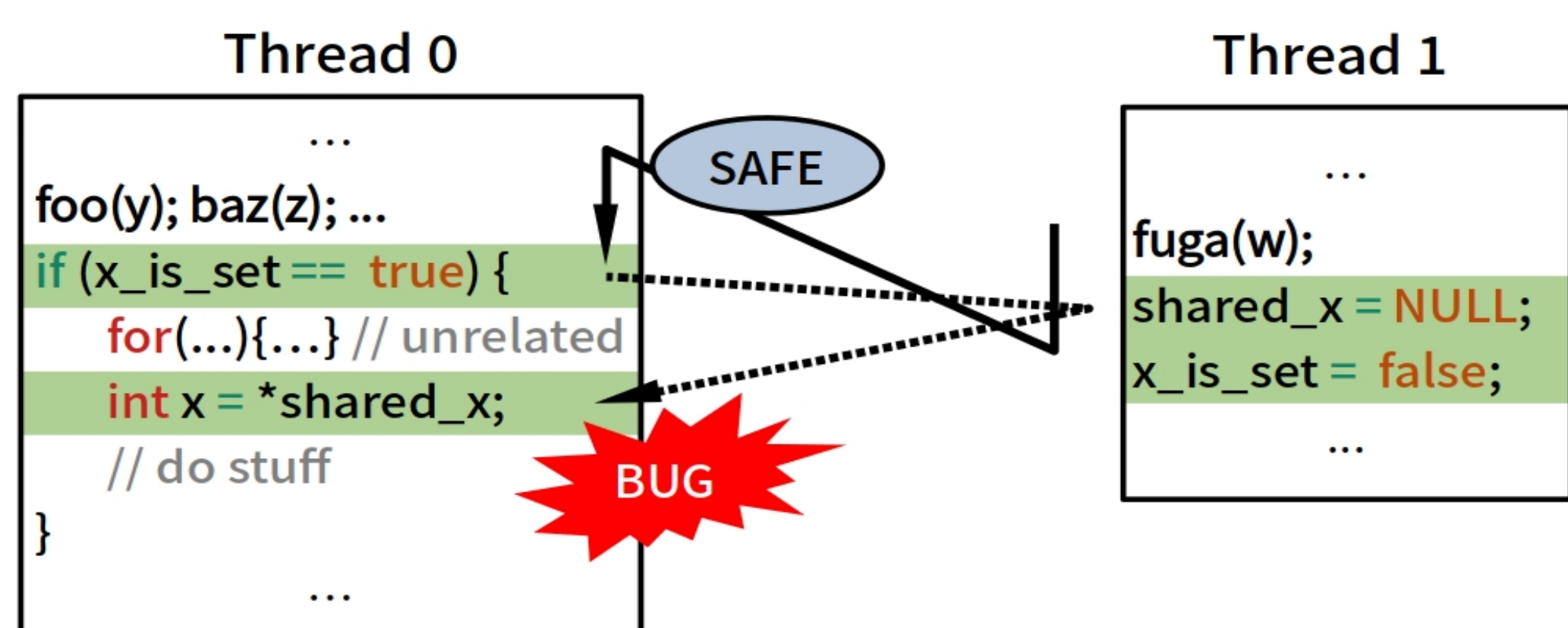
**P18** Shinji Hoshino, Yoshitaka Arahori, Katsuhiko Gondow (Tokyo Institute of Technology)

## Overarching Goal

- Efficient, Software-Only Reverse-Debugging of Deployed Concurrent Programs

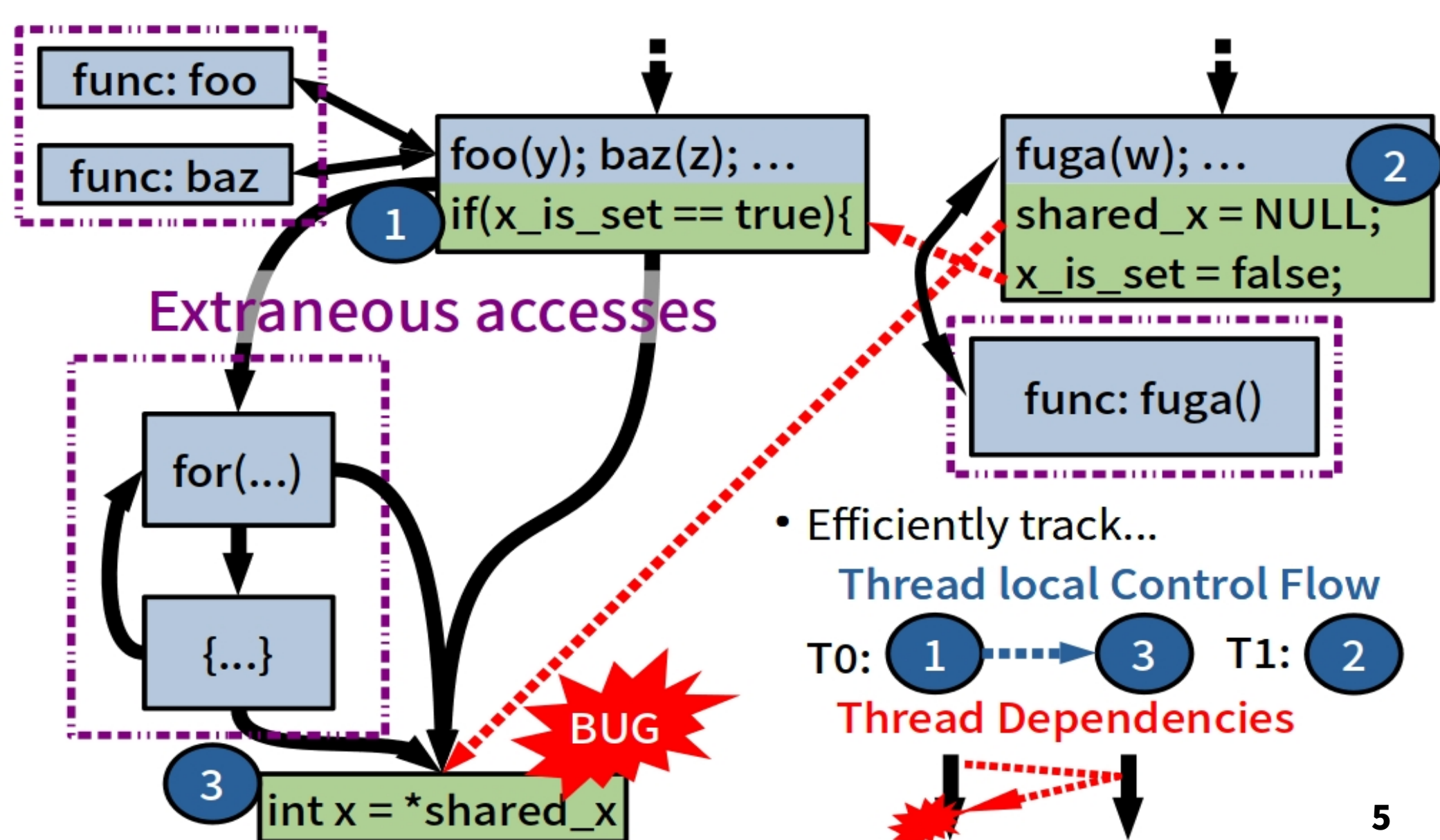


## Motivating Example



- Efficient & Software-only reverse debugging is possible with...
  - Cross-thread backward slices
  - Repeated state reconstruction
    - All done in LLVM-IR

## Cross-Thread Backslicing



## Related Work and Our Vision

	Hardware Dep.	Intrusive	Runtime Overhead	Analysis Overhead	Arch. Dep.	State Prec.	Recon. Recall
CLAP [0]	NO	YES	MED	VERY HIGH	NO	H	H
REPT [1]	YES	YES	VERY LOW	LOW	YES	H	L
Failure Sketch [2]	Y/N	YES	LOW	MED	Y/N	M	M
ConSeq [3]	NO	NO	MED	MED	YES	N/A	L
<b>Our Approach</b>	NO	NO	LOW	LOW	NO	H*	M

\*debug execution

[0] CLAP: Recording Local Executions to Reproduce Concurrency Failures, PLDI'13

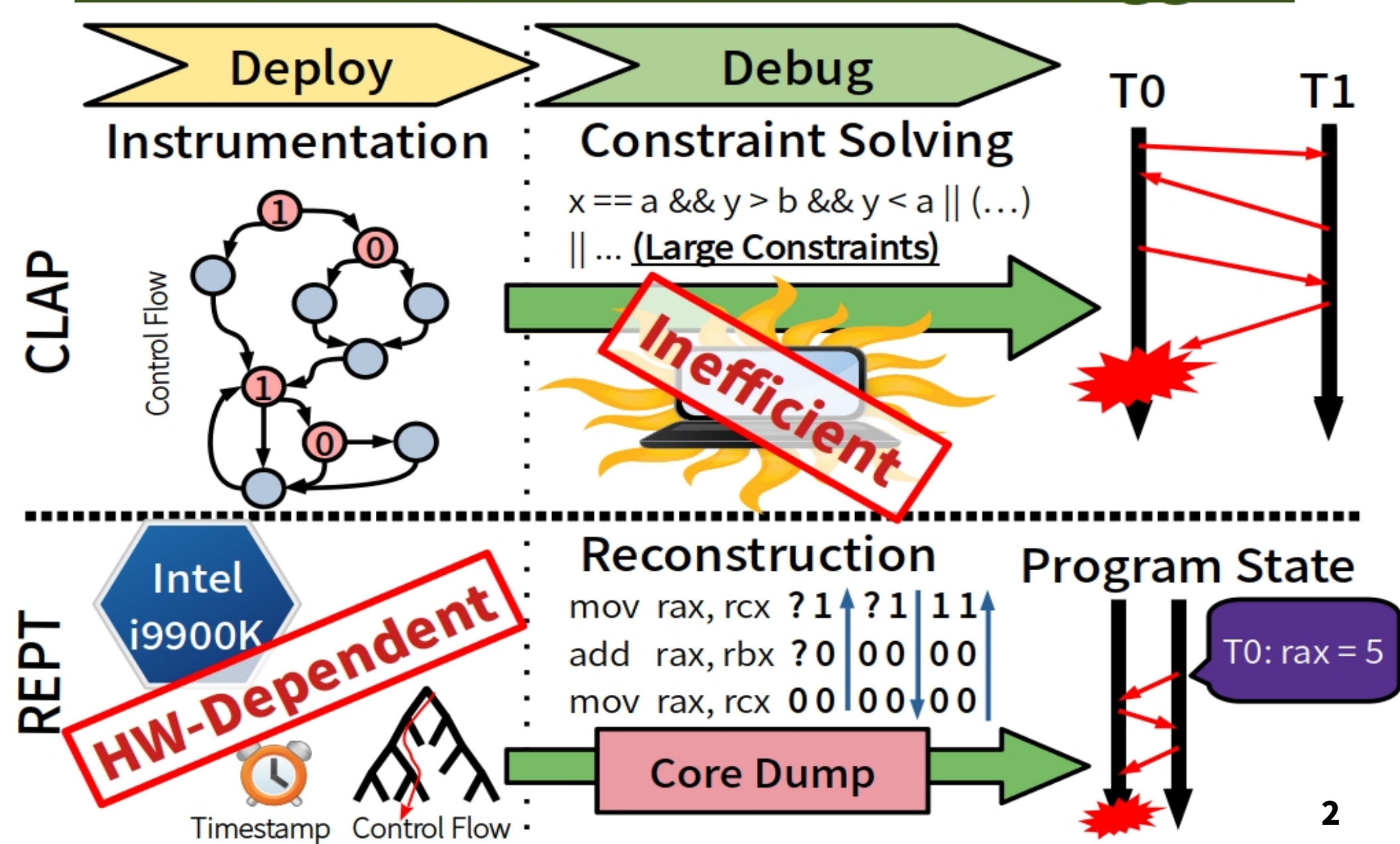
[1] REPT: Reverse Debugging of Failures in Deployed Software, OSDI'18

[2] Failure Sketching: A Technique for Automated Root Cause Diagnosis of In-Production Failures

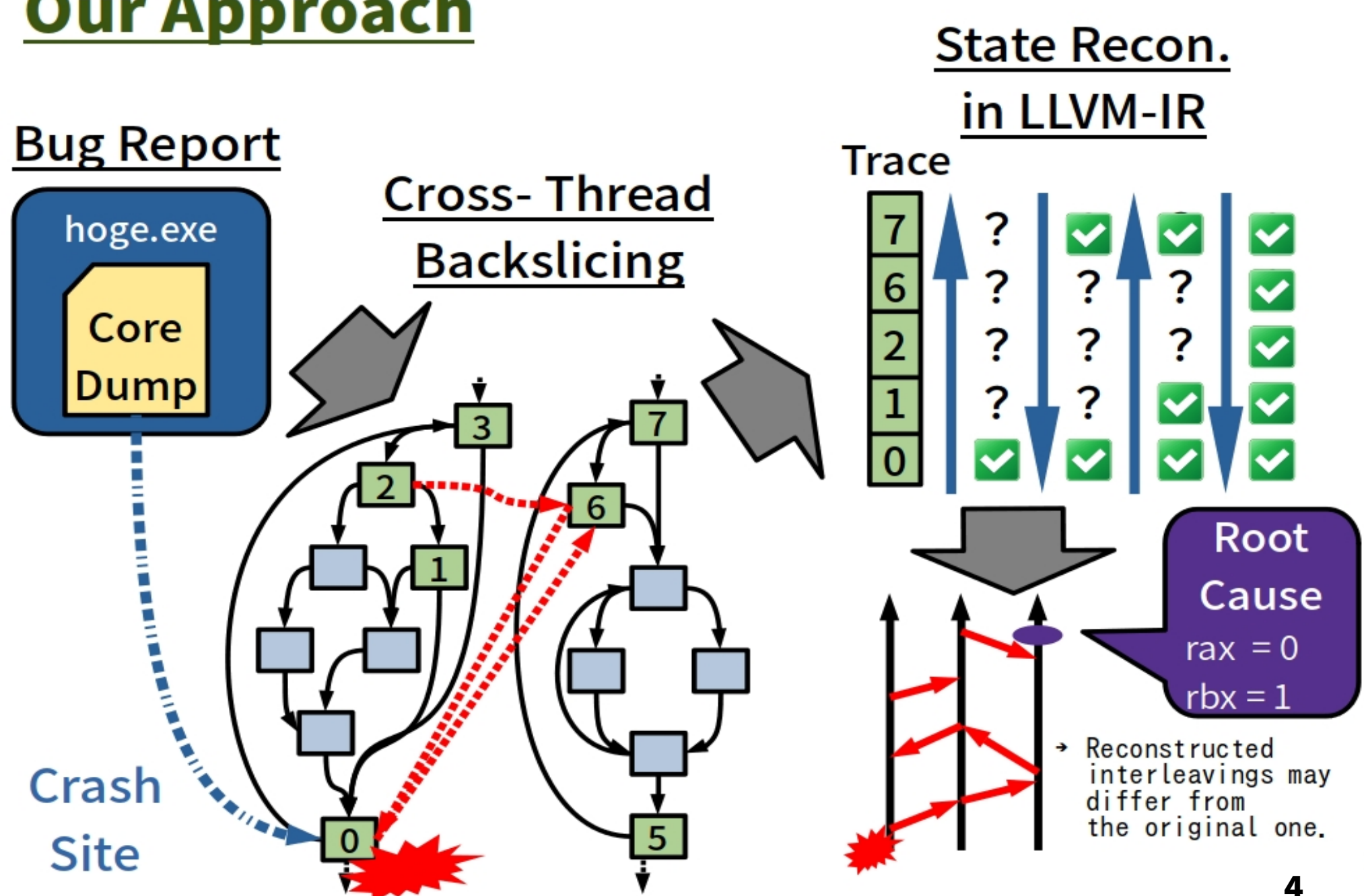
[3] Conseq: Detecting Concurrency Bugs through Sequential Errors, ASPLOS'11

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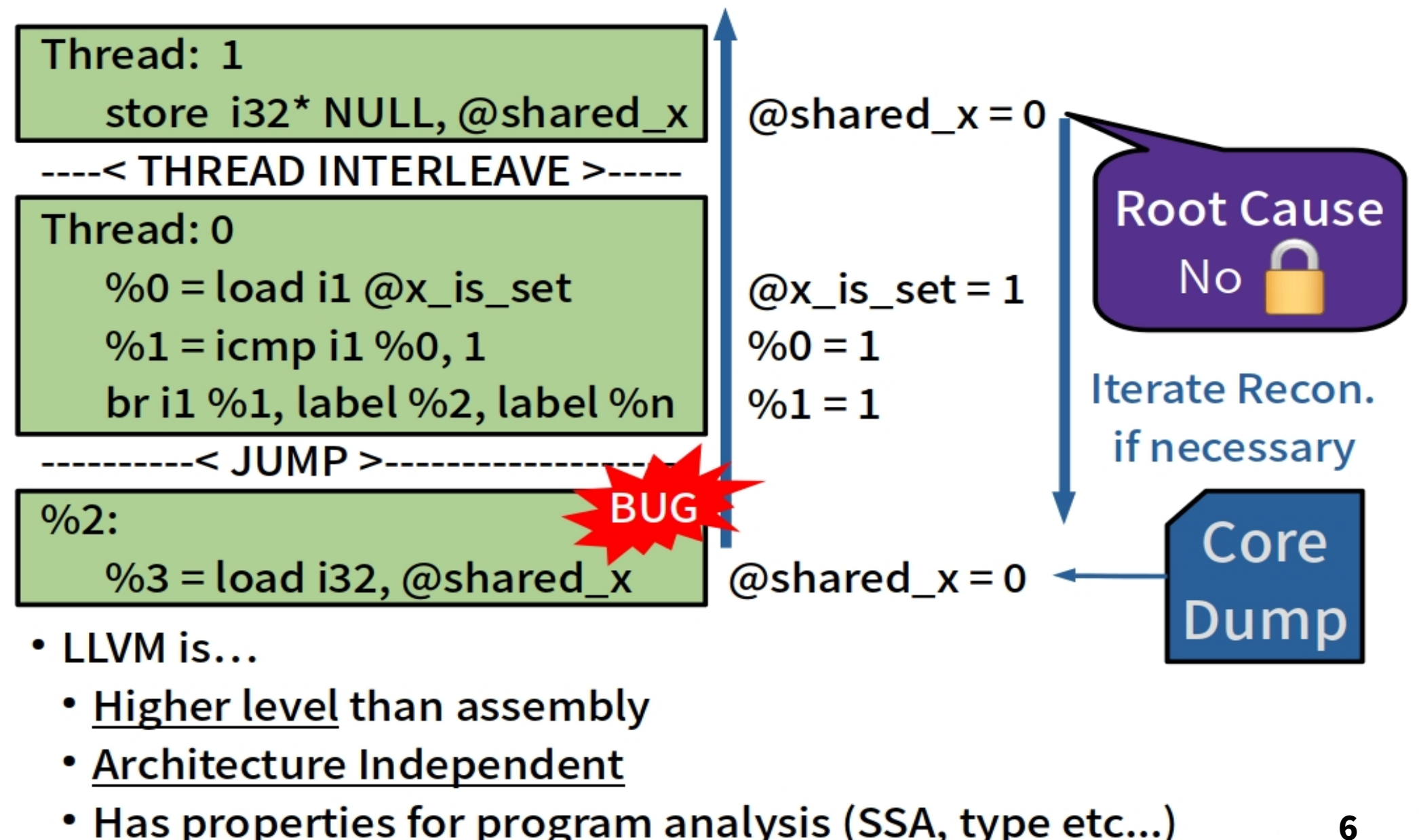
## Problems w/ SOTA Reverse Debuggers



## Our Approach

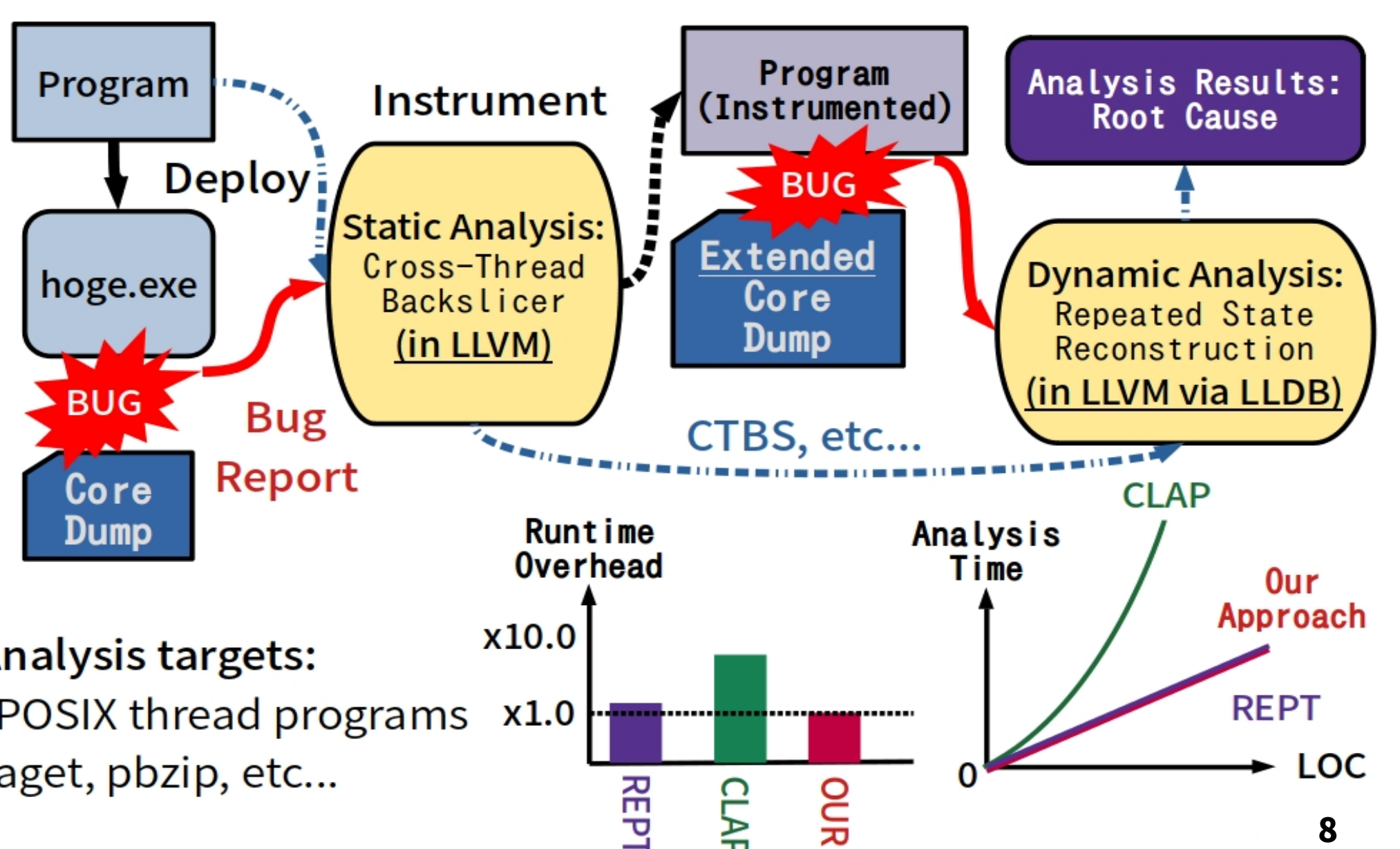


## Repeated State Recon. in LLVM-IR



- LLVM is...
  - Higher level than assembly
  - Architecture Independent
  - Has properties for program analysis (SSA, type etc...)

## Exp. Design & Expected Results



Analysis targets:

POSIX thread programs  
aget, pbzip, etc...

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