

# GPU Implementations of Object Detection using HOG Features and Deformable Models

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Vision-based object detection using camera sensors is an essential piece of perception for autonomous vehicles. Various combinations of features and models can be applied to increase the quality and speed of object detection. A well-known approach uses histograms of oriented gradients (HOG) with deformable models to detect a car in an image. A major challenge of this approach can be found in computational cost introducing a real-time constraint problem in the real world. In this paper, we present an implementation technique using graphics processing units (GPUs) to accelerate computations of scoring similarity of the input image and the pre-defined models. Our implementation considers not only the algorithm part but also the entire program structure for practical use. We also apply the presented technique for the real-world car detection program and demonstrate that our implementation using commodity GPUs can achieve speedups of 1.5x to 3x in frame-rate over sequential and multithreaded implementations using traditional CPUs.

**Index Terms**—GPGPU; Computer Vision; Object Detection

## I. INTRODUCTION

Grand challenges of cyber-physical systems (CPS) include a high computational cost of understanding the physical world. Object detection is one of compute-intensive tasks for CPS. For example, an autonomous vehicle needs to detect and track other vehicles by itself. Current autonomous driving technologies [6], [12], [20] tend to rely on active sensors such as GPS, RADAR, and LIDAR [10], [18] together with very accurate pre-configured maps, but the use of passive camera sensors is becoming more practical due to recent advances in computer vision [2]–[4]: vision-based object detection can be applied for various ranges and orientations. In particular, histograms of oriented gradients (HOG) [2] features provide reliable high-level representations of an image underlying many state-of-the-art object detection algorithms [3], [5], [17], [19], [21]. However, a major concern of HOG-based object detection remains in computational cost.

Previous work on the implementation of HOG-based object detection are limited to either hardware implementations [7], [8], [11] or specific parts of HOG algorithms [1], [16]. There is even no quantitative investigation of what implementation issues could prevent HOG-based object detection from being deployed in real-world applications. Given recent innovations in commodity hardware technology such as multicores and

graphics processing units (GPUs), it is worth exploring if the current state of the arts meets computational requirements of cutting-edge object detection implementations.

**Contribution:** This paper presents GPU implementations of HOG-based object detection in consideration of real-world applications using deformable part models [3]. While this is a popular vision-based object detection approach, what remains an open question is a generalized programming technique and a quantification of performance characteristics for practical use. We begin with an analysis of traditional CPU implementations to find fundamental performance bottlenecks of HOG-based object detection. This analysis reasons about our approach to GPU implementations where we parallelize compute-intensive blocks of the object detection program using the GPU step by step to minimize its makespan. The experimental results obtained from a real-world car detection program using a commodity GPU show that the GPU outperforms the CPU by 1.5x to 3x in frame-rate, while another 2x improvement would be needed at least to deploy in the real world.

**Organization:** The rest of this paper is organized as follows. Section II describes the assumption behind this paper. Section III presents an analysis of HOG-based object detection and our GPU implementation technique. Section IV evaluates the performance benefit of our technique over traditional CPU implementations. This paper concludes in Section V.

## II. ASSUMPTION

We consider the system composed of a multicore CPU and commodity GPU. They communicate with each other via the PCIe bus. We use CUDA [14] for GPU programming, whose development environment can be downloaded from NVIDIA's website [15]. Input images are loaded from pre-captured JPEG files, since we focus on a high computational cost of image processing. Systemized coordinations of computations and I/O devices are outside the scope of this paper. The use of multiple GPUs is also not in consideration.

We follow the object detection method presented by Felzenszwalb *et al.* [3], where objects are represented by HOG features [2] and the detectors is composed of a “root” filter plus a set of “parts” filters that allow visual appearance to be modeled at multiple scales. This is one of the most recognized approach to object detection. See [3] for the detail.

Object detection often requires a machine learning phase to construct the object models. We assume that this learning

phase has already been done a priori and the object models are stored in the system. Particularly we restrict our attention to vehicle detection in this paper, utilizing the vehicle models provided by prior work [13]. Although these models achieve a high detection rate, the computational cost of scoring similarity of an input image and the models using HOG features is very expensive. Specifically they include 2 root filters and 12 part filters, each of which needs to be scored against 32 resized images. The scoring could be conducted for every  $8 \times 8$  or  $4 \times 4$  pixels independently. In consequence, there are approximately 100 billion computational blocks for a single high-definition image, while the frame-rate needs to meet 10 to 20 frames per second (FPS) for practical use. This data-parallel compute-intensive nature of HOG-based object detection motivates the use of GPUs in this paper.

The CPU implementation of HOG-based vehicle detection has already been developed in prior work [13]. It leverages the POSIX *pthread* to parallelize the scoring per filter on a multicore CPU. While we use this multicore implementation for a performance comparison as it is, we also serialize it to execute on a single core so that we can compare our GPU implementation to two variants of the CPU implementation.

### III. GPU IMPLEMENTATION

This paper presents GPU implementations of the existing object detection program using a popular computer vision technique [13]. Our contribution is distinguished from prior GPU implementations work [1], [16] in that we analyze the performance characteristics of the object detection program to figure out what part of the program should be accelerated using the GPU and our implementations build on this analysis optimizing performance.

Note that this paper focuses on vehicle detection but the presented GPU implementations and our technical contribution can be applied for other object detection methods using HOG features and deformable models.

#### A. Analysis

#### B. Approach

#### C. Programming

### IV. EVALUATION

### V. CONCLUSION

In this paper, we have presented GPU implementations of HOG-based object detection and their performance evaluation. Unlike preceding work that highly stressed on performance improvements, our implementations are based on an analysis of performance bottlenecks posed due to an introduction of the deformable models in HOG-based object detection. This approach ensures that the GPU truly accelerates appropriate computational blocks. Our evaluation using a commodity GPU showed that our GPU implementation can speed up the existing HOG-based vehicle detection program tailored to the deformable models by 1.5x to 3x over traditional CPU implementations. Given that this performance improvement is obtained from the entire program runtime rather than particular

algorithm parts of the program, our contribution is useful and significant for real-world applications of vision-based object detection.

In future work, we plan to complement this work with systemized coordinations of computations and I/O devices. Since real-world applications require camera sensors to obtain input images while GPUs are compute devices off the host computer, the data I/O latency could become a bottleneck upon data buses. In this scenario, we need enhanced system support such as zero-copy approaches [9] to minimize the data latency raised between camera sensors and GPUs. We also plan to augment our GPU implementations using multiple GPUs in order to meet the real-time and real-fast requirement of real-world CPS applications.

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