## CPU

11	11	1 1	11	1 1	1 1	1 1	1 1
11	11	1 1	11	1 1	1 1	1 1	1 1
ш	ш	ш	ш	ш	ш	ш	ш

47 16	15 13	12	8 7 5	4 0
	' '			
imm	rs1	rd	dpcode_sub	opcode

*Figure 1.* 0000

47	23 22	18 17 13	12	8 7 5 4	0
reserved	rs2	rs1	rd	opcode_sub	opcode
			1	r . T I	. ''

*Figure 2.* 00000



OpCode	OpCodeSub	Inst	Kind	Description
00001	001	add	R	000
00001	xxx	xxx	X	XXXX

## 

## 00001

•  $add(001): 0000 rs1 0 rs2 000000000000 rd 00000 <math>\Rightarrow rd = rs1 + rs2$ 



yogo	setsumei
opcode	Operation Code DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD
opcode_sub	opcode DDDDDDDDDD
rd	Register Destination 000000000000000000000000000000000000
rs1, rs2	Register Source
imm	Immediate 000000000000000
reserved	
yogo	setsumei