

CPU

Instruction Format

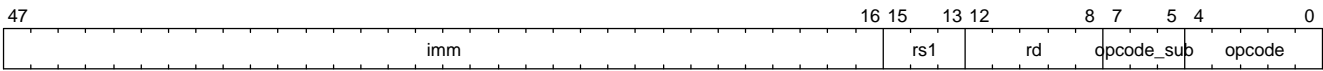


Figure 1. Instruction Format

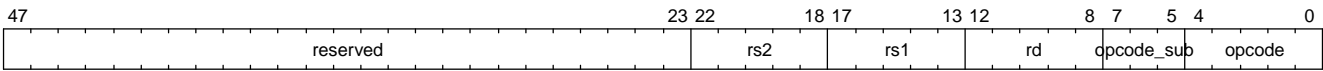


Figure 2. Instruction Format

OpCode

OpCode	OpCodeSub	Inst	Kind	Description
00001	001	add	R	add
	xxx	xxx	x	XXXX

OpCode

00001

Op

OpCode

Op

- add(001) : $rs1 + rs2 \rightarrow rd$

OpCode

yogo	setsume
opcode	Operation Code
opcode_sub	opcode
rd	Register Destination
rs1, rs2	Register Source
imm	Immediate
reserved	
yogo	setsume