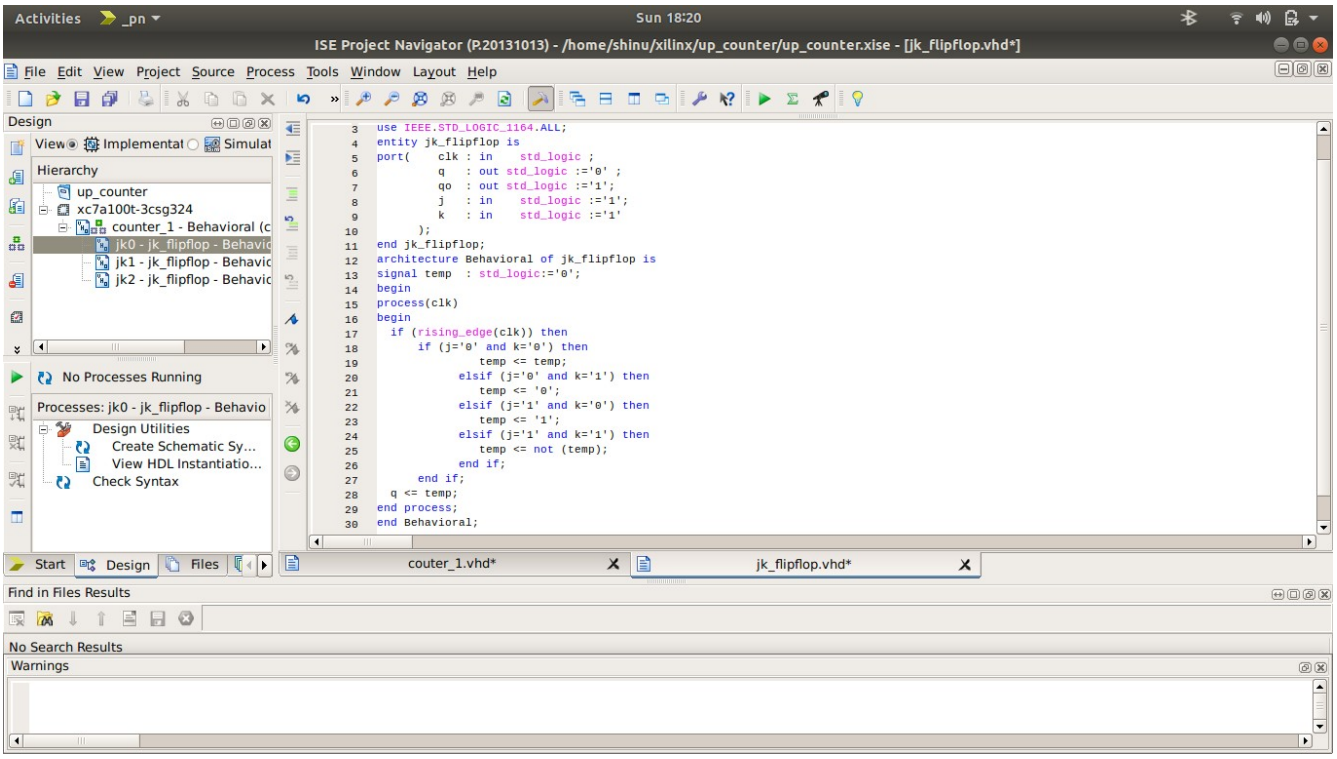
VHDL ASSIGNMENT

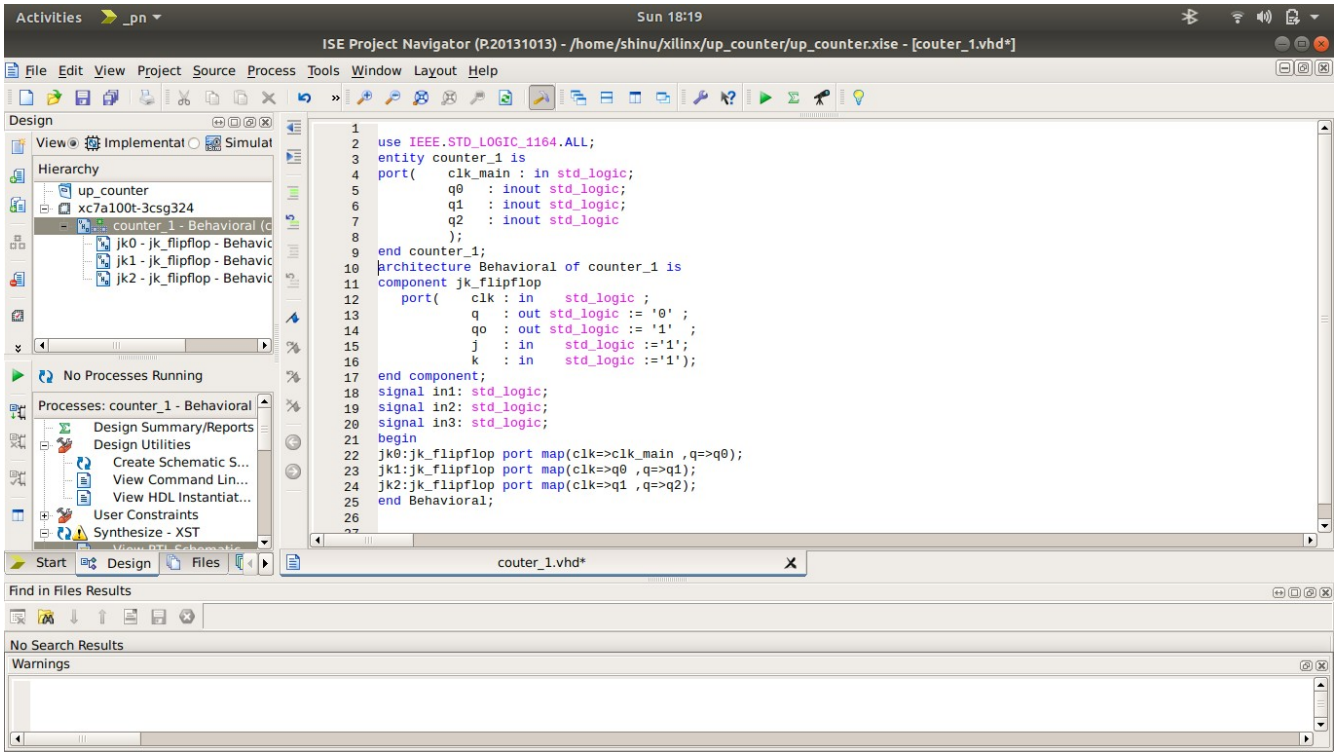
UP RIPPLE COUNTER USING J K FLIP FLOP

SHINU SHAJI C0761203

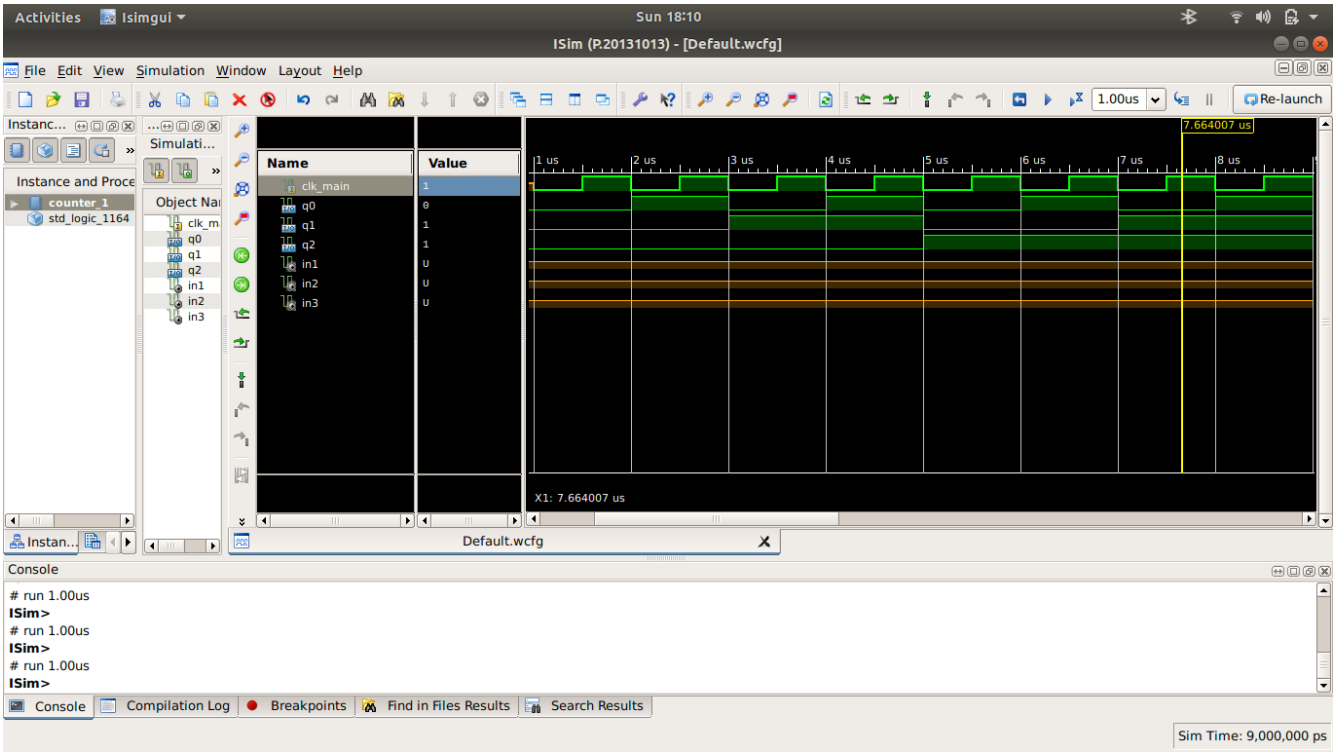
JK FLIP FLOP PROGRAMME



COUNTER PROGRAMME



PULSE WAVEFORM OUTPUT



SCHEMATICS OUTPUT