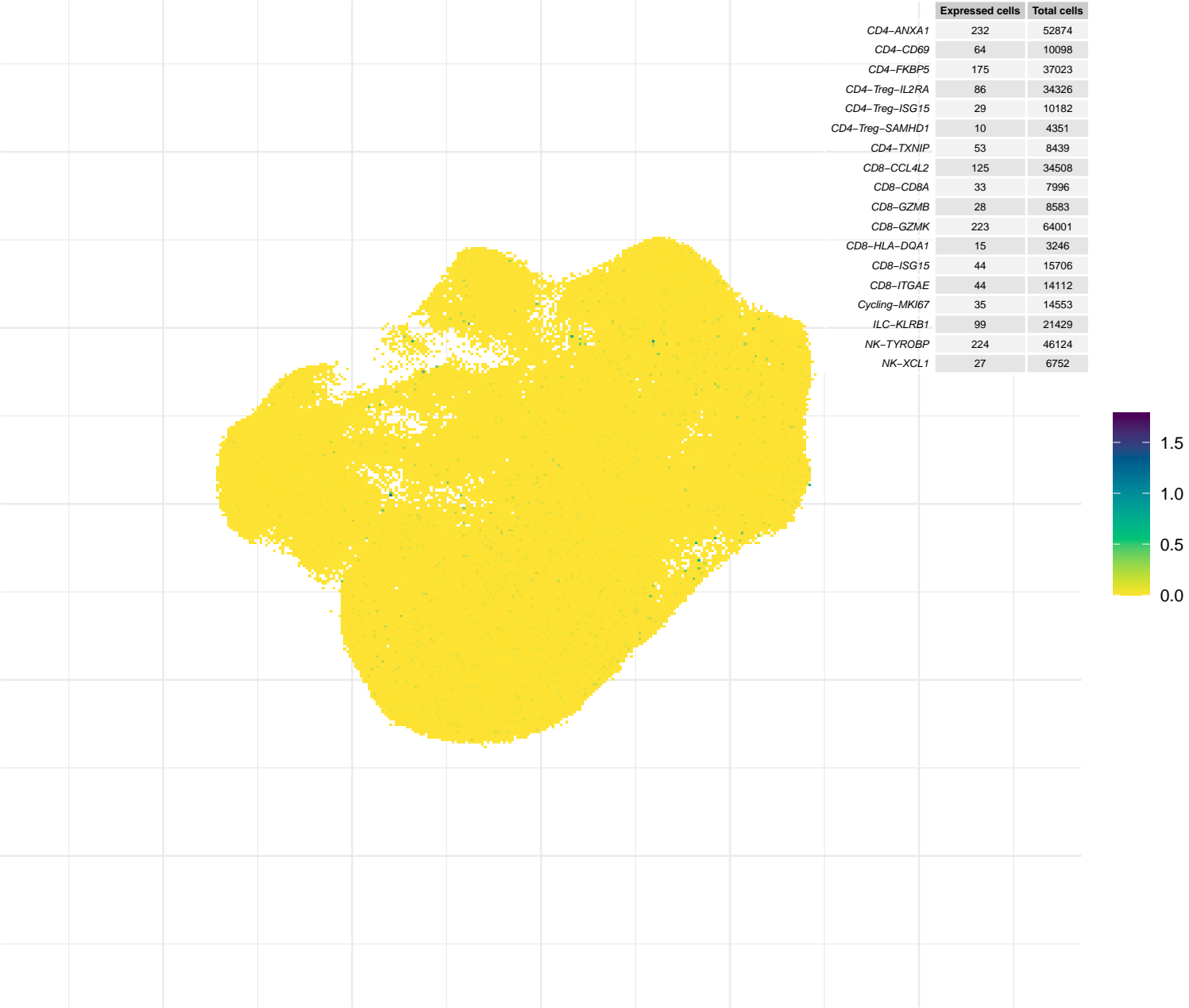


DDR2 in T_NK.cells



DDR2 in B.cells

