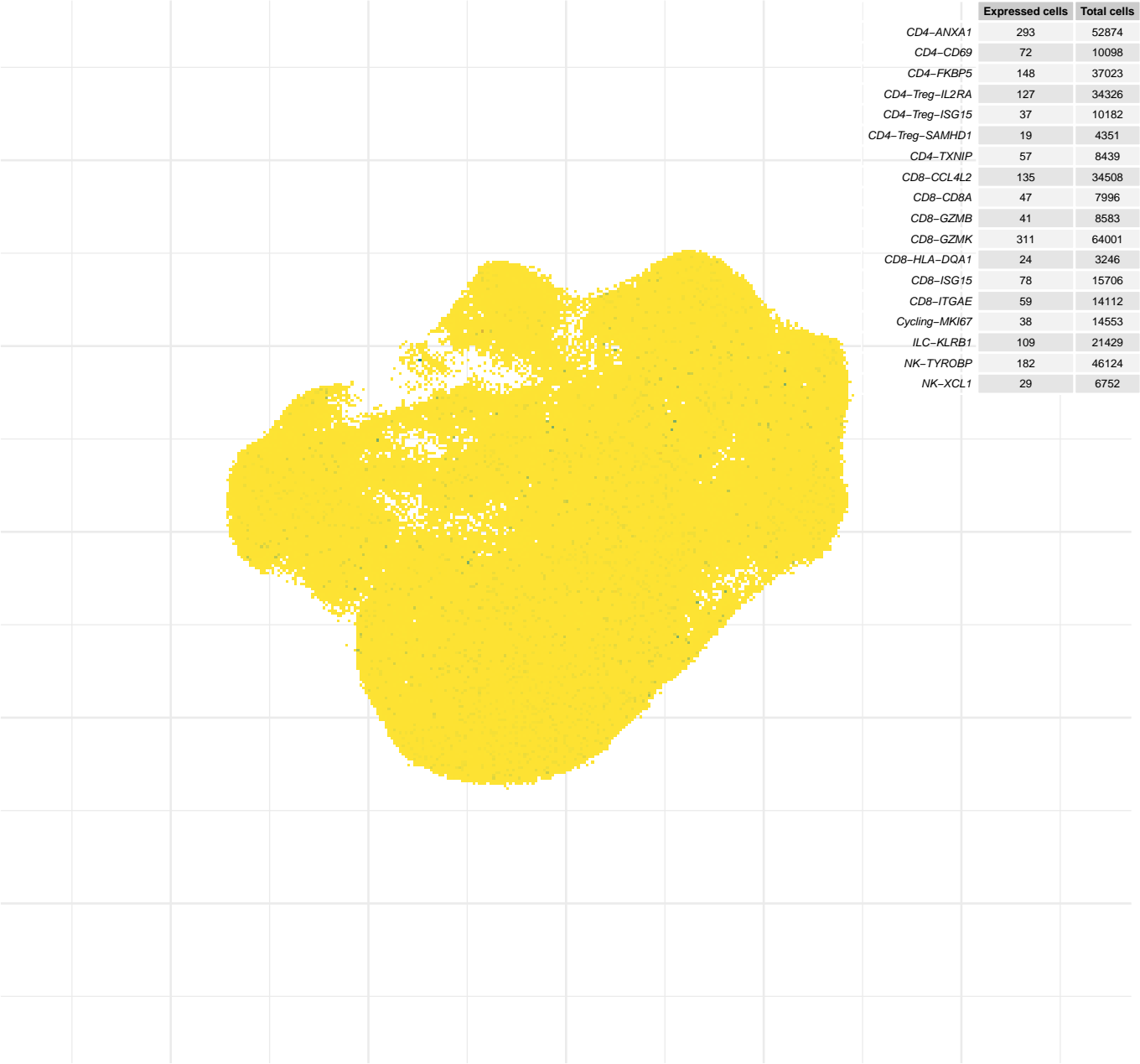


DDR1 in T_NK.cells



DDR1 in B.cells

