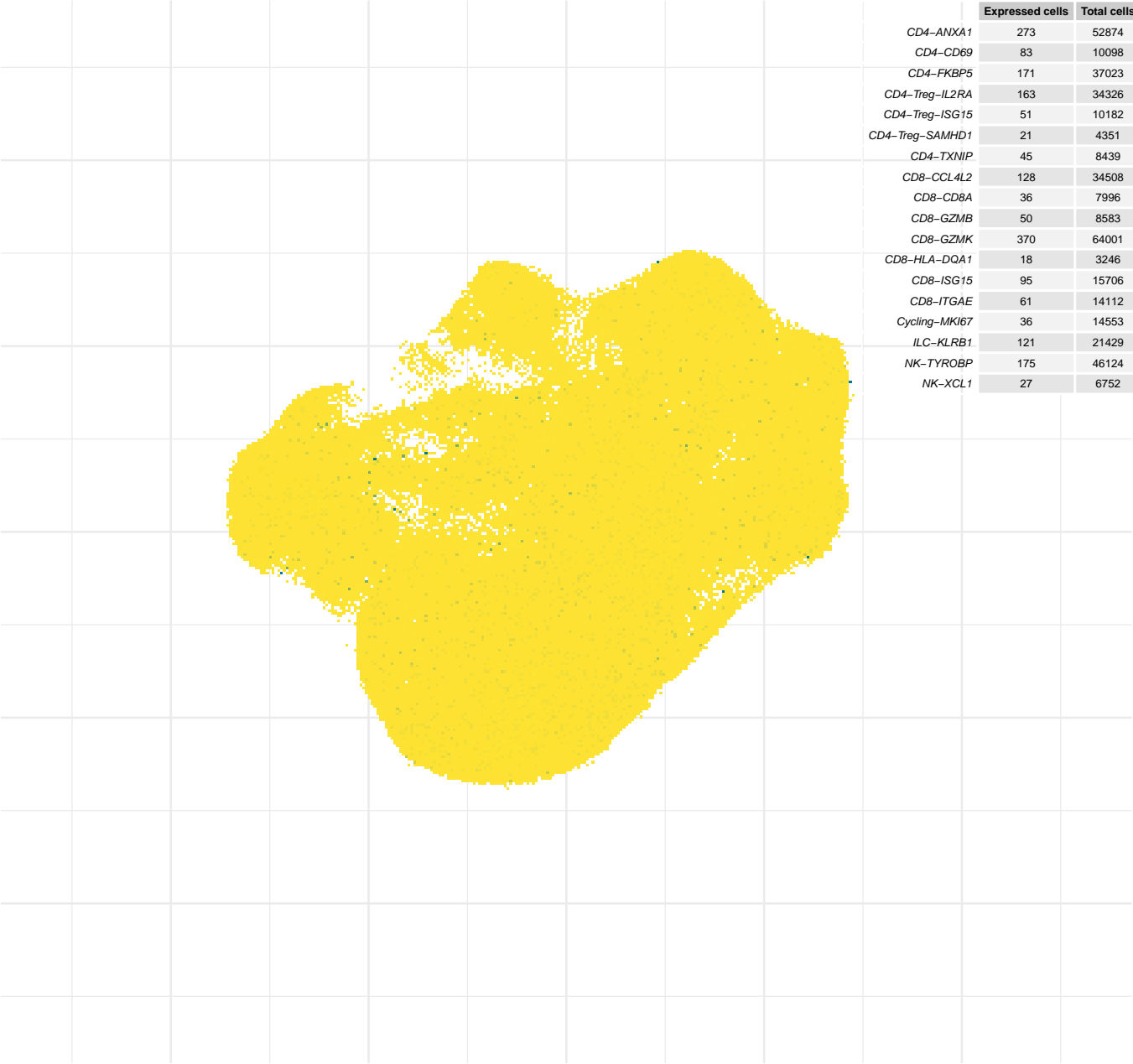


DDR1–DT in T_NK.cells



DDR1–DT in B.cells

